old DDS header footprint

R18 10k
R19 10k
DGND

PLL_LOCK

MASTER_RESET
CS
SCLK
SDIO
OSK
PROFILE0
PROFILE1
I/O_UPDATE
PROFILE2
DRHOLD
DRCTL
DROVER

SYNC_CLK
X6
DGND

SV1
2x13

DGND

10k R20
DGND

I/O_RESET
CS
SCLK
SDIO
OSK
SYNC_CLK
3P3
PROFILE0
PROFILE1
PROFILE2
R16 10k
DGND

I/O_UPDATE

EP
AGND

AD9910   U1

S-
SMA

RF4 DNP
R5
AGND
AGND

C3 1uF

GND$1   GND
RF_OUT   RF_IN
LFCN
RF2

AGND
AGND

DNP
R12
50R
AGND

3V3_ANALOG

AGND

76 AVDD(3.3V)   AVDD(3.3V) 75
77 AVDD(3.3V)   AGND 74
78 AGND   NC 73
79 AGND   I/O_RESET 72
80 IOUT   SCLK 71
81 IOUT   SDO 70
82 AGND   SDIO 69
83 AVDD(3.3V)   DGND 68
84 DAC_RSET   DVDD_I/O(3.3V) 67
85 AGND   DVDD(1.8V) 66
86 NC   DRHOLD 65
87 NC   DRCTL 64
88 AGND   DROVER 63
89 AVDD(1.8V)   OSK 62
90 REF_CLK   I/O_UPDATE 61
91 REF_CLK   SYNC_CLK 60
92 AVDD(1.8V)   DVDD(1.8V) 59
93 NC   DVDD_I/O(3.3V) 58
94 REFCLK_OUT   PROFILE0 57
95 XTAL_SEL   PROFILE1 56
96 AGND   PROFILE2 55
97 NC   DGND 54
98 NC
99 NC
100 NC

F0 50
F1 49
D0 48
DVDD(1.8V) 47 DUDD_1P8
DGND 46
DVDD_I/O(3.3V) 45 DUDD_3P3
D1 44
D2 43
D3 42
TXENABLE 41
PDCLK 40
D4 39
D5 38
D6 37
D7 36
D8 35
D9 34
D10 33
D11 32
D12 31
DVDD(1.8V) 30 DUDD_1P8
DGND 29
DVDD_I/O(3.3V) 28 DUDD_3P3
D13 27
D14 26

S+

R3 50R
DNP
AGND

C2 1uF

R6
AGND

GND$1   GND
RF_OUT   RF_IN
LFCN
RF3

AGND
AGND

DNP
R13
50R
AGND

10k R14

0.1uF
C6
AGND

R1
DNP
R2
0

+3U3

C1
0.1uF
AGND

RF1
VCC   TRI-STATE
OUTPUT
AFC/GND   GND
NCS1 NCS2 NCS3
7N-TCXO
40MHz TCXO
NCS5 NCS4

AGND

C4 6pF
R7 2k

R8 1k
AGND

R10 0

C5 0.1uF

R11 DNP

CLK
DNP
AGND

50R R9
AGND

Optional external reference clk

1 NC
2 PLL_LOOP_FILTER
3 AVDD(1.8V)
4 AGND
5 AGND
6 AVDD(1.8V)
7 SYNC_IN+
8 SYNC_IN-
9 SYNC_OUT+
10 SYNC_OUT-
11 DVDD_I/O(3.3V)
12 SYNC_SMP_ERR
13 DVDD(1.8V)
14 DGND
15 MASTER_RESET
16 DVDD_I/O(3.3V)
17 DGND
18 DVDD(1.8V)
19 EXT_PWR_DWN
20 PLL_LOCK
21 DVDD(1.8V)
22 DGND
23 RAM_SWP_OVR
24 DGND
25 D15

AGND
AUDD_P6
DUDD_3P3
DUDD_1P8
DUDD_3P3
DGND
DUDD_1P8
DUDD_3P3
DGND
DUDD_1P8

R15 2.37k
C8 15pF
C7 150pF
AUDD_P3

DUDD_3P3
DGND

SYNC_SMP_ERR
X1

SYNC_OUT_N
SYNC_OUT_P
SYNC_IN_N
SYNC_IN_P

X2
X3
X4
X5

DGND

Power & Filtering

JP1
+5V
C31 220uF
AGND

REG113
U3
ENABLE
VIN
VOUT
NR
GND$4
GND$3
GND$2
GND$1
+5V
C11 1uF
DGND
C13 10nF
DGND
DGND

>MIC5247
U4
VIN
GND
EN
VOUT
CBYP
DGND
C22 10nF
DGND

L5 BLM
DVDD_1P8
C26 10u
DGND
C34 1uF
C36 1uF
DGND
C42 0.1uF
C44 0.1uF
C46 0.1uF
C48 0.1uF
C50 0.1uF
C52 0.1uF
DGND

L6 BLM
DVDD_3P3
C27 10u
DGND
C35 1uF
C37 1uF
DGND
C43 0.1uF
C45 0.1uF
C47 0.1uF
C49 0.1uF
C51 0.1uF
C53 0.1uF
C54 0.1uF
DGND

REG113
U2
ENABLE
VIN
VOUT
NR
GND$4
GND$3
GND$2
GND$1
+5V
C9 1uF
AGND
C10 10nF
AGND
AGND
+3V3

L1 BLM
3V3_ANALOG
C12 10u
AGND
C14 1uF
C15 1uF
AGND
C16 0.1uF
C17 0.1uF
C18 0.1uF
AGND

>MIC5247
U5
VIN
GND
EN
VOUT
CBYP
+3V3
AGND
C19 10nF
AGND
AVDD_P3_NF
C23 10u
AGND
L2 BLM
C28 1uF
C31 1uF
AGND
AVDD_P3
C40 0.1uF
AGND

>MIC5247
U6
VIN
GND
EN
VOUT
CBYP
+3V3
AGND
C20 10nF
AGND
AVDD_P6_NF
C24 10u
AGND
L3 BLM
C29 1uF
C32 1uF
AGND
AVDD_P6
C38 0.1uF
AGND

>MIC5247
U7
VIN
GND
EN
VOUT
CBYP
+3V3
AGND
C21 10nF
AGND
AVDD_P89/92_NF
C25 10u
AGND
L4 BLM
C30 1uF
C33 1uF
AGND
AVDD_P89/92
C39 0.1uF
C41 0.1uF
AGND

L7 BLM
DGND
AGND