Sadayuki Yoshitomi. Ph.D

(BIO)

Sadayuki Yoshitomi. Ph.D (IEEE Member) has 30 year experience working with the research and development of process design kits for RF-C/BiCMOS and 3D-NAND flash memory products of TOSHIBA Corp and KIOXIA Corp Japan respectively. In 2022, he joined ASIC division of Megachips Corporation Japan.　He is currently involved in the system design for 6G one chip transmitter. Regarding the academia activities, he is a technical committee member of IEEE SSCS Open Source Ecosystem and IEEE ESSERC, BCICTS conferences.