

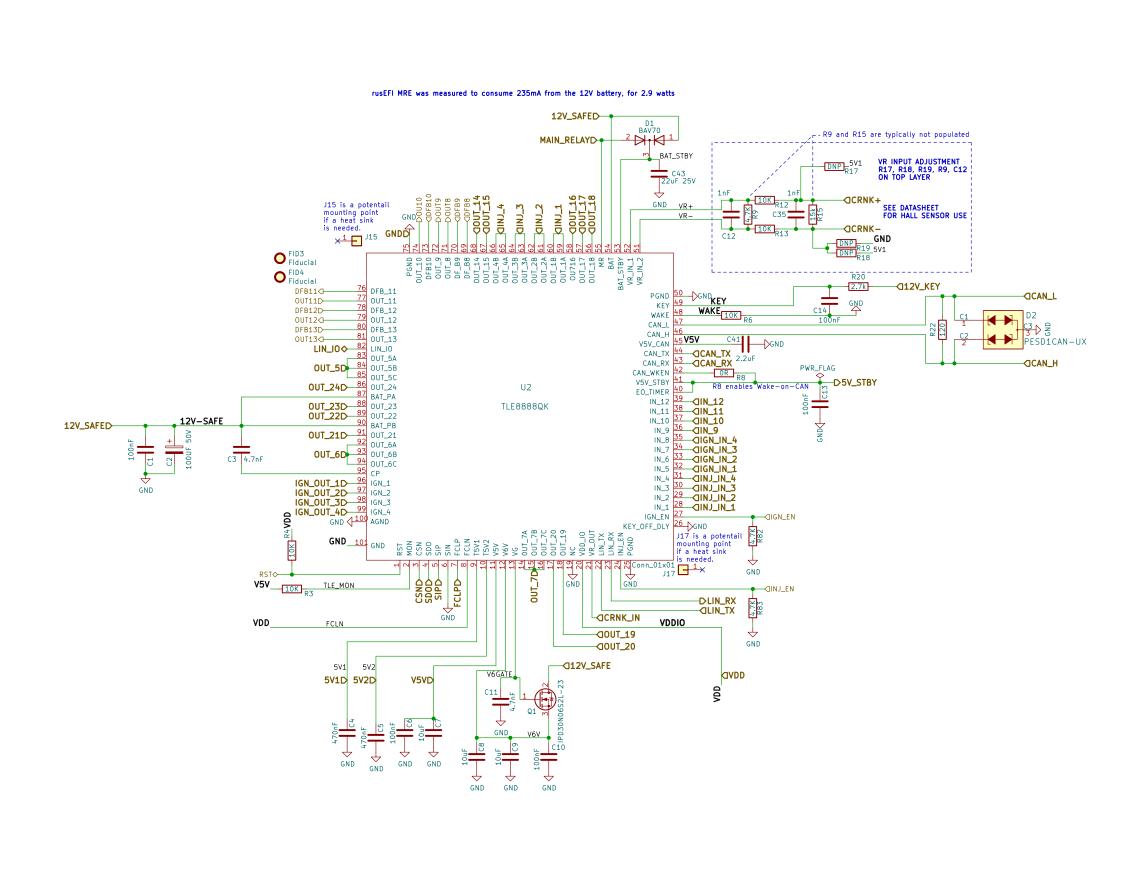
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Sheet: /MCU/ File: stm32.kicad_sch

Title: microRusEFI-2L

Date: 2022-04-10 Rev: R0.6.0 Size: B KiCad E.D.A. kicad (6.0.0) ld: 7/9

http://www.crystek.com/documents/appnotes/Pierce-GateIntroduction.pdf PCB per predictions with SaturnPCB has less then 3.5pF traces, STM32 pins assumed 5pF ESR = 80ohms max???
Rf = 2meg could be between 1meg and 10meg.
Cload should be 8pF per XTAL datasheet
Cload = ([Cin+C1][C2+Cout])/(Cin+C1+C2-Cout)+PCBstray
Cload = ([Cin+C1][4.7+5])/(5+4.7+4.7+5)+3.5= 8.35pF
C1=C2=C166=C167 = 4.7pF
Rs = 1/(2piFC2) = 1/(2*pi*8MHz*4.7pF) = 4.2ohms.



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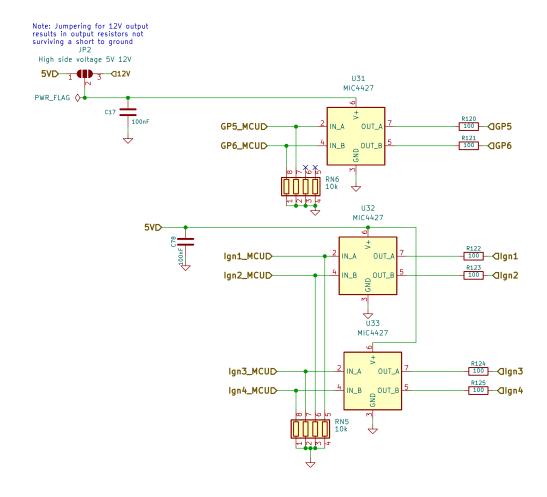
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Sheet: /TLE8888-1QK/
File: TLE8888-1QK.kicad_sch

Title: microRusEFI-2L
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6 channel high / low side driver



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Rev: R0.5.2 Id: 9/9