

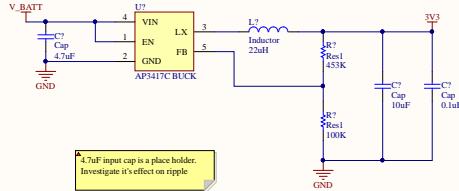
Voltage protection necessary? TVS diodes?

Double check pin 1 on header matches positive terminal of battery connector

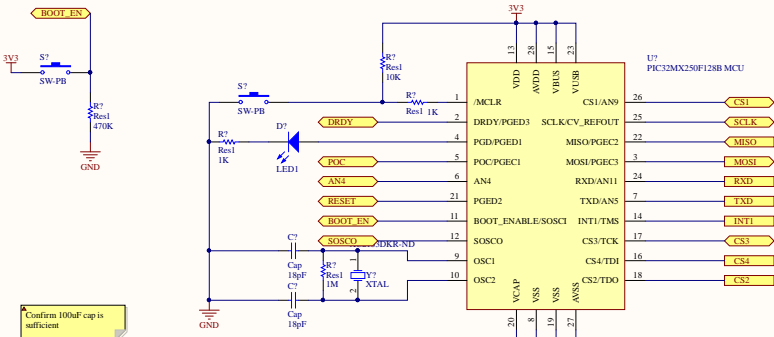
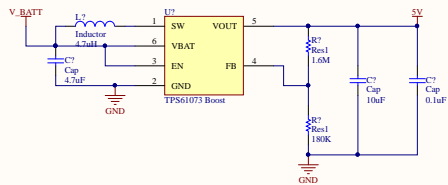
For later PCB design: add some sort of switch to turn on/off power so you dont need to unplug battery

Bias amp Note:

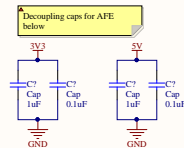
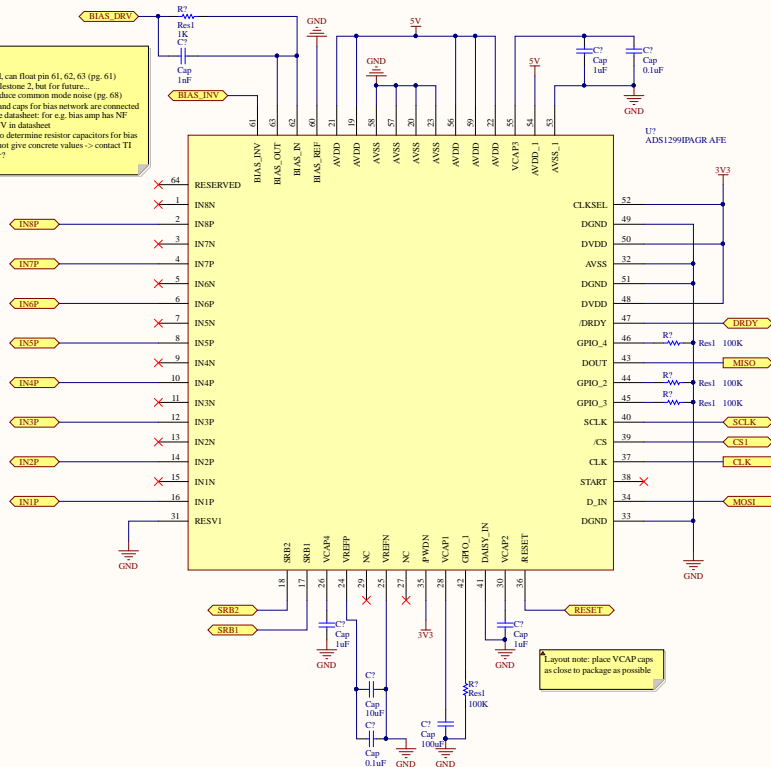
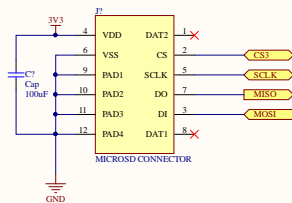
- if bias amp not used, can float pin 61, 62, 63 (pg. 61)
- Unnecessary for milstone 2, but for future...
- Bias amp used to reduce common mode noise (pg. 68)
- OpenDrCl resistors and caps for bias network are connected differently than in the database: for e.g. bias amp has NF network to BIAS_INV in datasheet
- Would need a way to determine resistor capacitors for bias amp, database does not give concrete values -> contact TI applications engineer?



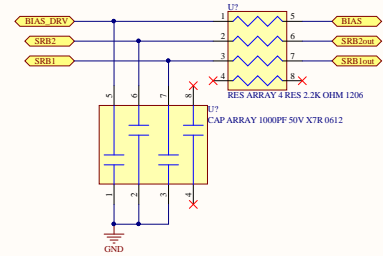
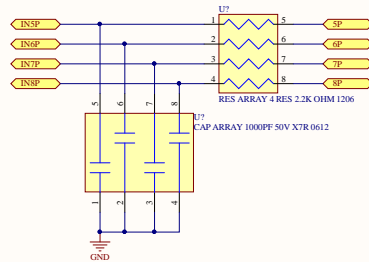
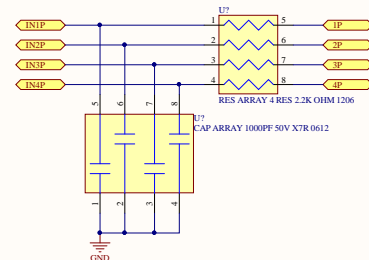
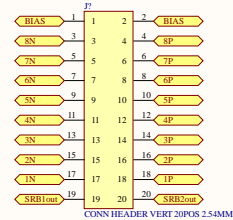
4.7uF input cap is a place holder. Investigate it's effect on ripple



Confirm 100nF cap is sufficient



Probably could use a header with less pins since N pins are NC'd



Electrode RC network note:

- Using referential montage setup as in DS (pg 67)
- Using initial RC values based on OpenDrCl values (may need to change these in the future)

Title		
Size	Number	Revision
A2		
Date:	2019-11-23	Sheet 6 of 6
File:	C:\Users\JEFG\Documents\EEG_schematic.SchDoc	Drawn By:

Board S