JOSE "SHINKIRO" SALINAS MEZA

Electrical & Computer Engineering | Hardware–Software Systems | AI
Arizona, USA | JOSESALINASMEZA@OUTLOOK.COM
LinkedIn: linkedin.com/in/JSM-Shinkiro | GitHub: github.com/JSM-Shinkiro | Webpage: jsm-shinkiro.github.io

SUMMARY

I am an electrical & Computer Engineering student with strong hands-on experience across **computer architecture, digital and analog hardware, embedded systems, and applied machine learning**. Proven ability to design and debug **end-to-end systems**, from a fully pipelined CPU and PCB-level analog circuits to data-driven ML models and large-scale network analysis. Seeking **Summer 2026 engineering internships** in hardware, software, systems, or AI-adjacent roles; open to relocation across the U.S. and remote opportunities**.**

EDUCATION

University of Arizona — B.S. Electrical & Computer Engineering
Minor Focus: Computer Science & Artificial Intelligence
Expected Graduation: 2026

Selected Coursework

Computer Architecture, Digital Logic Design, Analog & Mixed Signals Circuits Design, Embedded Systems, Data Structures & Algorithms, Machine Learning, Artificial Intelligence, Deep Learning, Computer Networks, Linear Algebra, Probability

TECHNICAL SKILLS

Programming & HDL: Python, C/C++, Java, Verilog, MATLAB, MIPS Assembly, HTML/CSS
Hardware & ECE: FPGA (Vivado), Digital Logic Design, Embedded Systems, SPICE Simulation, PCB Design (Eagle, KiCad)
AI & Data: PyTorch, scikit-learn, NumPy, pandas, Matplotlib
Tools & Platforms: Git, GitHub, Linux, VS Code
Languages: English (fluent), Spanish (native)

PROJECT EXPERIENCE

5-Stage Pipelined MIPS CPU — Verilog, Vivado
- Designed a fully pipelined MIPS-like CPU with IF/ID/EX/MEM/WB stages
- Implemented hazard detection and forwarding logic
- Verified correctness via simulation and waveform inspection

Audio Power Amplifier PCB — Analog Circuits
- Designed a multi-stage amplifier with an op-amp preamp and a push-pull output stage
- Simulated gain, stability, and distortion using SPICE
- Designed custom PCB layout

Embedded Systems: Dual-Axis Solar Tracking System
- Designed control logic for dual-axis positioning using sensor feedback
- Integrated hardware control with embedded software for real-time adjustment

Neural Networks vs Naive Bayes — PyTorch
- Implemented and compared neural networks against Naive Bayes baseline
- Tracked learning curves and overfitting behavior

Internet AS Topology Analysis — Python
- Built provider-to-customer graphs and computed customer cones
- Analyzed routing hierarchy and scalability

INTERESTS

Computer architecture, embedded and digital systems, software engineering, hardware acceleration, applied machine learning, semiconductor systems, and large-scale technical systems.