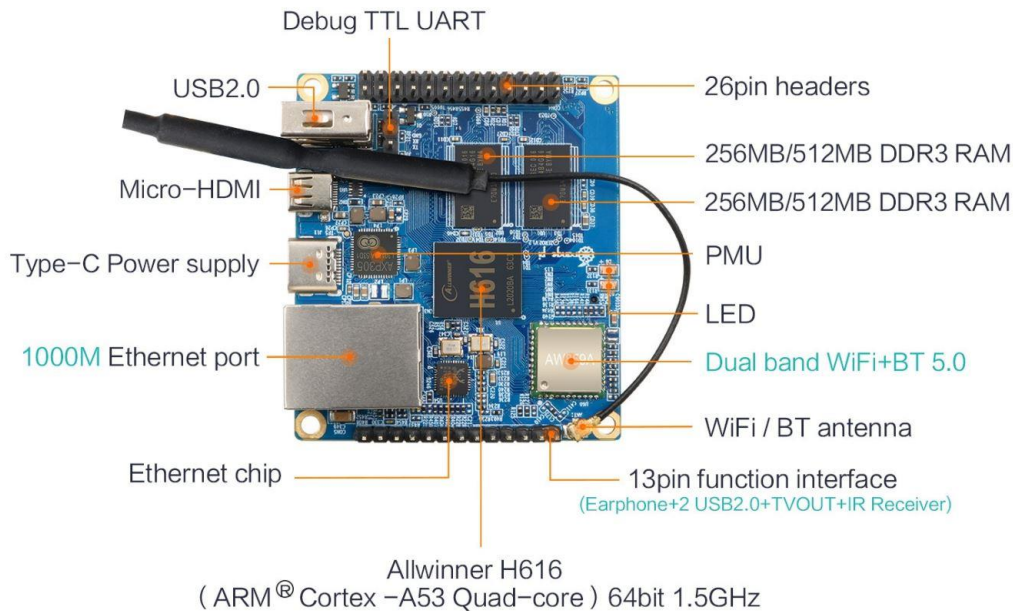


## MaxAir Technical – Using the Orange Pi Zero2 Board

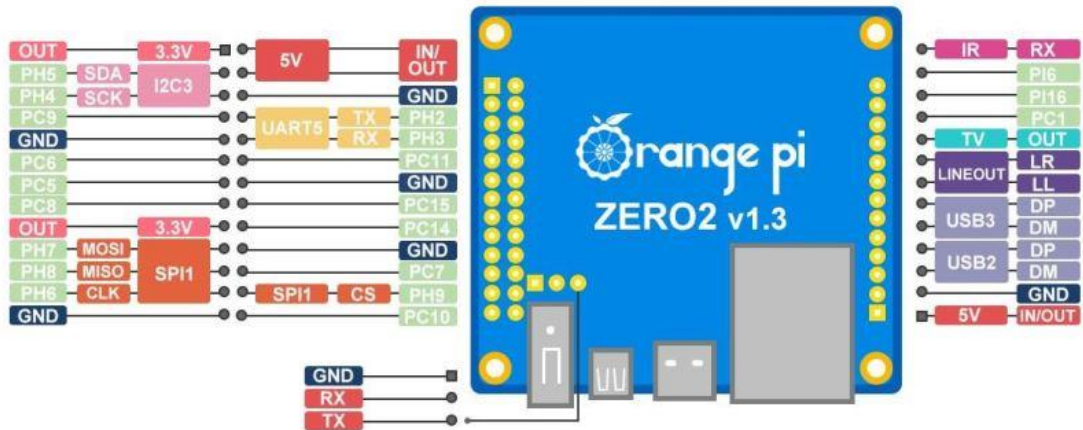
The Orange Pi Zero2 is based on the Allwinner H616 SoC (system on a chip), it can be purchased with either 512MB or 1GB of DDR3 Random Access Memory, costs around £25 for the 1GB version and has performance similar, if not better, to the Raspberry Pi 3.



## Hardware Specification

- Allwinner H616, Quad core Cortex-A53 64-bit SoC @ 1.5GHz
- 512MB or 1GB DDR3 SDRAM (depending on model)
- H.265 (4kp60 decode), H264 (4kp30 decode, 4kp25 encode or 1080p60 encode)
- ARM G31 GPU Supports OpenGL ES 3.2/2.0/1.0, Vulkan 1.1 OpenCL 2.0
- AW859A module Support IEEE 802.11 a/b/g/n/ac wireless, Bluetooth 5.0
- 1000M/100M/10M Ethernet
- Micro-HDMI ports (up to 4kp60 supported)
- 3 USB 2.0 ports. (Two of them are via 13pin interface board)
- 26 pin GPIO header with I2C, SPI, UART and multiple GPIO ports
- 13 pin GPIO header with 2 USB Host, IR pin, TV-out, Audio and GPIO ports
- 5V DC via USB-C connector (minimum 2A)
- OS: Android10, Ubuntu and Debian
- Dimension: 85mm×56mm
- Weight: 30g

# Pinout Diagram



At this point in time there is no working Armbian release, but MaxAir has been installed on the Orange Pi Zero 2 Debian Buster release.

The main difference between this board (and a number of other Orange Pi boards) is that the main Input/Output interface is presented on a 26pin header, which is rotated 180° as far as pin numbering is concerned when compared to the Raspberry Pi's 40pin I/O connector. Pins 1-26 of the Orange Pi Zero 2 board I/O connector correspond to pins 1-26 of the Raspberry Pi boards 40pin I/O connector. The result is that the Orange Pi Zero 2 has less available GPIO pins, when compared to the Raspberry Pi. Not taking in to account the UART5, I2C3 and SPI1 interface pins, the available GPIO pins are 7, 11, 12, 13, 15, 16, 18, 22 and 26 ie a total of nine. An additional four GPIO pins are available on the 13pin header, these are pins 10, 11, 12 and 13. The MaxAir numbering scheme used is that pins 1-26 identify pins on the 26pin header, while pins 27-39 pins on the 13pin header. The board is supported by Adafruit-Blinka and Adafruit-PlatformDetect and hence is compatible with MaxAir's use of GPIO pins.

UART operation is supported on the same physical pins as the Raspberry Pi, the UART used is identified as 'ttyS5', when used for MaxAir's Serial Gateway.

## Using the SPI Interface

The Orange Pi Debian operating system exposes two SPI ports, `spidev0.0` and `spidev1.1`, although only SPI1 is available on the board itself. A current limitation of Adafruit-Blinka is that it only supports SPI port 0, hence `spidev1.1` need to be mapped to `spidev1.0`. This can be achieved by creating a permanent symbolic using a 'udev rule' as follows: create and edit the file `/etc/udev/rules.d/99_spi.rules`, then add line **`KERNEL=="spidev1.1", SYMLINK+="spidev1.0"`** and save the file, finally reboot.

## 1 Wire Interface

The Orange Pi Zero 2 Debian Buster release does not support the 1-wire interface on pin7, this should be supported when the Armbian release becomes available.