gangliao / TIGER

implement a full compiler based on c++ 11

compiler parser scanner graph-coloring code-generator cplusplus-11 front-end backend Manage topics

317 commits 2 branches 1 release 2 contributors Apache-2.0

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cmake	add cmake compiler	16 days ago
deprecated	move testcase1 into deprecated	12 hours ago
design_doc	refine doc	8 hours ago
img	add demo	9 hours ago
src	Add more	9 hours ago
testCases2	Fix test5 cfg mode	14 hours ago
.gitignore	Add cmake	16 days ago
.pre-commit-config.yaml	Add clang-format	2 months ago
.travis.yml	load and store vars during blocks	2 days ago
CMakeLists.txt	Add cmake	16 days ago
LICENSE	Initial commit	3 months ago
Phase1_Testing_and_Output.pdf	refine doc	8 hours ago
Phase2_Testing_and_Output.pdf	fix syntax words	8 hours ago
README.md	Update README.md	18 seconds ago
report.pdf	update demo note	8 hours ago

README.md

TIGER - A Tiny Full Compiler

build passing

This tiny compiler includes both front end and back end.

> Front end: Grammar Rules, LL(1) Parse Table, Syntax and Semantic Check and Intermediate Code.

> Back end: IR Optimization (Intra-block CFG optimization), MIPS Register Allocation, Instruction Selection and Code Generation.

You can access https://github.com/gangliao/TIGER to view the documentation!

How to Build

0. development environment

   Currently, this project repository is maintained on github publicly and also been deployed on Travis CI. It supports both Ubuntu and Mac OS X.

1. build:

```
# cd project dir
cd Tiger-Compiler
# build scanner, parser, generator
mkdir build && cd build
# cmake building tool
cmake ..
make -j4
```

2. run:

   You can parsing test cases named `*.tiger` under `/testCases2` to generate IR code. Default it will utilize CFG optimized technique to generate MIPS asm code.

```
# verbose mode: "-d to implement a verbose mode"
./src/parser <filename> -d
```

   If you want to use the naive mode to generate asm code, simply issue:

```
./src/parser <filename> -d -naive
```

3. test:

   In testCases2 directory, it includes a test script `test.sh` to execute all test cases and generate the corresponding asm files `*.naive.s` and `*.cfg.s`.

   After souce code is compiled, you can simply issue the commands:

```
cd testCases2
sh ./test.sh
```

Demo

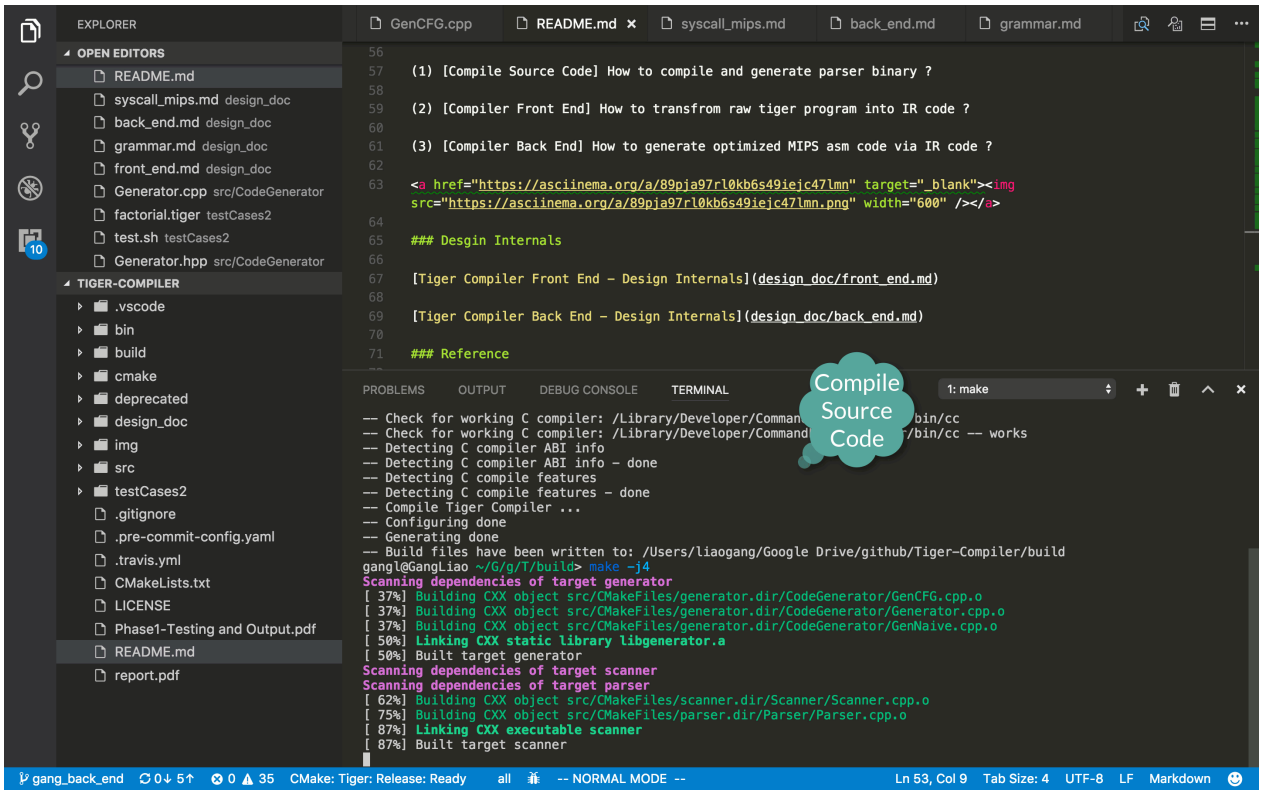
NOTE: This demo is `gif` graph format. If you have problem to view it in the markdown file, you can directly open it which located at `img/demo.gif`.

This Demo shows that

```
(1) [Compile Source Code] How to compile and generate parser binary ?

(2) [Compiler Front End] How to transfrom raw tiger program into IR code ?

(3) [Compiler Back End] How to generate optimized MIPS asm code via IR code ?
```



Desgin Internals

Tiger Compiler Front End - Design Internals

Tiger Compiler Back End - Design Internals

Test Cases

We passed all tests cases which provided by TA.

Please check out the details in report Phase2_Testing_and_Output.pdf from current directory, which includes test cases and their quality comparisons for naive and CFG intra-block register allocation.

Accomplishment

- [x] Register allocation code
  - [x] Naive
  - [x] CFG and intra block allocation
  - [ ] EBB and intra-EBB allocation
  - [x] Whole function register allocation
  - [x] Live Range Analysis and Graph Coloring
- [x] Instruction selection and generation code
- [x] Passes tests using generated code executing on simulator.
- [x] Report (desgin Internals, how to build, run, code quality comparisions, etc.)