


This repository


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Apache-2.0

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gangliao committed on GitHub Merge pull request #106 from gangliao/gang_back_end

Latest commit ab6effc 4 minutes ago

cmake	add cmake compiler	16 days ago
deprecated	move testcase1 into deprecated	4 hours ago
design_doc	refine doc	6 minutes ago
img	add demo	2 hours ago
src	Add more	an hour ago
testCases2	Fix test5 cfg mode	6 hours ago
.gitignore	Add cmake	16 days ago
.pre-commit-config.yaml	Add clang-format	2 months ago
.travis.yml	load and store vars during blocks	2 days ago
CMakeLists.txt	Add cmake	16 days ago
LICENSE	Initial commit	3 months ago
Phase1_Testing_and_Output.pdf	refine doc	6 minutes ago
Phase2_Testing_and_Output.pdf	fix syntax words	21 minutes ago
README.md	refine doc	6 minutes ago
report.pdf	Update report	a month ago

README.md

TIGER - A Tiny Full Compiler

build

error

This compiler includes both front end and back end.

Front end: Grammar Rules, LL(1) Parse Table, Syntax and Semantic Check and Intermediate Code.

Back end: IR Optimization (Intra-block CFG optimization), MIPS Register Allocation, Instruction Selection and Code Generation.

How to Build

0. development environment

Currently, this project repository is maintained on github privately and also been deployed on Travis CI. It supports both Ubuntu and Mac OS X.

1. build:

cd project dir
cd Tiger-Compiler

```
# build scanner, parser, generator
mkdir build && cd build
# cmake building tool
cmake ..
make -j4
```

2. run:

You can parsing test cases named `*.tiger` under `/testCases2` to generate IR code. Default it will utilize CFG optimized technique to generate MIPS asm code.

```
# verbose mode: "-d to implement a verbose mode"
./src/parser <filename> -d
```

If you want to use the naive mode to generate asm code, simply issue:

```
./src/parser <filename> -d -naive
```

3. test:

In `testCases2` directory, it includes a test script `test.sh` to execute all test cases and generate the corresponding asm files `*.naive.s` and `*.cfg.s`.

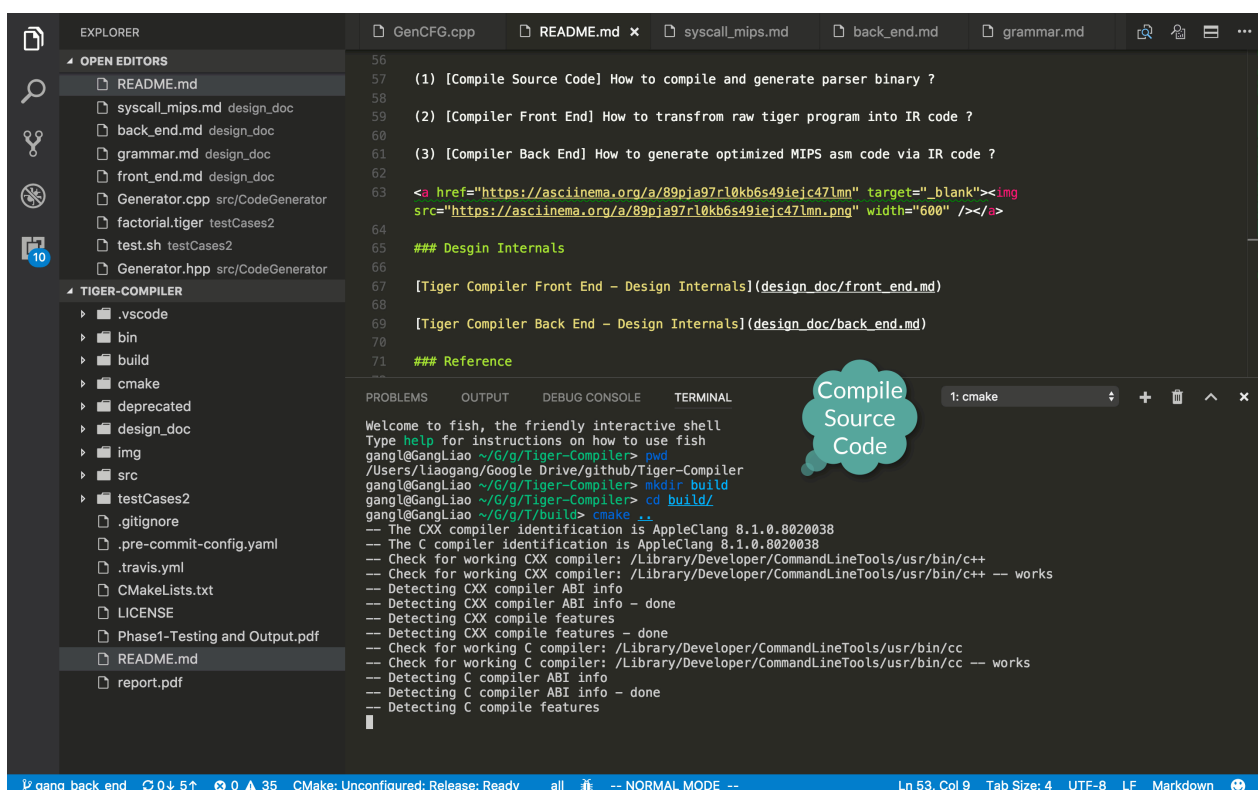
After source code is compiled, you can simply issue the commands:

```
cd testCases2
sh ./test.sh
```

Demo

This Demo shows that

- (1) [Compile Source Code] How to compile and generate parser binary ?
- (2) [Compiler Front End] How to transform raw tiger program into IR code ?
- (3) [Compiler Back End] How to generate optimized MIPS asm code via IR code ?



Design Internals

[Tiger Compiler Front End - Design Internals](#)

[Tiger Compiler Back End - Design Internals](#)

Test Cases

We passed all tests cases which provided by TA.

Please check out the details in report [Phase2_Testing_and_Output.pdf](#) from current directory, which includes test cases and their quality comparisons for naive and CFG intra-block register allocation.

Accomplishment

- ☒ Register allocation code
 - ☒ Naive
 - ☒ CFG and intra block allocation
 - ☐ EBB and intra-EBB allocation
 - ☒ Whole function register allocation
 - ☒ Live Range Analysis and Graph Coloring
- ☒ Instruction selection and generation code
- ☒ Passes tests using generated code executing on simulator.
- ☒ Report (design Internals, how to build, run, code quality comparisons, etc.)

