


$$R_2 = 2.0 \text{ k}\Omega$$

$R_2 = 6.0 \text{ k}\Omega$
expected max current of $PDA \rightarrow PDU = 1.7 \text{ mA}$

max current in pms
Pxx is 2.5 ma

P_{xx} is 2.5 m

$p \times B$ is at the zero scale upper value
 and full scale upper value

$P_X A$ is of the form

P_{XW} is wires - 2 wires
from connected to $P \times B$ for wires
value 0×00 , to $P \times A$ for wires
value 0×100 for B but devices.

SPI-DEMO
WIRING DIAGRAM
C. SPINDLER
1-13-2020

WORKED WELL WITH

SECRET

spi-demoino

worked on

1-12-2020

11:00 PM

DWG. NO CFS-1-13-2020-A1

Rev 1 - 1-21-2020

added alternate pins for SPI bus;
change wire colors; changed to NANO