


$$R_1 = 5.1 \text{ k}\Omega$$

$$R_2 = 2.0 \text{ k}\Omega$$

expected max current of $P\Phi A + P\Phi W = 1.7 \text{ ma}$

max current in pins
 P_{xx} is 2.5 ma 

P_{xB} is at the zero scale wiper value

P_{xA} is at the full scale wiper value

P_{xW} is wiper - it varies
 from connected to P_{xB} for wiper
 value 0x00, to P_{xA} for wiper
 value 0x100 for 8 bit devices.

WORKED WELL WITH
 SKETCH spi-demo.ino
 dated 9-19-2019
 worked on
 1-13-2020
 at 11:00 PM

SPI - DEMO

WIRING DIAGRAM

C. SPINDLER

1-13-2020

DWG. NO CFS-1-13-2020-A