

R=5.1 KD R2=2.0 KD

expected moxament of PAA : PAW = 1.7 ma

most current in pins Pxx is 2.5 ma [] PXB is at the zero scale wiper value PXA is at the full seall wiper value Pxw so wiper- it vario from connected to PxB for wyper value 0 x 00, to 8 x A for wife value 0x100 for 8 bit devices.

Nano pintrobichare alternate Te I CSP are SCK (D13), MISO(D12) & MOSI (D11), all varified to work on breadboard NANO on 1-21-2020

WORKED WELL WITH SKETCH spi-demoi ino worked on 1-13-2020 at 11:00 PM

SPI - DEMO WIRING DIAGRAM C. SPINDLER 1-13-2020

Rev 1 - 1-21-2020 added atternate pins, for SPI bus; change wire colors; changed to NANO DWG. NO CFS-1-13-2020- A1