

## Jinwoo Choi

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### CONTACT INFORMATION

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🏠 Homepage: <https://jwchoi1996.github.io> in [LinkedIn](#)

### RESEARCH INTERESTS

**System & Architecture Optimization for Neural Networks /  
Mobile & Embedded System**  
Scheduling, Neural Processing Unit (NPU), Performance Modeling

### EDUCATION

**Yonsei University**, Seoul, Korea,  
Ph.D. Candidate, Computer Science **Mar. 2020 - Present**  
• Advisor: Prof. Youngsok Kim  
B.S., Computer Science **Mar. 2015 - Feb. 2020**

### RESEARCH EXPERIENCE

**Graduate Researcher** **Mar. 2020 - Present**  
Advisor: Prof. Youngsok Kim (High Performance Computing Platforms Lab, Yonsei University)  
  
**Undergraduate Researcher** **Oct. 2019 - Feb. 2020**  
Advisor: Prof. Youngsok Kim (High Performance Computing Platforms Lab, Yonsei University)  
  
**Undergraduate Researcher** **Jun. 2018 - Oct. 2019**  
Advisor: Prof. Hojung Cha (Mobile Embedded Systems Lab, Yonsei University)

### PUBLICATIONS

Suhyun Lee, Chaemin Lim, **Jinwoo Choi**, Heelim Choi, Chan Lee, Yongjun Park, Kwanghyun Park, Hanjun Kim, and Youngsok Kim, “SPID-Join: A Skew-resistant Processing-in-DIMM Join Algorithm Exploiting the Bank- and Rank-level Parallelisms of DIMMs”, In *Proc. 2025 ACM International Conference on Management of Data (SIGMOD)*, June 2025.

**Jinwoo Choi**<sup>1</sup>, Yeonan Ha<sup>1</sup>, Hanna Cha, Seil Lee, Sungchul Lee, Jounghoo Lee, Shin-haeng Kang, Bongjun Kim, Hanwoong Jung, Hanjun Kim and Youngsok Kim, “MPC-Wrapper: Fully Harnessing the Potential of Samsung Aquabolt-XL HBM2-PIM on FPGAs”, In *Proc. 32nd IEEE International Symposium On Field-Programmable Custom Computing Machines (FCCM)*, May. 2024.

<sup>1</sup>Co-first authors

**Jinwoo Choi**, Yeonan Ha, Jounghoo Lee, Sangsu Lee, Jinho Lee, Hanhwi Jang, and Youngsok Kim, “Enabling Fine-Grained Spatial Multitasking on Systolic-Array NPUs Using Dataflow Mirroring”, *IEEE Transactions on Computers (TC)*, Aug. 2023

Chaemin Lim, Suhyun Lee, **Jinwoo Choi**, Jounghoo Lee, Seongyeon Park, Hanjun Kim, Jinho Lee, and Youngsok Kim, “Design and Analysis of a Processing-in-DIMM Join Algorithm: A Case Study with UPMEM DIMMs”, In *Proc. 2023 ACM International Conference on Management of Data (SIGMOD)*, June 2023.

**Jinwoo Choi**<sup>1</sup>, Jaeyeon Kim<sup>1</sup>, Chaemin Lim<sup>1</sup>, Suhyun Lee, Jinho Lee, Dokyung Song, and Youngsok Kim, “GuardiaNN: Fast and Secure On-Device Inference in TrustZone Using Embedded SRAM and Cryptographic Hardware”, In *Proc. 23rd ACM/IFIP International Middleware Conference (Middleware)*, Nov. 2022.

<sup>1</sup>Co-first authors

Jounghoo Lee<sup>1</sup>, **Jinwoo Choi**<sup>1</sup>, Jaeyeon Kim, Jinho Lee, and Youngsok Kim, “Dataflow Mirroring: Architectural Support for Highly Efficient Fine-Grained Spatial Multitasking on Systolic-Array NPUs”, In *58th ACM/IEEE Design Automation Conference (DAC)*, Dec. 2021.

<sup>1</sup>Co-first authors

PATENTS	<b>[Registration: JP 2023129058]</b> Youngsok Kim, <b>Jinwoo Choi</b> , Chaemin Lim, Suhyun Lee, Dokyung Song, Jinho Lee, "Artificial Intelligence Device Based on Trust Environment", <i>Japan Patent</i>	
PROJECTS	<b>Searching Quality-Aware NN Scheduler</b> <ul style="list-style-type: none"> <li>• LG Electronics</li> <li>• Project Leader</li> </ul>	<b>Mar. 2024 - Present</b>
	<b>Designing a PIM-Based NPU Architecture and FPGA Prototyping</b> <ul style="list-style-type: none"> <li>• Samsung Advanced Institute of Technology (SAIT)</li> <li>• Project Leader</li> </ul>	<b>Mar. 2023 - Feb. 2024</b>
	<b>Sparsity-Aware Spatial Multitasking NPU Architecture and Scheduling</b> <ul style="list-style-type: none"> <li>• National Research Foundation of Korea (NRF)</li> <li>• Project Leader</li> </ul>	<b>Sep. 2023 - Aug. 2024</b>
	<b>Developing Software Platform for Programming of PIM</b> <ul style="list-style-type: none"> <li>• Institute for Information &amp; communications Technology Promotion (IITP)</li> <li>• Project Leader</li> </ul>	<b>Jun. 2021 - Dec. 2023</b>
	<b>Smart, Attack-resistant IoT Networks</b> <ul style="list-style-type: none"> <li>• Korea Institute for Advancement of Technology (KIAT)</li> <li>• Research Assistant</li> </ul>	<b>Jun. 2021 - Dec. 2023</b>
	<b>Spatial Multitasking on Systolic Array Neural Processing Units</b> <ul style="list-style-type: none"> <li>• Samsung Advanced Institute of Technology (SAIT)</li> <li>• Project Leader</li> </ul>	<b>Mar. 2020 - Feb. 2021</b>
AWARDS AND HONORS	<b>Best Paper - Yonsei University Innovation Paper Award</b> <ul style="list-style-type: none"> <li>• Yonsei University Graduate School</li> </ul>	<b>Jul. 2023</b>
	<b>Fellowship from NRF of Korea</b> <ul style="list-style-type: none"> <li>• National Research Foundation (NRF) of Korea</li> <li>• 1-year tuition</li> </ul>	<b>May 2022</b>
SKILLS	<b>Programming Languages</b> <ul style="list-style-type: none"> <li>• C, C++, Python</li> </ul> <b>Tools</b> <ul style="list-style-type: none"> <li>• Deep Learning Frameworks (TensorFlow, TensorFlow Lite, MACE)</li> <li>• Simulators (SCALE-SIM, DRAMSim3, OpenRoad Flow, Accelergy, CACTI-7)</li> </ul> <b>Languages</b> <ul style="list-style-type: none"> <li>• Korean(native), English</li> </ul>	