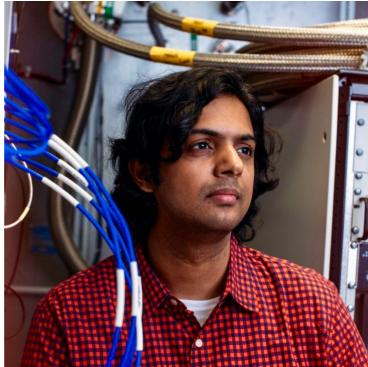# PennState Physics Colloquium

Quantum computation with noisy superconducting qubits

Abhinav Kandala [1]

[1]IBM T.J. Watson Research Center

## Speaker Biographic Summary

Abhinav Kandala is an experimental physicist in the quantum computing group at the IBM T.J. Watson Research Center. He received his Bachelors degree in Engineering Physics from the Indian institute of Technology (Bombay), and a PhD in Physics from The Pennsylvania State University, under the supervision of Prof. Nitin Samarth. Dr. Kandala went on to join the quantum computing group at IBM Research for his post-doctoral work and studied the applicability of a noisy super-conducting quantum processor to simulate small molecules and quantum magnets. This work was recognized by MIT Technology Review as one of 10 Breakthrough Technologies 2018, and by C&E News' Research of the Year 2017. [1]

Research Interests

Dr. Kandala's research interests focus on the coherence and control of superconducting qubits, multi-qubit device characterization, and applications of near-term quantum computers. His prior research interests spanned exploratory semiconductors, spintronics, nano-science and solar cells, providing extensive experience with nano-fabrication, cryogenic magneto-transport, and materials characterization techniques. His most recent work has focused on error mitigation for computations performed on noisy quantum processors [1]
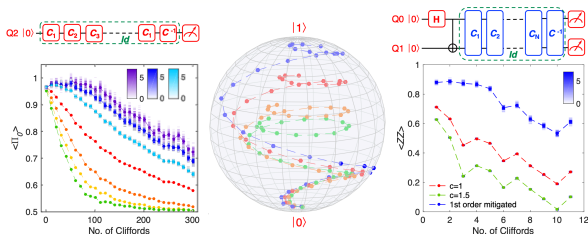

Error mitigation of random 1-qubit and 2-qubit circuits. [2]

Talk Abstract

Improvements in the control and coherence of artificial atoms built from superconducting circuits have enabled the development of noisy processors with over 50 qubits, and the exploration of problems addressable by these devices that are intractable to classical computation. In this talk, I shall present a brief summary of superconducting qubit technology and highlight some of the dominant errors and outstanding challenges. I shall then discuss results from small-scale demonstrations of algorithms for quantum simulation. These experiments highlight the detrimental effect of decoherence and measurement errors on computations with noisy quantum processors. While this can be remedied, in theory, with quantum error correction, the resources required for its physical realization are prohibitively large for the near term. In this context, I shall introduce "error mitigation" techniques that enable access to noise-free estimates of expectation values for short depth quantum circuits, without requiring any additional hardware resources.

Brief Background

A bit about qubits [3]

$$|\psi\rangle = cos\frac{\theta}{2}|0\rangle + e^{i\varphi}sin\frac{\theta}{2}|1\rangle$$
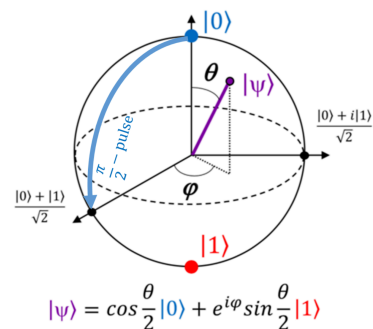
Error mitigation basics [2]

References

[1] "A. Kandala," researcher.watson.ibm.com/researcher/view.php?person=us-akandala.

[2] A. Kandala, K. Temme, A. D. Corcoles, A. Mezzacapo, J. M. Chow, and J. M. Gambetta, Nature **567**, 491 (2019), arXiv:1805.04492 .

[3] F. Jazaeri, A. Beckers, A. Tajalli, and J.-M. Sallese, arXiv:1908.02656 [physics, physics:quant-ph] (2019), arXiv:1908.02656 [physics, physics:quant-ph] .

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