$ams \\ math \ graphic \\ x$ 

Lab 2: Combinational Circuits CSC258H1 – Computer Organization

Your Name

document

\*Part I

The 4-to-1 multiplexers I designed using two-level logic, hierarchical design with logic gates, and hierarchical design with CMOS gates are shown in the figure below.

figure[ht!] A schematic for TwoLevelMux41