

Enabling photodetection electronics for fluorescent diamond
based quantum sensing
Project plan

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Chapter 1

Introduction

This chapter introduces the assignment and some foundational concepts of quantum sensing.

1.1 Background

Nitrogen-vacancy (NV) centers [1] are imperfections in the atomic structure of diamonds. The two types of NV centers are NV0 and NV-, as seen in Figure 1.1, but the NV- structure is much more commonly used in quantum applications. These imperfections have the useful property of spin-dependent luminescence. This means that the spin of the NV center affects the frequency of the light emitted by the structure¹. Using this quality of the NV structure, different environmental metrics (e.g magnetic fields) can be measured.

The Applied Nanotechnology research group is working on a NV-center-based sensor setup. There exist several quantum protocols, but the one this setup needs to support is called Continuous-Wave Optically Detected Magnetic Resonance (CW ODMR). At its core, ODMR is a set of protocols, which can detect magnetic fields based on the fluctuations in the fluorescence of NV centers [3]. CW ODMR in particular involves exposing the NV center to a MicroWave (MW) sweep while illuminating it with a constant light source. This is in contrast with pulsed ODMR techniques, which use different TTL (transistor-transistor logic) pulse schemes [4] to modulate the MW signal and the light source.

Processing data from the setup requires working with weak signals that are hard to distinguish from the environmental noise. While this is a significant problem, it is also a very common one. Because of this, there is already widely-used system used to isolate signals in such cases: the lock-in amplifier.

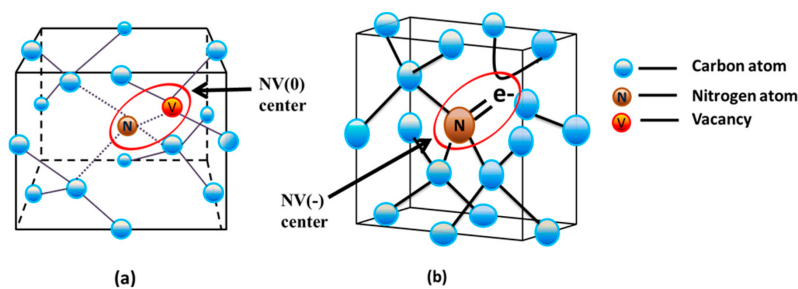


Figure 1.1: NV0 (a) and NV- (b) structures in diamonds (image credit to Haque et al [5])

1.2 Purpose of the assignment

Implementing a lock-in amplifier is the main purpose of the assignment. To create a complete solution, there are several different functionalities and systems that need to be developed.

Before doing anything else, the raw sensor data needs to be extracted and then fed to a lock-in amplifier. This should be done in a standardized manner, in order to facilitate testing with different devices. After establishing connection, a control interface needs to be implemented. It needs to

¹The NV center only emits light after absorbing photons, a phenomenon called photoluminescence [2]

be programmed so that it can control all necessary features of the lock-in amplifier. Following the development of the program, a custom photodetection circuit needs to be designed. The circuit should accommodate the sensors and lock-in amplifier. Lastly, an OLIA² circuit needs to be tested and compared to conventional lock-in systems.

1.3 Assignment specifications

As already explained, the assignment is quite broad and involves both hardware and software, causing the need for a number of different tools.

Most of the hardware tools are already available at the Applied Nanotechnology lab. The lock-in amplifiers which will be used for the tests are the most important pieces of hardware. Zurich Instruments HF2LI is the benchmark lock-in amplifier. The target amplification is at least 10dB. There are also several different photodetectors available and the one which fits the project best will be picked at a later date. Chapter 1.1 already discussed the basics of the CW ODMR protocol. In order to get an operational CW ODMR setup, an MW generator and a laser will be used. MW sweeping needs to be done in the range of 2,8 - 2,9 GHz and the lab already has a custom-built MW generator that can output these frequencies. The laser is mostly outside the scope of the assignment, as it is almost entirely optical in nature. Setting it up, together with the NV samples, is up to the client. However, it is important to note that the fluorescence wavelength should be in the range of 637 - 800 nm, as it plays an important part in reading the CW ODMR data.

In terms of software, there is more freedom of choice. Interfacing with the HF2LI is done through proprietary software, but this is the only required program. There are various electronic computer-aided design (ECAD) software suites that offer the same base functionality. KiCad was selected because the client prefers open-source software. The program for retrieving data from the lock-in amplifiers can be written in both Python and MATLAB. Both languages have good integration with the main lock-in amplifier. They also offer graphic user interface (GUI) programming capabilities and are good for scientific computing overall.

1.4 Methodological approach

The V-Model methodology was selected, as it is well-suited for low-level projects. Figure 1.2 shows a diagram of the phases of the V-Model. Unlike some software-oriented models, the V-Model is very sequential. This can sometimes be seen as detrimental, but in this case it helps with structuring the project. Another benefit of this model is that there are multiple testing activities, which underpin the quality assurance. A contentious feature of the V-Model is the heavy reliance on the initial requirements. This need for deliberate project requirements can be hard to meet, especially if the client representative is not technically proficient. However, this is not the case in this project. The requirements were extensively discussed with the client representative, based on which the project boundaries in Chapter 2 were set up.

²Open Lock-In Amplifier (OLIA) is an open-source microcontroller-based lock-in amplifier. It uses common components, which makes it easy to build [6]

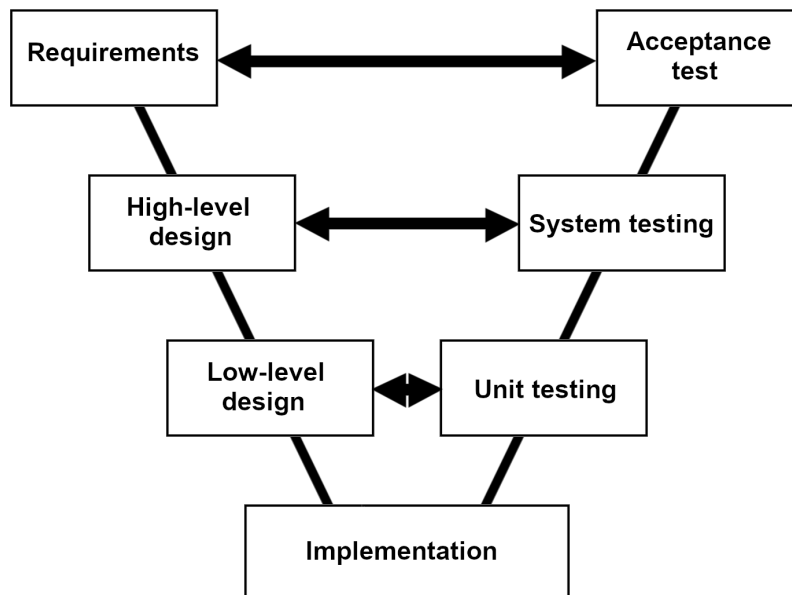


Figure 1.2: V-Model diagram

Chapter 2

Project boundaries

The project boundaries were initially based on the assignment form, but were later discussed with the client and refined further.

Must have

- Hardware platform for photodetection
- Software for signal processing and visualization

Should have

- Tests with different diamond samples

Could have

- Tests with different quantum protocols
- OLIA implementation
- Tests comparing OLIA to market solutions

Will not have

- Laser as a part of the hardware platform
- MW driver upgrade

Chapter 3

Products and project objectives

This chapter describes all the required products and based on them sets up the objectives for the project.

3.1 Products

There are several technical products that need to be delivered, all of which fall into the categories of hardware, software and test data.

Developing a hardware platform for measurements is the most important part of the project. The photodiode subsystem needs to be in the form of a printed circuit board (PCB). It needs to have output connectors that can accommodate a lock-in amplifier. Additionally, an OLIA can also be built using the available schematics.

In terms of software, an application needs to be developed to process and visualize the signal readouts from a lock-in amplifier. It should be able to retrieve data from different lock-in amplifiers. If necessary, modifications to the OLIA firmware will also be made.

Lastly, performance measurements also need to be delivered to the client in a digestible manner. Test data should compare performance of different lock-in amplifiers using different diamond samples. The most important metrics are signal-to-noise ratio (SNR), bandwidth and stability. If there is enough time at the end of the project, tests with alternative quantum protocols can be done. Finally, if an OLIA was built, it needs to be compared in the same way to the different lock-in systems available on the market.

3.1.1 Goals

Based on the MoSCoW priorities from Chapter 2, a set of goals was created to further specify all items from each prioritization category. Every goal was designed so that its outcome results in a tangible project milestone (e.g. a deliverable).

Goal 1 : Create a hardware setup, which measures and amplifies photodiode signals

Goal 2 : Develop software to process and visualize lock-in amplifier signals

Goal 3 : Compare the performance of different lock-in amplifiers

While these goals are practical, they are still not specific enough. To eliminate the possibility of confusion, a set of tasks were created. All tasks contribute to one of the three goals.

Task 1.1 : Design a photodiode PCB, which can accommodate different lock-in amplifiers

Task 1.2 : Build an operable OLIA

Task 2.1 : Develop software that acquires signals and is then able to visualize them

Task 3.1 : Use key performance metrics to compare the OLIA implementation to market solutions

Task 3.2 : Measure OLIA performance using different diamond samples and quantum protocols

Task 1.1 involves the design and production of a photodiode PCB. The PCB has to output signals that are not only compatible with lock-in amplifiers that are available on the market, but also with the OLIA. This part of the hardware design has the highest priority, which is why it will be done first.

Task 1.2 is to build an OLIA amplifier, which can be used at Applied Nanotechnology's laboratory. This will be done with the technical specifications and firmware provided by Harvie and de Mello [6]. The necessity for an OLIA is low, because the Applied Nanotechnology research group already has two lock-in amplifiers.

Task 2.1 is to write an application in Python or MATLAB. This can be done on a different setup, but ideally it will use the hardware setup from **goal 1**. Because the OLIA project uses open-source firmware that differs from proprietary solutions, there might need to be two separate applications. This task can only be completed once a measurement setup is built, so its execution will follow the first two tasks.

Task 3.1 requires all previous tasks to be finished. The completed setup needs to be used to measure the performance of lock-in amplifiers available on the market and the OLIA implementation. SNR, bandwidth and stability are the main metrics that need to be compared.

Task 3.2 is similar to **task 3.1**, but it is a much broader exploration of the performance of the lock-in amplifiers. Using different diamond samples and quantum protocols will show how the amplifier performs and how different conditions affect it. Because the task can be used to verify the setup from **goal 1**, it can also be done before **task 3.1**. Tests with varying diamond samples are more important to the client, which is why they will take precedence over tests with different quantum protocols.

3.1.2 Deliverables

The description of the tasks already provided context for the deliverables, but this subsection contains a formalized version of the deliverables.

1. Photodetection PCB
2. OLIA implementation
3. Software application
4. Comparison visualization
5. Technical documentation

The only deliverable, which was not mentioned in Chapter 3.1.1 is the technical documentation. This is because it should contain information about every task.

Chapter 4

Project activities

Based on the V-Model from Chapter 1.4, four types of activities can be established: requirements, design, implementation and testing. Additionally, documentation is also an activity that is integral to the project.

4.1 Requirements

As already discussed, establishing requirements should be done deliberately, so that no changes need to be made during the project. Several different activities are needed for this stage of the project. One of the most productive ways of creating requirements is by collaborating with the client. Brainstorming sessions are the first step, followed by the creation of a project plan (this document). A careful review by the client is then in order, which should guarantee mutual understanding and agreement on the requirements. During the initial learning and familiarization phase of the project there is still room to modify the requirements, but after that there should be no more changes.

4.2 Design

Both the high-level and low-level design phases fall into this category. The level of detail is what separates the two. In the high-level design phase, the hardware and software designs are done on a more conceptual level. This includes working out what technologies and features the products require, how they should interact and what limitations there might be. Conversely, the low-level design phase is about the details and not the big picture. Some of the central activities during this phase are drawing detailed schematics, compiling component lists and working out the specifics of the different software submodules.

4.3 Implementation

Implementation activities are numerous and range in type and importance. Their goals are discussed in Chapter 3.1.1. Some of the main activities include designing the PCB using software, soldering it together with the components and writing code.

4.4 Testing

Extensive testing is one of the main features of the V-Model. Because of this, there are three separate testing stages. Unit testing is a low-level procedure for verifying all submodules and subsystems work as expected. During this phase both the hardware and software submodules need to be evaluated separately. After ensuring that the proper working of all components, the integration testing phase can begin. It involves combining the different elements of the hardware and software implementations and testing how they work together. Aside from activities about validating the hardware and software integration, there should be activities for testing how the two work together, when applicable. Lastly, acceptance testing needs to be conducted. This involves verifying whether the products meet the requirements. What is even more important than the requirements is ensuring the client is satisfied with the final results.

4.5 Documentation

Documenting different parts of the project is important for future work. There are many different documents which need to be delivered. The most important is the technical report, which should contain technical specifications, operation instructions, possibilities for future development and other information that might be useful to the client. Another important document is the final presentation. The document is useful as an introduction to the assignment, but what is more important is how it is presented.

Chapter 5

Quality assurance

As with any assignment, quality is extremely important. Successful completion of the project is expected, but in order to receive a complete solution without corners being cut, the quality of the work of the student and the resulting products need to be monitored along the way.

Establishing a recurring meeting for discussions with the client is the most simple way of ensuring the output of the student remains consistent and focused. Frequent meetings also help mitigate some project risks borne out of miscommunication.

Based on these discussions, proper testing procedures can be set in place. By planning tests and then reviewing the results together with the client, the wanted level of quality can be achieved.

Chapter 6

Project organization

Some aspects of the project organization were already discussed very briefly, but this chapter is meant to explicitly define all organizational structures.

6.1 Organization

Table 6.1 shows all who is involved in the project and what their roles are.

Name	Work email	Role
Vladislav Serafimov	TBA	Student
Mehmet Can	m.can@saxion.nl	Saxion coach
Ari Ortiz-Moreno	a.r.ortizmoreno@saxion.nl	Client/Company coach

Table 6.1: People involved in the project

Out of the three people mentioned, only the student works on the project directly. It is their responsibility to organize and plan the work. Both coaches are only there to support the student and their execution of the assignment. However, their support functions are slightly different. The Saxion coach is there to ensure the student delivers all required documents to graduate, while the company coach is more concerned with the technical functioning of the student. One responsibility which both coaches share is grading. It is important to note that the company coach also doubles as a client representative. As such, they need to present the student with relevant information about the project and what the client requires from the student.

6.2 Communication

Online communication is convenient, because it offers instantaneous file sharing and storage. Furthermore, the Microsoft Teams environment is good for settling simple questions and issues quickly. Because of this, communication in this project will mainly be done online. However, in-person communication is also extremely important. As mentioned in 5, discussion meetings are vital. Meeting online is possible, but presenting progress is much easier and effective when done in-person.

Chapter 7

Planning

The general schedule, based on the phases from the V-Model, can be seen in Figure 7.1 in the form of a Gantt chart.

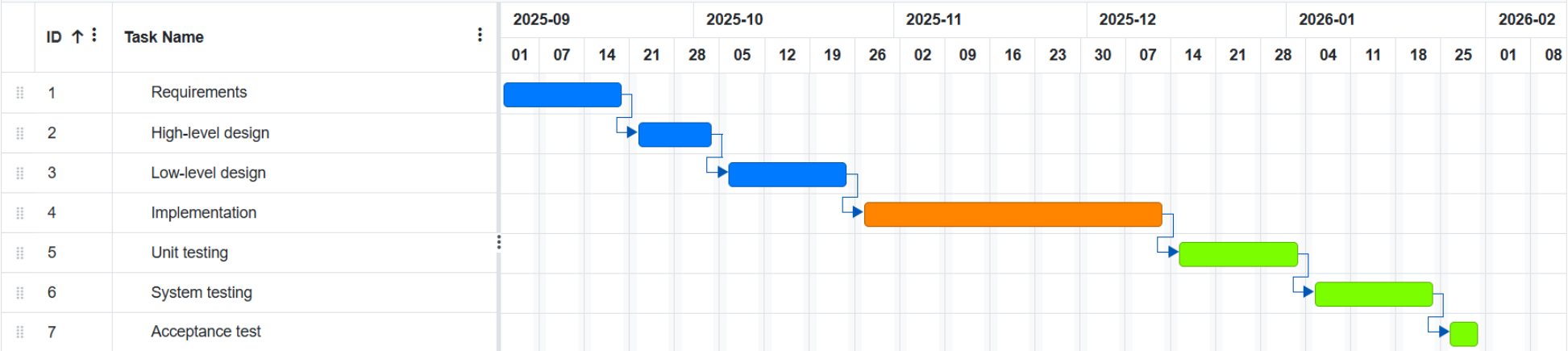


Figure 7.1: Gantt chart

Additionally, there are several important dates, which can be seen in Table 7.1. Most significant of all is the deadline for the Learning and the Technical Report (LR and TR). Some dates are not determined yet, so they have been left as "to be discussed" (TBD).

Name	Type	Week	Date
Project plan	Deliverable due date	W1.3	19.09.2025
Company visit 1	Visit	W1.5	TBD
Draft LR/TR	Deliverable due date	W1.9	07.11.2025
Concept LR/TR	Deliverable due date	W2.4	12.12.2025
Go/no go visit	Visit	W2.6	TBD
Concept LR/TR	Deliverable due date	W2.8	22.01.2026
Company grade	Grade deadline	W2.9	29.01.2026
Company presentation	Presentation	TBD	TBD
Final presentation	Presentation	W2.10	TBD

Table 7.1: Important dates

Chapter 8

Costs and benefits

The direct costs associated with the project are minimal. Largest of all is the monetary compensation of the student. Manufacturing is not a major expense, because there needs to be only one setup. However, the exact cost cannot be easily pinpointed due to the fact that there might be several iterations of the PCBs before settling on one. That being said, the manufacturing cost, combined with the component costs, should still be in the hundreds of euros. There might also be some indirect costs, but they cannot be calculated due to the fact that all lab facilities are shared. It is important to mention that, even though they are not quantifiable, there are some opportunity costs. They arise from the sharing of lock-in amplifiers with researchers, which slows down their progress. Intangible costs were not deemed impactful enough to be included in the cost-benefit analysis (CBA) for this specific project.

Direct benefits are not expected from this project. Research and education are the main purposes of the setup, so it will not be directly profitable. However, there are a number of indirect and intangible benefits to the project. Using the setup as a tool for future research is important to the research group and will probably lead to direct benefits at some point in the future. Educating quantum computing experts is also a significant undertaking. It requires a significant amount of educators, but it also promises direct benefits in the future.

Chapter 9

Risk analysis

Risk analysis and management is closely related to quality assurance and is equally important. The complete risk breakdown can be found in [the attached spreadsheet](#).

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