Enabling photodetection electronics for fluorescent diamond based quantum sensing

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Introduction

This chapter introduces the assignment, along with some foundational concepts of quantum sensing. Furthermore, it covers the methodology, goals and boundaries, which all help outline the trajectory of the project.

3.1   Background

Nitrogen Vacancy (NV) centers [1] are imperfections in the atomic structure of diamonds. The two types of NV centers are NV0 and NV-, as seen in Figure 3.1, but the NV- structure is much more commonly used in quantum applications. These imperfections have the useful property of spin-dependent luminescence. This means that the spin of the NV center affects the frequency of the light emitted by the structure[1]. Using this quality of the NV structure, different environmental metrics (e.g magnetic fields) can be measured.

The Applied Nanotechnology research group is working on a NV-center-based sensor setup. There exist several quantum protocols, but the one this setup needs to support is called Constant-Wave ODMR (ODMR). At its core, Optically Detected Magnetic Resonance (ODMR) is a set of protocols, which can detect magnetic fields based on the fluctuations in the fluorescence of NV centers [3]. CW-ODMR in particular involves exposing the NV center to a Microwave (MW) sweep while illuminating it with a constant light source. This is in contrast with Pulsed ODMR (ODMR) techniques, which use different Transistor-Transistor Logic (TTL) pulse schemes [4] to modulate the MW signal and the light source.

Acquiring and processing data from the setup requires working with weak signals that are hard to distinguish from the environmental noise. While this is a significant problem, it is also very common. Because of this, the industry has already adopted systems to detect weak light sources and measure their intensity. This project is mainly about developing a similar photodetection system for the NV setup.
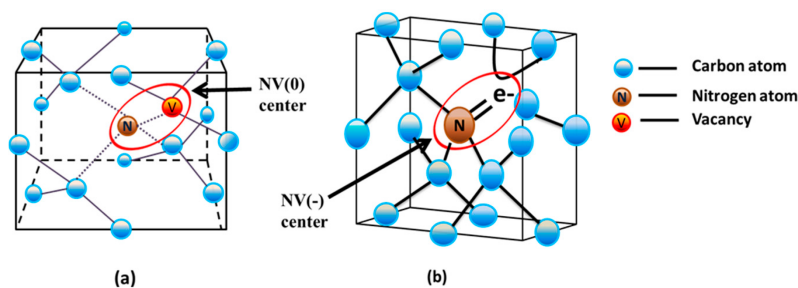
Figure 3.1: NV0 (a) and NV- (b) structures in diamonds (image credit to Haque et al [5])

3.2   Purpose of the assignment

The main purpose of the assignment is to implement a photodetector. The quantum sensing setup requires is made up of other systems as well, which is why there are also several additional functionalities and systems that need to be developed.

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[1]The NV center only emits light after absorbing photons, a phenomenon called photoluminescence [2]

Firstly, a custom photodetector needs to be designed. The circuit should accommodate the sensors and output to a lock-in amplifier. After establishing connection, an Open Lock-In Amplifier (OLIA) [2] circuit needs to be tested and compared to conventional lock-in systems. Additionally, a control interface can be implemented, if there is enough time. It needs to be programmed so that it can control all necessary features of the lock-in amplifier. Aside from the data acquisition system, work also needs to be done on the existing laser-driver, as it needs to be tested and the pulsing sequences for it need to be programmed.

3.3   Assignment specifications

As already explained, the assignment is quite broad and involves both hardware and software, causing the need for a number of different tools.

Most of the hardware tools are already available at the Applied Nanotechnology lab. The lock-in amplifiers which will be used for the tests are the most important pieces of hardware. Zurich Instruments HF2LI is the benchmark lock-in amplifier. The target amplification is at least 10dB. Chapter 3.1 already discussed the basics of the CW-ODMR protocol. In order to get an operational CW-ODMR setup, an MW generator and a laser will be used. MW sweeping needs to be done in the range of 2,8 to 2,9 GHz and the lab already has a custom-built MW generator that can output these frequencies. The laser is mostly outside the scope of the assignment, as it is almost entirely optical in nature. The driving circuitry for it has been developed by a previous intern, but the integration with the sensing setup will be carried out in this project. The driver will be fed TTL pulse data from an Analog Discovery 2. It is important to note that the fluorescence wavelength is in the range of 637 to 800 nm, as it plays an important part in photodetection. It is different from the laser wavelength, which is filtered out and is not supposed to be detected by the photodetector.

In terms of software, there is more freedom of choice. Interfacing with the HF2LI is done through proprietary software. Similarly, pulse generation is going to be done with Waveforms, the Analog Discovery 2 software, but those are the only programs that cannot be replaced. As for Electronic Computer-Aided Design (ECAD) software, there are various suites that offer the same base functionality. KiCad was selected because the client prefers open-source software. The program for retrieving data from the lock-in amplifiers can be written in both Python and MATLAB. Both languages have good integration with the main lock-in amplifier. They also offer Graphical User Interface (GUI) programming capabilities and are good for scientific computing overall.

3.4   Scope of work

The scope of the project was extensively discussed with the company coach. Chapter 3.4.1 sets the scope and Chapter 3.4.2 builds on it, providing more specific details.

3.4.1   Project boundaries

The project boundaries were initially based on the assignment form, but were later discussed with the client and refined further.

Must have

- Hardware platform for photodetection

- Software for signal processing and visualization

- Setup integration

Should have

- Tests with different quantum protocols

- Tests with different diamond samples

Could have

- OLIA implementation

- Tests comparing OLIA to market solutions

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[2] OLIA is an open-source microcontroller-based lock-in amplifier. It uses common components, which makes it easy to build [6]

Will not have

- Laser as a part of the hardware platform

- Laser driver upgrade

3.4.2 Goals

Based on the MoSCoW priorities from Chapter 3.4.1, a set of goals was created to further specify all items from each prioritization category. Every goal was designed so that its outcome results in a tangible project milestone (e.g. a deliverable).

Goal 1 : Create a hardware setup, which measures and amplifies photodiode signals

Goal 2 : Develop software to drive the laser and process lock-in amplifier signals

Goal 3 : Compare the performance of different lock-in amplifiers

While these goals are practical, they are still not specific enough. To eliminate the possibility of confusion, a set of tasks were created. All tasks contribute to one of the three goals.

Task 1.1 : Design a photodetector Printed Circuit Board (PCB)

Task 1.2 : Build an operable OLIA

Task 1.3 : Set up and test laser driver

Task 2.1 : Develop software that acquires signals and is then able to visualize them

Task 2.2 : Program quantum protocol pulse sequences for the laser driver

Task 3.1 : Use key performance metrics to compare the OLIA implementation to market solutions

Task 3.2 : Measure test setup performance using different diamond samples and quantum protocols

Task 1.1 involves the design and production of a photodiode PCB. The PCB has to output signals that are not only compatible with lock-in amplifiers that are available on the market, but also with the OLIA. This part of the hardware design has the highest priority, which is why it will be done first.

Task 1.2 is to build an OLIA amplifier, which can be used at Applied Nanotechnology's laboratory. This will be done with the technical specifications and firmware provided by Harvie and de Mello [6]. The necessity for an OLIA is low, because the Applied Nanotechnology research group already has two lock-in amplifiers.

Task 1.3 is one of the more minor hardware tasks. As the driver is already designed and fabricated, it only needs to be tested and integrated with the rest of the setup. Tests should show that the driver can switch the laser at high speeds, which is necessary for several quantum protocols.

Task 2.1 is to write an application in Python or MATLAB. This can be done on a different setup, but ideally it will use the hardware setup from **Goal 1**. Because the OLIA project uses open-source firmware that differs from proprietary solutions, there might need to be two separate applications. This task can only be completed once a measurement setup is built, so its execution will follow the first two tasks.

Task 2.2 makes sense only as a continuation of **Task 1.3**. Proper integration testing requires pulsing sequences, which will also be used when the setup is functional. These sequences are vital for the second stage of testing of the laser driver, so they need to be developed while the driver is still being tested with simple periodic signals.

Task 3.1 requires all previous tasks to be finished. The completed setup needs to be used to measure the performance of lock-in amplifiers available on the market and the OLIA implementation. Signal-to-Noise Ratio (SNR), bandwidth and stability are the main metrics that need to be compared.

Task 3.2 is similar to **Task 3.1**, but it is a much broader in scope. Using different diamond samples and quantum protocols will show how the sensing setup performs and how different conditions affect it. Because the task can be used to verify the photodetector from **Goal 1**, it can also be done before **Task 3.1**. Tests with varying diamond samples are more important to the client, which is why they will take precedence over tests with different quantum protocols.

3.4.3   Deliverables

The description of the tasks already provided context for the deliverables, but this subsection contains a formalized version of the deliverables.

1. Photodetection PCB

2. OLIA implementation

3. Data acquisition application

4. Pulsing sequences

5. Technical documentation

The only deliverable, which was not mentioned in Chapter 3.4.2 is the technical documentation. This is because it should contain information about every task.

3.5   Methodology

The V-Model methodology was selected, as it is well-suited for low-level projects. Figure 3.2 shows a diagram of the phases of the V-Model. Unlike some software-oriented models, the V-Model is very sequential. This can sometimes be seen as detrimental, but in this case it helps with structuring the project. Another benefit of this model is that there are multiple testing activities, which underpin the quality assurance. A contentious feature of the V-Model is the heavy reliance on the initial requirements. This need for deliberate project requirements can be hard to meet, especially if the client representative is not technically proficient. However, this is not the case in this project. The requirements were extensively discussed with the client representative, based on which the project boundaries in Chapter 3.4.1 were set up.
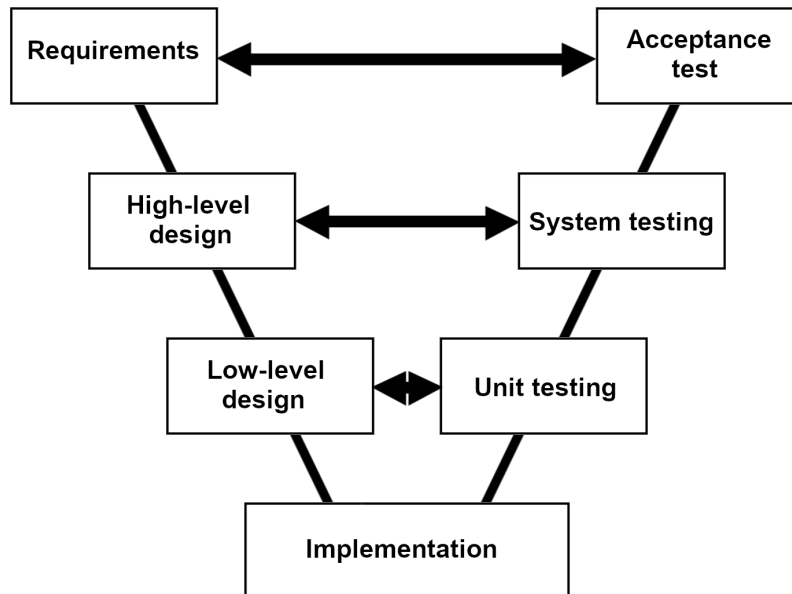

Figure 3.2: V-Model diagram

3.6   Report outline

The introduction is followed by the functional design chapter, which introduces background knowledge, needed to understand the NV setup. After that, the high-level design of the system is presented. It explains the functionality of the various systems that make up the project without delving into specifics.

After that, the technical design explores the low-level design of the systems of the project. It mentions all the necessary details, including calculations, simulations and implementation steps.

Following the design sections, the testing chapter describes the testing goals and what methods were used for measuring. The chapter then presents the test results.

Lastly, the conclusion and recommendation chapters summarize the outcomes of the project and offer proposals for further development.

Chapter 4

Functional design

The functional design presents a high-level overview of the project and what explains what design choices were made on a system-level. Additionally, it provides tangential information that can be useful for understanding the working principle of the quantum sensing setup.

4.1 Background knowledge

Some background knowledge is required to understand the purpose of the projects, because the setup the project revolves around uses several non-trivial physics concepts. This section outlines the basic ideas behind the physics that enable the quantum sensing setup to work.

4.1.1 Spin states

Spin, at least in quantum mechanics, is the intrinsic angular momentum of a particle, which is described by the quantum number of the particle. Importantly, it differs from the angular momentum in classical mechanics, which is extrinsic. Spin characterizes systems of particles, usually electrons, using quantum entanglement. This phenomenon refers to the "entanglement", or spin correlation, of a set of particles.

These foundational concepts make it possible to describe quantum systems using various states. The most simple states, used as descriptors, are the energy states. Ground states refer to the system being in an energy minimum. On the other hand, excited states signify that the system has more energy than at its ground state. Additionally, there can be intermediate states during state transition.

While the aforementioned states describe system energy, they have no bearing on the spin. For the purposes of this project, only two spin states need to be explained. The first one is called singlet state. It occurs when an entangled system has a total spin of 0, caused by the mutual cancellation of spin. For example, for a system of two entangled electrons to be a singlet, the two spins would need to point in opposite directions. The second spin state is called triplet and it has a total spin of 1. Triplets can consist of, for instance, two unpaired electrons with aligned spins that sum up to 1. Singlets and triplets both have major distinguishing features and properties, which is why they can be used for quantum sensing. Aside from the difference in spin, triplets tend to have higher energy levels. They also exhibit attraction to magnetic fields, while singlets cannot be influenced directly by magnetism.

4.1.2 Zeeman effect

Discovered by Pieter Zeeman in 1896, the Zeeman effect is another important phenomenon that enables quantum sensing. If under normal circumstances a light-emitting quantum system only emits one spectral line, then when a magnetic field is applied to it the line will split, thus exhibiting the Zeeman effect. In an NV center, this phenomenon causes the $|\pm 1\rangle$ energy level to split into $|+1\rangle$ and $|-1\rangle$.

4.1.3 Energy levels

Figure 4.1 shows the energy level diagram of an NV center.

After illuminating the NV center with a green laser, electrons go from a ground state to an excited state. They then need to return to the ground state. This decay process is usually direct
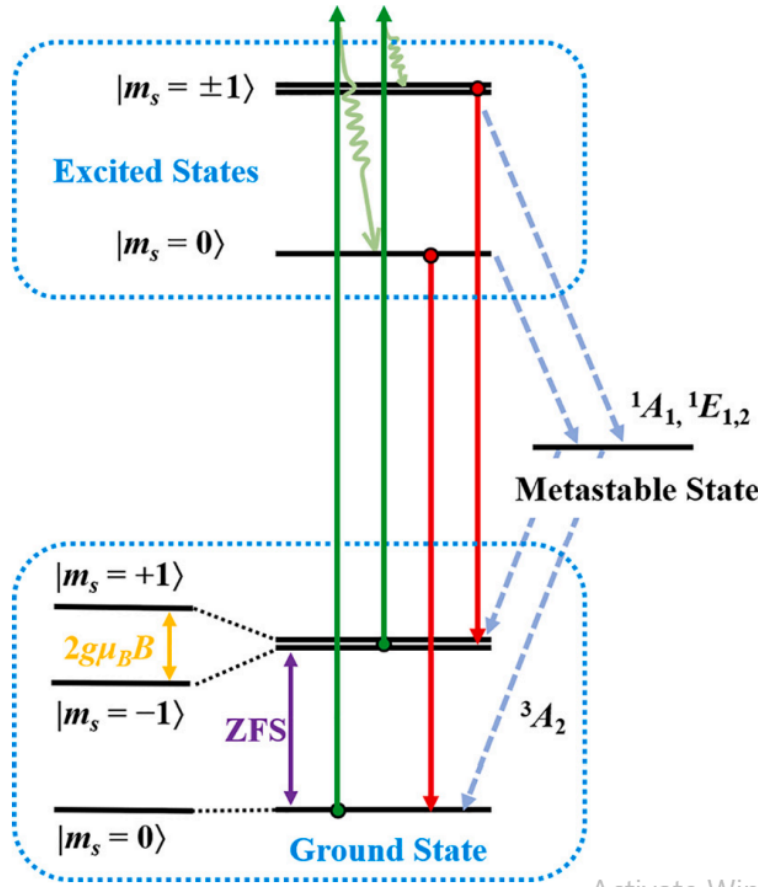
Figure 4.1: NV center energy level diagram (image credit to Song et al [7])

and emits a red photon, however it can also go through the metastable singlet state and emit an infrared photon. It should be noted that whenever the NV center is exposed to the resonant frequency $\nu = 2,87 GHz$ the probability of emitting an infrared photon is significantly increased.

4.2   Quantum protocols

There are a number of different quantum protocols, which differ in what they can measure, in how precisely they can measure it and in the complexity of the hardware they require to operate. CW-ODMR is the main protocol this project is aimed at facilitating. As Saijo et al [8] demonstrate, CW-ODMR is relatively simple, while still detecting magnetic field with reasonable sensitivity. P-ODMR does outperform CW-ODMR [9], but because of the added complexity working with it is a "Could have" (see Chapter ??). Before being able to run P-ODMR on the setup at the lab, several protocols need to be implemented first [4]. T_1 measurements, which are one of the fundamentals of Magnetic Resonance Imaging (MRI), should be conducted first. Afterwards, Rabi oscillations need to be observed and measured in order to calibrate the setup. Without these intermediate protocols, P-ODMR cannot be performed.

4.2.1   CW-ODMR

CW-ODMR is a quantum protocol that has seen extensive usage in sensing setups that measure magnetic fields. Its working principle is centered around the photoluminescence of NV centers and the difference in light emission based on spin states. As already discussed in Chapter 4.1.3, the NV center emits less visible light when at the resonant frequency ν. Additionally, two more dips appear on the spectrum if a magnetic field is applied.Calculating the magnetic field can be done using the formula $h\nu = g_e \mu_B B_{AC}$[1]. Figure 4.2 shows an example of what a CW-ODMR spectrum might look like.

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[1]3, In the formula, h is the Planck constant, g_e is the g-factor of the electron and μ_B is the Bohr magneton. Knowing all other variables, B_{AC} can easily be calculated.
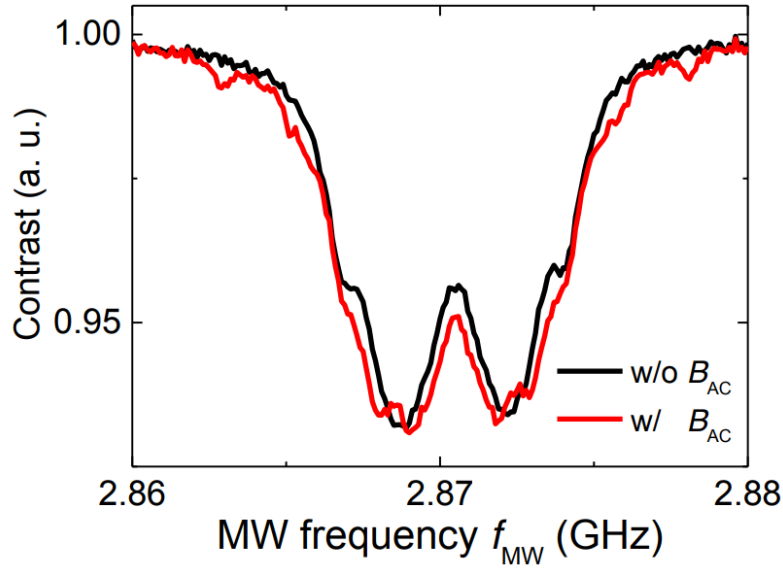
Figure 4.2: Example of a CW-ODMR spectrum **with** and **without** a magnetic field (image credit Saijo et al [8])

4.2.2 T_1 relaxometry

T_1, T_2 and T_2^* relaxation time measurements are commonly associated with radiometry [10], but they have other uses too. T_1 measurements, in particular, are useful in the realm of quantum sensing. Knowing the T_1 relaxation time, which refers to the time it takes for the spins in an NV system to decay back to their original state, makes it possible to adjust the pulse sequences of more complex protocols and thus get better results.
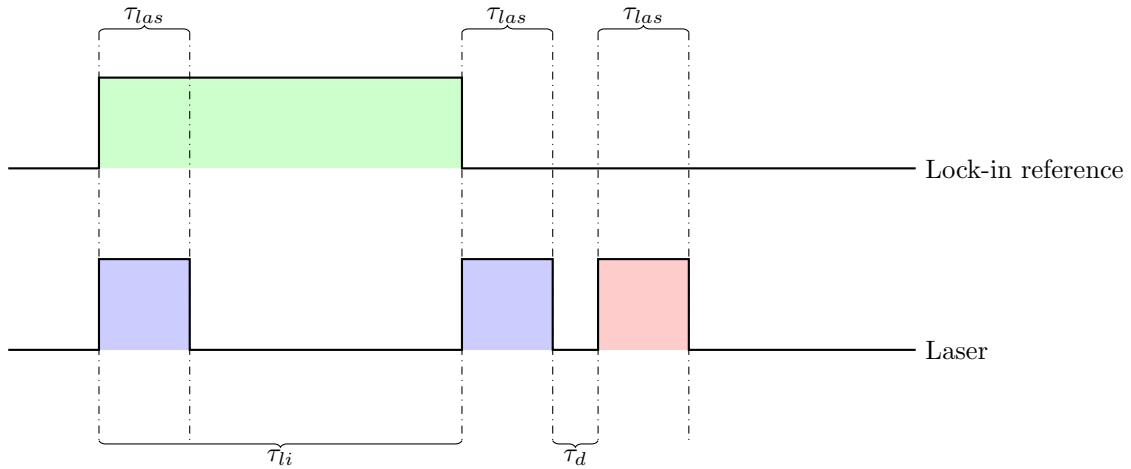

Figure 4.3: Laser and reference signals for T_1 measurements

The waveforms which are needed for T_1 measurements are shown if Figure 4.3. While both are important in practice, the lock-in is not as relevant, at least in this section of the report. However, its working principle is explored in Chapter 4.6. For now, all that needs to be said about the lock-in reference signal is that it is periodic and τ_{li} is much longer than τ_{las} (Sewani et al [4] propose $\tau_{li} = 15ms$ and $\tau_{las} = 5\mu s$). Laser pulses, on the other hand, are not periodic. Instead, there are three short pulses every reference period (which is $2\tau_{li}s$ long). The two blue pulses have the same timing every cycle, because they initialize the NV spins. However, the red pulse always occurs after the variable dark time τ_d. Depending on the T_1 decay at the time of the readout pulse, a different voltage will be detected. Figure 4.4 shows what results can be expected when measuring T_1. Determining the value of T_1 is done using Formula 4.1, where I is the light intensity, I_∞ is the light intensity offset and τ_d is the dark time.

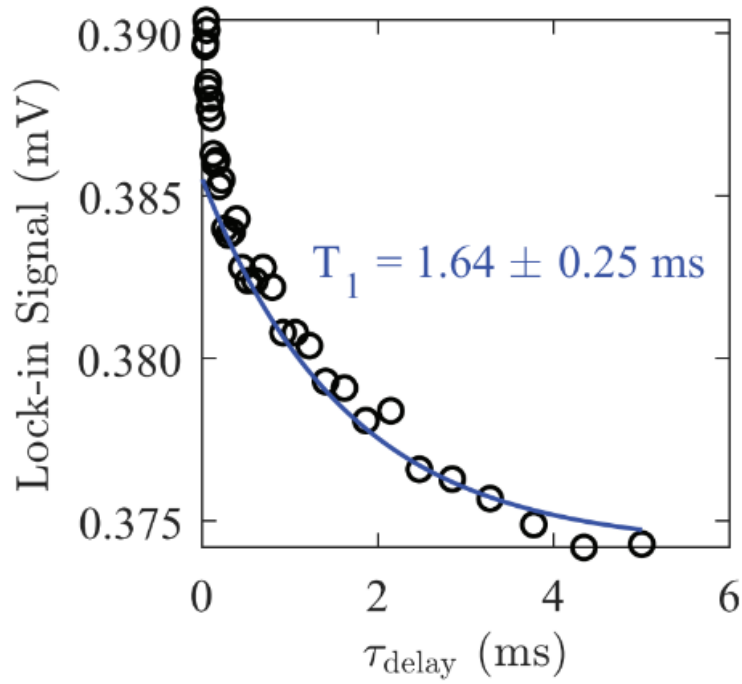$$I(t) = I_\infty + I(0)e^{-\tau_d/T_1} \tag{4.1}$$

Figure 4.4: Results of a set of T_1 measurements with varied dark time τ_d (image credit to Sewani et al [4])

4.2.3   P-ODMR

As a descriptor P-ODMR is somewhat vague, which is why it has been used for a number of protocols. Different pulse schemes can result in radically different . Some researches use

4.3   Quantum sensing setup

Executing all of the aforementioned protocols requires a sensing setup with some specific capabilities. This section discusses the devices that make up the setup and the functionalities require by each protocol.

A high-level diagram of the quantum sensing system can be seen in Figure 4.5. There are 2 input devices in the system, but most protocols exclusively use the function generator. On the other hand, only a single lock-in amplifier is used as an output device for all measurements.

The blue boxes show the components responsible for sending and receiving light signals. They constitute the core of the setup. Waveforms are generated by the function generator, no matter the protocol. Using a current driver, the laser is then used to illuminate the diamond sample. The resulting luminescence is then measured by the photodetector and processed by the lock-in amplifier. All protocols need this part of the setup, however they utilize it differently.

Microwave generation, when needed, starts with a TTL signal from the function generator or a frequency sweep using a dedicated device. Out of all protocols, only CW-ODMR requires a sweep. The antenna then broadcasts the signal, while the switch isolates the signal generator from it.
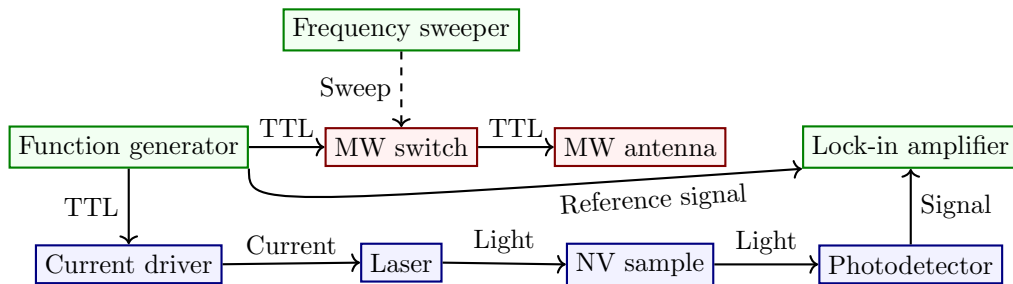

Figure 4.5: High-level overview of the quantum sensing setup

4.4   Laser driver

As discussed in Chapter 3.3, the laser driver has already been designed and built. It gets TTL signals from a waveform generator and outputs current to drive the connected laser.

However, the designer explicitly states that tests were only done with frequencies of up to 100 kHz [11]. This is insufficient for the project, as for example, the pulsing sequence for T_1 measurements use pulses with duration of 5 μs and downtime of as little as 1 μs[2].

4.5   Photodetection

Photodetection is how the NV photoluminescence is measured, effectively turning light into current using a photodiode. However, a bare photodiode cannot be connected to a lock-in amplifier, because it functions as a current generator. This is where the photodetection PCB comes in. Its purpose is to transform the current into voltage and then amplify the resulting signal to the necessary voltage level.

4.5.1   Photodiodes

At the heart of the photodetector lies the photodiode. It is what turns light into current and drives the Transimpedance Amplifier (TIA) that follows it. While the specifications of the diode are not the most important part of the photodetector circuit, they still need to be discussed, due to the impact the two ways of biasing can have on the circuit.

The two most common photodiode are shown in Figure 4.6. Reverse-biased circuits, similar to the one in Figure 4.6a, are used in optical receivers and other high-speed use cases. However, the drawback of biasing the diode using the negative supply voltage V_{EE} is that it results in an increased dark current, which is current that, unlike photocurrent, runs through the diode without any light being present. Precise low-light measurements, such as those needed by the quantum setup, require the photodetector to have high linearity, which cannot be achieved when the dark current is comparable to the photocurrent. Photovoltaic-mode biasing reduces the dark current at the cost of signal strength and bandwidth [12]. This configuration is much more common in low-illumination setups, where the signals can be in the nanoampere to picoampere range. As the photoluminescent NV centers emit extremely low amounts of light, the photovoltaic configuration was chosen for the photodetector implementation for the quantum sensing setup.
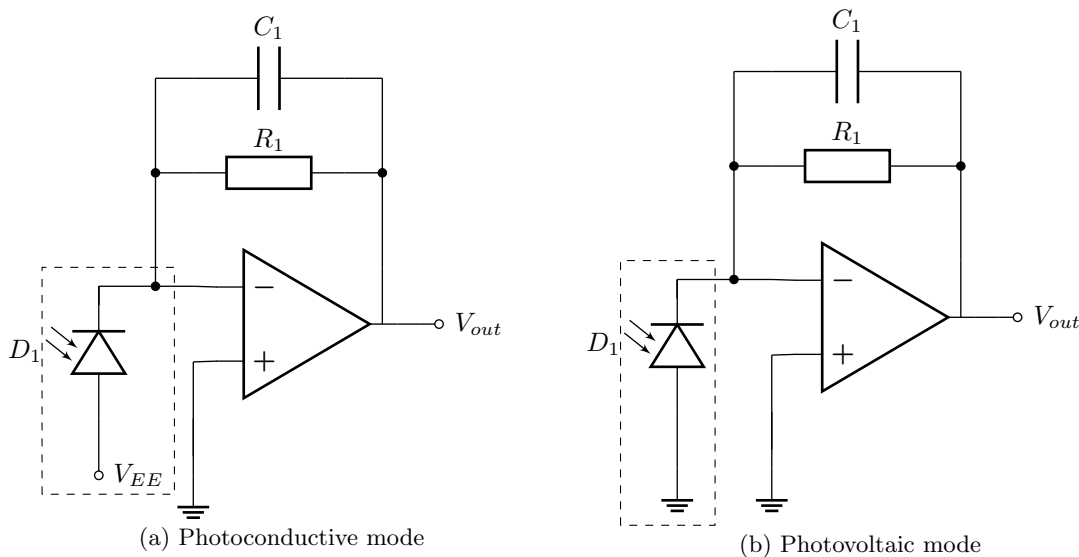

(a) Photoconductive mode

(b) Photovoltaic mode

Figure 4.6: Reverse-bias photodiode configurations

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[2]These values are taken from Sewani et al [4], but the clients wants to make the timings used by the quantum sensing setup even faster

4.5.2   TIA design

Designing the photodetector is mostly about creating a TIA, which is a tool used for converting current to voltage, and specifically tuning its parameters so that it works with the selected photodiode under operating conditions.
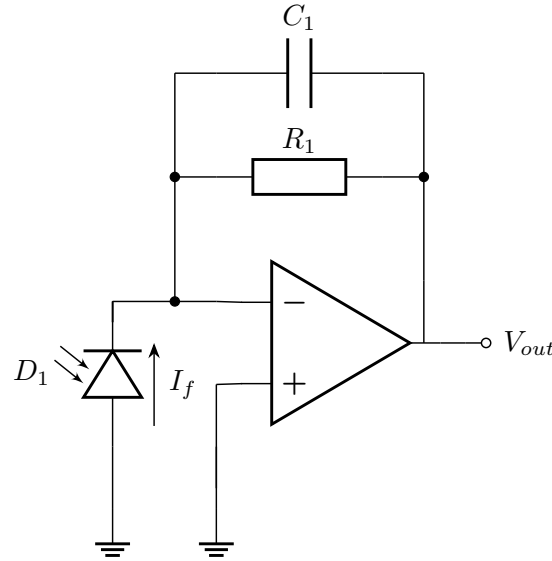

Figure 4.7: Basic TIA circuit

The basic circuit, shown in Figure 4.7, is all that is required for photodetection. Following the Texas Instruments guidelines for making a TIA [13], all the parameters of the circuit can be calculated.

$$R_1 = \frac{V_{o\ max}}{I_{f\ max}} \text{ assuming } V_{o\ min} = 0\text{V} \tag{4.2}$$

The resistor R_1 determines the transimpedance gain of the amplifier. Equation (4.2) shows how its value can be calculated by using the output voltage and input current. It should be noted that the minimum output voltage is always 0 in this use case.

$$C_1 = \frac{1}{2\pi R_1 f_{rc}} \tag{4.3}$$

The capacitance of C_1 can be calculated using the resistance of R_1 and the cutoff frequency f_{rc}, as shown in Equation (4.3).

In addition, a non-inverting amplifier can be added, to increase the gain of the circuit even more, without affecting the phase. Figure 4.8 shows how it can be connected to the output of the TIA from Figure 4.7.

Equation (4.4) is used to calculate the gain of second amplification stage. The formula is derived from the voltage divider formed by the two resistors and the fact that $V_{ot} = V_-$.

$$A_{nia} = \frac{Vout}{Vot} = 1 + \frac{R_3}{R_2} \tag{4.4}$$

The design is based on circuits provided by the client. At their request, the first version of the PCB is an exact replica of the original circuit.

4.5.3   Power requirements

Knowing the power needs of the photodetector is important for decreasing the size of the quantum setup. As bench power supplies take up a lot of space and exceed the photodetector requirements by orders of magnitude, an on-board supply or a breakout board will better fit the project. To minimize overhead while still having a supply that can handle the circuit, the power usage of the photodetector needs to be calculated.

When calculating the power of the circuit in Figure 4.8, the most important elements to consider are the op-amps. As the non-inverting amplifier has high input impedance, the system can be broken down into two op-amp circuits.
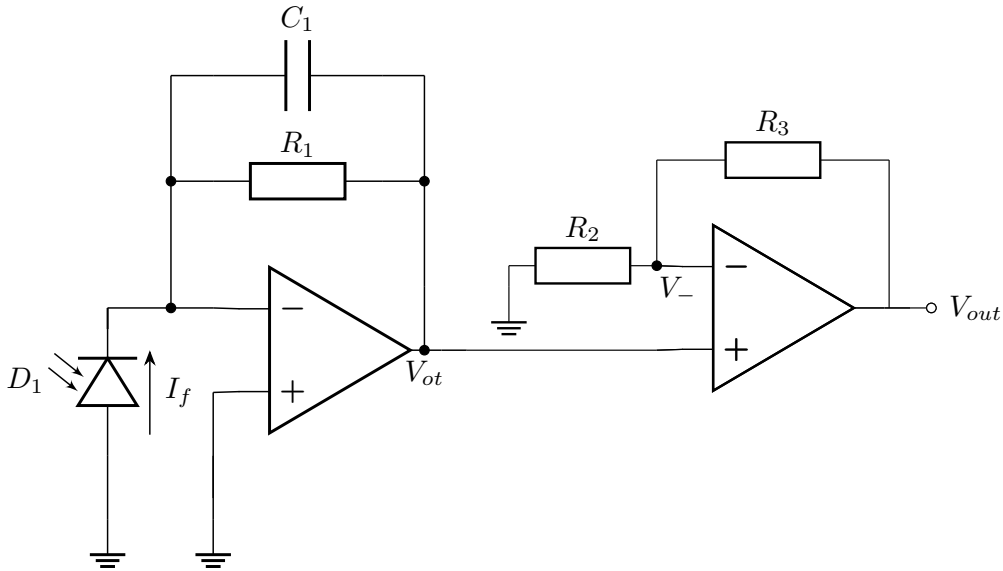
Figure 4.8: TIA with non-inverting output amplification

The op-amp in the TIA circuit has an intrinsic power dissipation caused by its quiescent current, as well as a load power dissipation. Due to the very high input impedance of the non-inverting topology that follows the TIA, there is no load current. Nevertheless, the feedback loop still draws a current $I_{f\alpha}$ from the amplifier. Equation (4.5) shows how this current can be calculated, after which the feedback power dissipation $I_{f\alpha}$ can be determined using Equation (4.6). It should be noted that for $C_1 R_1 \ll 1$ the equivalent impedance Z_1 is equivalent to the resistance R_1 at low frequencies.

$$\begin{cases} I_{f\alpha} = \frac{V_{ot}}{Z_1} \\ Z_1 = R_1 || (\frac{1}{sC_1}) = \frac{R_1}{1+sC_1 R_1} \end{cases} \tag{4.5}$$

$$P_{f\alpha} = (V_{cc} - V_{ot})I_{f\alpha} \tag{4.6}$$

As a characteristic of the amplifier Integrated Circuit (IC), the quiescent current $I_{q\alpha}$ can be taken from the datasheet to calculate its power dissipation $P_{q\alpha}$ with the formula shown in Equation (4.7).

$$P_{q\alpha} = (V_{cc} - V_{ee})I_{q\alpha} \tag{4.7}$$

Ultimately, the total power dissipation P_α of the TIA can be calculated by summing the results of the previous equations (see Equation (4.8)).

$$P_\alpha = P_{f\alpha} + P_{q\alpha} \tag{4.8}$$

Similarly to the amplifier that was just discussed, the non-inverting op-amp has a quiescent power dissipation $P_{q\beta}$ and a load power dissipation $P_{f\beta}$. It should be noted that even though V_out is not connected to in Figure 4.8, the output of the photodetector in the sensing setup needs to be connected to a lock-in amplifier. Even then, the load current that runs through it is imperceptibly low, which is why the power in Equations (4.9) and (4.10) is calculated in a similar way.

$$I_{f\beta} = \frac{V_{out} - V_{ot}}{R_3} \tag{4.9}$$

$$P_{f\beta} = (V_{cc} - V_{out})I_{f\beta} \tag{4.10}$$

Equation (4.11) shows the quiescent power dissipation, which again is based on the intrinsic characteristics of the op-amp and combining all powers again yields the total power dissipation P_β of the amplifier, shown in Equation (4.12).

$$P_{q\beta} = (V_{cc} - V_{ee})I_{q\beta} \tag{4.11}$$

$$P_\beta = P_{f\beta} + P_{q\beta} \tag{4.12}$$

Additionally, the power dissipated by R_2 is given in Equation (4.13).

$$P_2 = V_{ot} I_{f\beta} \tag{4.13}$$

4.6 Lock-in amplification

As a means of retrieving data, lock-in amplification is the most suitable for the setup due to its relatively low cost, signal retrieval capabilities and the possibility of using one amplifier for several setups.
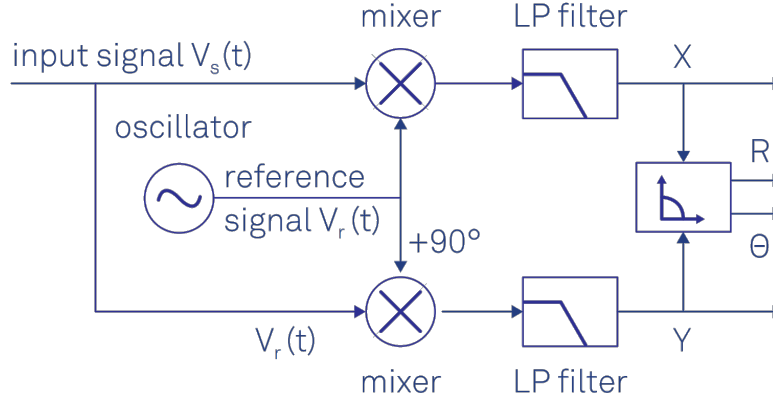

Figure 4.9: High-level diagram of a lock-in amplifier (image credit to Zurich Instruments [14])

Figure 4.9 shows an overview of how lock-in amplification works. Simply put, the amplifier receives a signal V_s and extracts the data at the frequency of a reference signal V_r. To explain this more thoroughly, lock-in amplification utilizes the fact that every signal is made of periodic waves, which are equal to zero when averaged.

$$V_s = R\cos\left(\omega_s t + \theta\right) = \frac{R}{2}e^{i(\omega_s t + \theta)} + \frac{R}{2}e^{-i(\omega_s t + \theta)} \tag{4.14}$$

Equation (4.14) shows how the mathematical expression of a sine wave signal, where ω_s is the frequency and θ is the phase of the signal. Using the trigonometric identity $\cos x = \frac{1}{2}(e^{ix} + e^{-ix})$, the signal can be represented in terms of complex numbers.

$$V_r = e^{-i\omega_r t} \tag{4.15}$$

In Equation (4.15), a simplified reference signal is shown. Real applications might require more complex reference signals, but V_r is perfect for demonstrating the lock-in principle.

$$V_m = V_s V_r = \frac{R}{2}e^{i(\omega_s - \omega_r)t + \theta} + \frac{R}{2}e^{-i(\omega_s + \omega_r)t + \theta} \tag{4.16}$$

Finally, Equation (4.16) shows the product of the signal V_s and the reference V_r. In the case of $\omega_s = \omega_r$, there is a signal at 0 Hz and another one at $2\omega_s$ Hz. The latter, however, should be attenuated to an undetectable level by the low-pass filters shown in Figure 4.9. This way, both the unwanted signal appearing at double the frequency and all of the noise at different frequencies is attenuated.

Chapter 5

Technical design

This chapter discusses the implementation and integration of the various parts of the project. Even though the main focus here is on design and simulation, implementation is also covered where necessary.

5.1 Current driver

5.1.1 Testing configuration

To run test on the current driver, the setup shown in Figure 5.1 was used. The Analog Discovery is used as a function generator and its signal is fed to the driver. Current from the driver is then ran through a resistive load and the resulting voltage is measured by the oscilloscope. Additionally, the TTL signal from the Analog Discovery might need to be measured as well, because its stability starts to deteriorate at around 1 MHz.
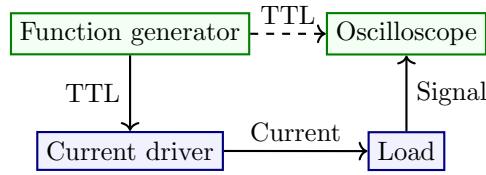

Figure 5.1: Current driver testing setup

5.1.2 Pulsing sequences

Previously, T_1 and P-ODMR were mentioned when discussing pulsed protocols. However, due to the high-frequency instability of the Analog Discovery signals, only sequences for T_1 measurements were made. Chapter 4.2.2 presented an overview of the T_1 pulsing scheme.

    The lock-in amplifier reference signal can easily be generated by the Waveforms software. This is not the case for the laser driver signal. Waveforms supports custom sequence programming, however the system for writing these sequences only allows for pulses, which have a duration τ_{las} for which $\tau_{las} \geq \frac{T_{seq}}{100}$ holds true[1]. It should also be noted that using the built-in pulse generation functionalities only allow for pulses that fit the formula $\tau_{las} = x\frac{T_{seq}}{100}$, where $x \in \mathbb{Z}^+$ and $x \leq 100$. These restrictions make it impossible for pulsing sequences to be designed in Waveforms. Thankfully, the software can read files, which makes it possible to customize a sequence more.
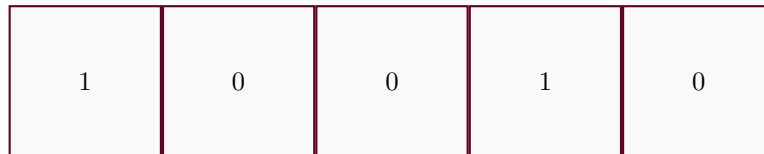

Figure 5.2: Sequence buffer

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[1] T_{seq} is the period of the whole sequence

But before delving into implementation details, the working of pulse generation in Waveforms needs to be discussed. An example of a filled Waveforms buffer, is shown Figure 5.2. A TTL signal can be generated using the values from it by setting the period T_{seq} of the sequence and the peak voltage V_{max}. Those two values are necessary, because the buffer contains normalized values. In the example, each element has a value of either 0 or 1, which translate to 0 or V_{max} when entered into the function generator. Furthermore, each pulse has a value of $\tau_{las} = \frac{T_{seq}}{5}$.

A MATLAB script (see Figure A.4 for full script) was written to generate buffer values based on the pulse duration τ_{las}, the scaling factor $\gamma = \frac{T_{seq}}{\tau_{las}}$ and the normalized dark time $t_d = \frac{\tau_d}{\tau_{las}0}$. Figure 5.3 shows a T_1 sequence where $\tau_{las} = 20$ µs, $T_{seq} = 30$ ms and $\tau_d = 200$ µs (consequently $\gamma = 1500$ and $t_d = 10$).
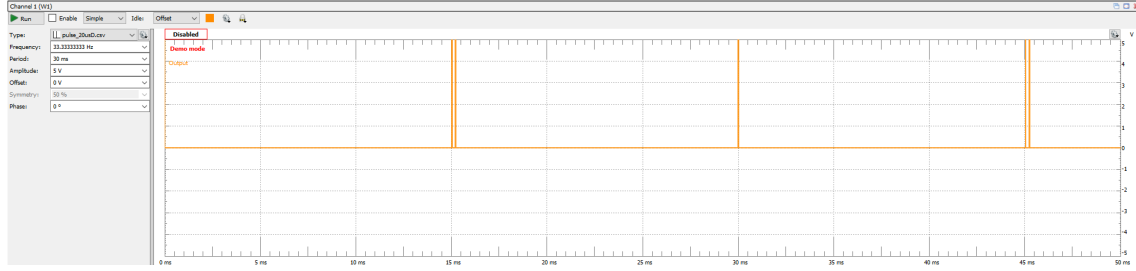

Figure 5.3: Waveforms function generator view after importing a T_1 pulse sequence

5.2   Photodetector design

As an integral part of the sensing setup, the photodetector needs a lot of attention. Good amplification is crucial to increasing the SNR of the quantum setup. Based on the design ideas presented in the functional design (see Chapter 4.5.2), the photodetection circuit can be drawn up.

5.2.1   First iteration

As previously mentioned, the first iteration of the PCB uses component values provided by the client. There is no specific information for the design process, aside from the fact that the original designer made the photodetector for input currents in the range of several nanoamperes. Such low inputs require high R_1 values, however increasing the resistor value results in more biasing current going to the amplifier and a more limited dynamic range [15]. Furthermore, it is entirely possible that the original design did not thoroughly consider the bandwidth needed for different protocols, as CW-ODMR setups do not require broad bandwidths [16]. Biasing is another concern with this photodiode setup. The configuration shown in Figures 4.7 and 5.4 has the diode in the photovoltaic mode, which is suitable for low-frequency, low-light operation. In spite of this setup being suited to CW-ODMR, pulsed protocols require micro to nanosecond precision

5.2.2   Second iteration

Due to time constraints, this iteration uses the same topology as the first one. That being said, it is still designed analytically, taking into account the general practices, but also considering more subtle factors that might hinder the performance of the system. This section uses the mathematical principles presented in [17–19] to optimize the design of the existing amplifier.

Introducing the real-world parasitic capacitances to the ideal TIA shown in Figure 4.7 results in the circuit in Figure 5.4. The diode capacitance C_d, combined with the differential and common-mode capacitances of the amplifier (C_{df} and C_{cm}), contributes a significant amount of capacitance to the circuit and can result in instability [20].

$$C_i = C_d + C_{df} + C_{cm} \tag{5.1}$$

The combined input capacitance (Equation (5.1)) is needed when solving the equation arising from Kirchhoff's Current Law (KCL) at the inverting input of the amplifier. Z_1 (Equation (5.2)), or the combined impedance of R_1 and C_1, is another prerequisite for deriving the transimpedance Z_t using KCL. Additionally, the open-loop gain A_{ol} is needed for the derivation of the transimpedance. Equation (5.3) shows the open-loop gain, expressed with the Direct Current (DC) open-loop gain A_o and the open-loop cutoff frequency ω_o, which is equivalent to $\frac{v_{out}}{v_{in}}$.
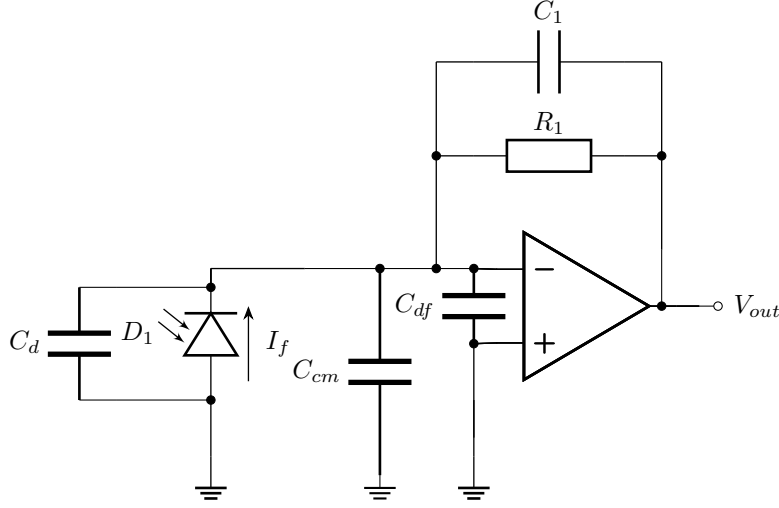
Figure 5.4: Parasitic capacitances in a TIA circuit

$$Z_1(s) = \frac{1}{\frac{1}{R_1} + sC_1} \tag{5.2}$$

$$A_{ol}(s) = A_o \frac{\omega_0}{s + \omega_0} = \frac{v_{out}}{v_{in}} \tag{5.3}$$

Finally, the values in the KCL equation (Equation (5.4)) can be substituted, from which Z_t can be derived, as seen in Equation (5.5).

$$i_f = \frac{v_i}{\frac{1}{sC_i}} + \frac{v_{in} - v_{out}}{Z_1(s)} \tag{5.4}$$

$$Z_t(s) = \frac{v_{out}}{i_f} = (R_1 \frac{A_o}{1 + A_o}) \frac{\frac{\omega_o(1+A_o)}{(C_i+C_1)R_1}}{s^2 + s\frac{1+\omega_o(C_i+(1+A_o)C_1)R_1}{(C_i+C_1)R_1} + \frac{\omega_o(1+A_o)}{(C_i+C_1)R_1}} \tag{5.5}$$

Before moving on to the pole analysis, the impact of the quality factor Q on the system response should be considered (see Equation (5.12)). Two system poles present when $Q > 0,5$. For $Q = \frac{\sqrt{3}}{3}$, the system has a Bessel response, which has the flattest group delay, however, if $Q = \frac{\sqrt{2}}{2}$, then the system has a Butterworth response, which means it has the flattest possible amplitude response. Although the group delay is not as important as the amplitude response, a Bessel response also results in smaller amount of overshoot and less jitter, which is why it is preferred.

$$H(s) = H_0 \frac{(-s_1)(-s_2)}{(s - s_1)(s - s_2)} = H_0 \frac{s_1 s_2}{s^2 + s(-s_1 - s_2) + s_1 s_2} \tag{5.6}$$

Based on this consideration, the poles can be found using the general form of a second order transfer function, shown in Equation (5.6). The general form also makes it obvious that the DC gain $H_0 \approx R_1$, assuming that A_0 is very big.

$$\begin{cases} -s_1 - s_2 = \frac{1+\omega_o(C_i+(1+A_o)C_1)R_1}{(C_i+C_1)R_1} \\ s_1 s_2 = \frac{\omega_o(1+A_o)}{(C_i+C_1)R_1} \end{cases} \tag{5.7}$$

Taking the general-equation poles and their counterparts from the Z_t expression yields the system of equations shown in Equation (5.7). Solving for one pole first results in a quadratic equation with roots shown in Equation (5.8).

$$s_{1,2} = -\frac{1 + \omega_o(C_i(1 + A_o)C_1)R_1}{2(C_i + C_1)R_1} \left(1 \pm j\sqrt{\frac{4\omega_o(1 + A_o)(C_i + C_1)R_1}{(1 + \omega_o(C_i + (1 + A_o)C_1)R_1)^2} - 1}\right) \tag{5.8}$$

$$s_{1,2} = -\frac{3}{2}(1 \pm j\frac{\sqrt{3}}{3}) \tag{5.9}$$

Achieving a critically-damped system, can be done by getting the roots of the general form of a Bessel filter (as seen in Equation (5.9)) and using the imaginary parts to get Equation (5.10). This expression can be solved with the quadratic formula again, finally resulting in an answer for the compensating capacitor.

$$\frac{4\omega_o(1 + A_o)(C_i + C_1)R_1}{(1 + \omega_o(C_i + (1 + A_o)C_1)R_1)^2} = \frac{4}{3} \tag{5.10}$$

$$C_1 = \frac{-2\omega_o R_1 C_i \pm \sqrt{12 A_o C_i R_1 \omega_o - 3}}{2\omega_o R_1 (1 + A_o)} \tag{5.11}$$

Although Equation (5.11) says there are two solutions for the capacitor, the term under the square root will always be much bigger than the rest of the numerator[2], effectively determining the sign of the roots. Simply put, this leaves only one root possible, as capacitors cannot have negative capacitance values.

To further characterize the system, the cutoff frequency ω_c and damping factor Q can be calculated, because the system adheres to the general form, shown in Equation (5.12). Substituting in the pole expressions from Equations (5.6) and (5.7).

$$H(s) = H_o \frac{\omega_c^2}{s^2 + s\frac{\omega_c}{Q} + \omega_c^2} \tag{5.12}$$

$$\begin{cases} \omega_c = \sqrt{\frac{\omega_o(1 + A_o)}{(C_i + C_1)R_1}} \\ Q = \omega_c \frac{(C_i + C_1)R_1}{1 + \omega_o(C_i + (1 + A_o)C_1)R_1} \end{cases} \tag{5.13}$$

In the process of designing the TIA circuit, the noise should be considered. Figure 5.5 shows the thermal noise generated by the resistor (modeled as the current source i_{nR}) and the differential noise of the amplifier (modeled as the current source i_{nA})[3].
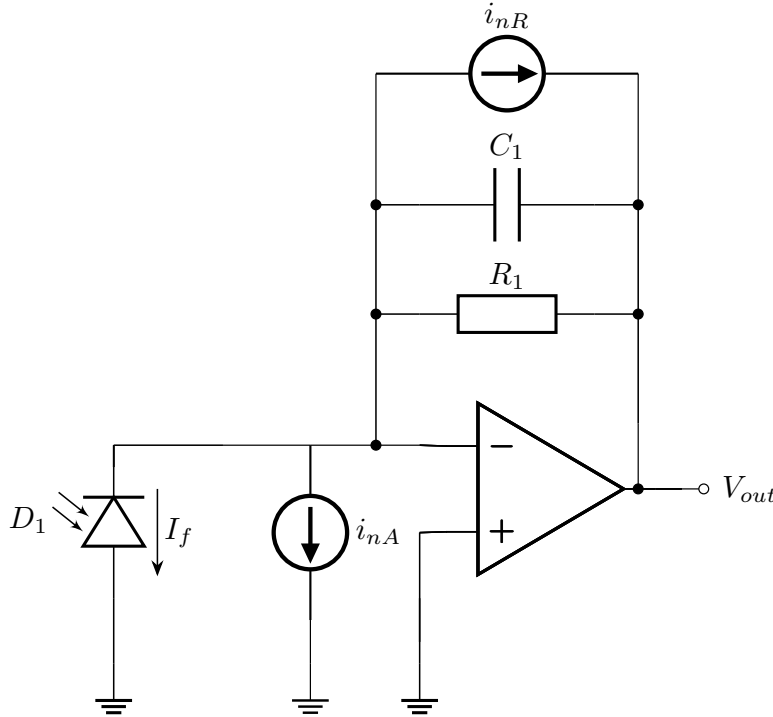

Figure 5.5: Noise sources in a TIA circuit

Calculating i_{nR} can be done using the Johnson-Nyquist formula, as shown in Equation (5.14). Given the fact that the temperature T and noise cutoff frequency δf cannot be modified in this case, R_1 is the only variable that can be used to lower the thermal noise current.

---

[2]Assuming real-world amplifier and diode specifications, and realistic transimpedance gain requirements

[3]Other noise generators, like the gate shot noise, channel noise and induced gate noise can also be calculated, but because they largely depend on the underlying technology of the Field-Effect Transistor (FET) op amp, they have not been investigated

$$i_{nR} = \sqrt{\frac{4kT\Delta f}{R_1}} \tag{5.14}$$

Combining the noise sources leads to Equation (5.15), which also shows that the noise can be modeled as voltage sources.

$$i_n(f) = \sqrt{i_{nR}^2 + i_{nA}^2} \Leftrightarrow v_n(f) = \sqrt{v_{nR}^2 + v_{nA}^2} \tag{5.15}$$

In order to get a better understanding of the noise, its transfer function can be calculated in much the same way as the transimpedance (Equations 5.3 - 5.5), leading to Equation (5.16). While it might not be immediately obvious from the mathematical expression, the noise gain is 0 dB at low frequencies, but it peaks when $f \approx f_c$. Chapter 5.3.2 shows the noise frequency response obtained from the system equation.

$$H_n(s) = \frac{v_o}{v_n} = \frac{A_o \omega_o (s + \frac{1}{(C_i + C_1)R_1})}{s^2 + s(\frac{1}{(C_i + C_1)R_1} + \omega_o(1 + A_o \frac{C_1}{C_i + C_1})) + \frac{\omega_o(1 + A_o)}{(C_i + C_1)R_1}} \tag{5.16}$$

5.2.3   Third iteration

While the second version of the photodetector focuses on improving the TIA performance, the third iteration is more about adapting the design for the setup. The most important considerations for the setup are function and size. Form factor constraints arise from the fact that the setup needs to be as small and adhere to the Thorlabs mounting standard. Photodetection also needs to not require many external connections or ideally be standalone. This is feasible for the data flow, as the photodetection PCB only needs light and an output to work. Unlike the data connections, version two of the design has a suboptimal power delivery system, as it uses three pins for the supply of power.

Three alternatives methods for power delivery were considered, all of which only need power and ground to work. The first one is an onboard split-rail power supply, which would take the power input and would output a fixed positive and negative voltage. Figure 5.6 shows an example of what a split-rail supply implementation might look like. There are also external option [21], which might be necessary in the future, if the setup size needs to be reduced even more.
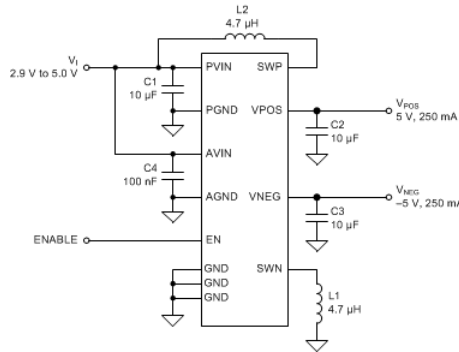

Figure 5.6: Example of a split-rail power supply implementation using the TPS65133 IC (image credit to [22])

A simpler solution would be to use a virtual ground driver IC, as it would provide a reference point at half of the supply voltage. Such a supply would also be good for decreasing the size, as the PCB needs minimal additions to satisfy the power needs of the photodetector. Additionally, a virtual ground circuit, which behaves similarly, can be made using two voltage regulators. The example in Figure 5.7 is a slightly more stable implementation

Another possibility is to implement a charge pump, which inverts the input voltage. This setup would only need half of the input voltage required for the virtual ground solution, effectively enabling it to be powered via a standard Universal Serial Bus (USB) connector.

The specifications of the different power supply solutions are shown in Table 5.1, from which can be deduced that there is a correlation between complexity and performance. The split-rail power supply, as the most complex solution, has the biggest current output range and lowest current consumption, but as illustrated in Figure 5.6, there are a number of other components that are required for proper implementation. In contrast, the virtual ground supply concept has relatively
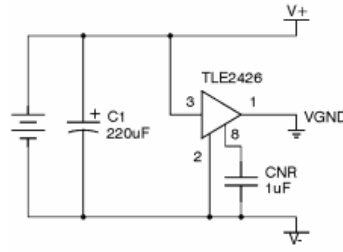
Figure 5.7: Example of a virtual ground driver using the TLE2426 IC (image credit to [23])
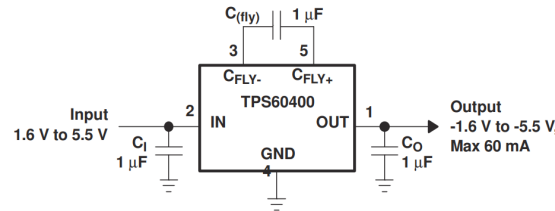

Figure 5.8: Example of a charge pump inverter using a TPS6040x family IC (image credit to [24]

poor specifications compared to the other options, but it is the only solution that does not employ a switching mechanism, thus avoiding any possible noise. Lastly, the charge pump inverter offers reasonable performance, while not needing as many extra components as the split-rail supply. Furthermore, it addresses the stability concerns that might arise when using a virtual ground IC.

Ultimately, the charge pump is the most fit for this project, because it is relatively simple, while also offering excellent stability. The decision was reached only after taking into account the power calculations from Chapter 4.5.3, according to which all devices can handle the power requirements of the circuit. Another important factor when making the decision was the efficiency of the switching solutions. Based on slight fluctuations in the amount of current drawn, the charge pump can have as much as twice the efficiency of the split-rail power supply [22, 24], due to its high efficiency at low currents.

5.3 Photodetection simulation

Simulation is an important part of the designing process, as digital models of devices can be more detailed than the mathematical models presented thus far. Using different simulation programs, the real-world performance can be approximated and the math can be validated.

5.3.1 First iteration
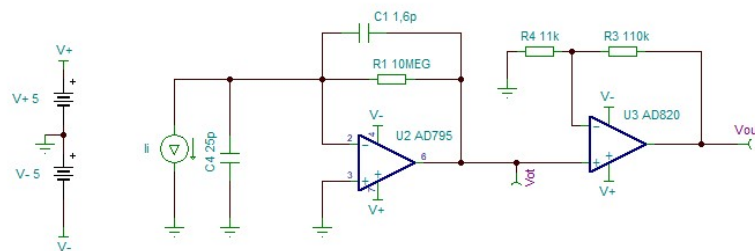
Figure 5.9: First iteration of the photodetector circuit

Figure 5.9 shows the schematic of the circuit. The capacitor C_4, as well as the current source I_1 are used to simulate the behavior of a photodiode.

Tina-TI was used to simulate and visualize the DC gain and frequency response of the system, as seen in Figure 5.10. The signals V_{ot} and V_{out} correspond to the output of the transimpedance and non-inverting amplification stage respectively. The AC plot (Figure 5.10a) shows the cutoff,

	Split-rail power supply	Virtual ground	Charge pump
IC	TPS65133	TLE2426	TPS60400
V_{in} (V)	5	10	5
V_{out} (V)	±5	±5	-5
I_{out} (mA)	250	40	60
Current draw (μA)	15	300	210

Table 5.1: Comparison of power supply options

at 10,77 kHz, and the gain inside the gain bandwidth, which is 160,82 dB. The DC plot (Figure 5.10b) shows the voltage with respect to the current and demonstrates the linearity of the system in the range of 0 to 46,31 nA. After V_{out} reaches 5 V, the output remains fixed, because it cannot exceed the voltage provided to the amplifier.
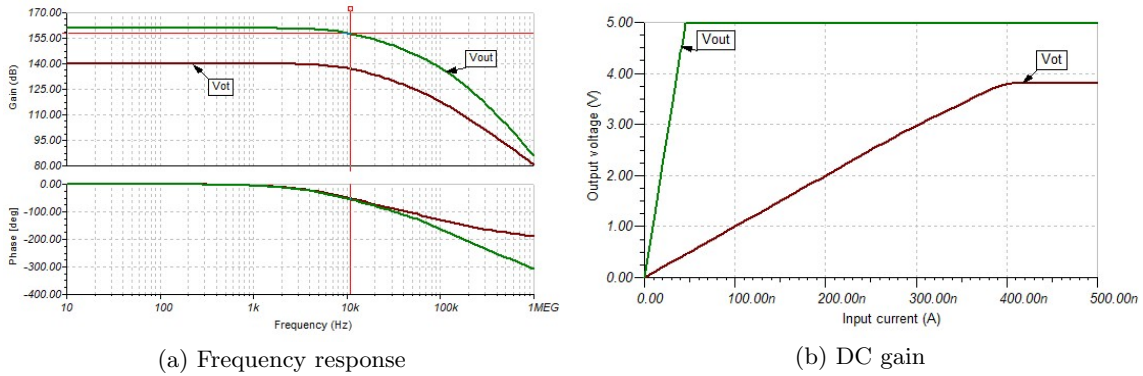

(a) Frequency response

(b) DC gain

Figure 5.10: Simulation of the first iteration of the photodetector

5.3.2   Second iteration

A MATLAB script was written to calculate the component values based on the calculations in Chapter 5.2.2.

MATLAB calculates the component values with high precision and sourcing components with the exact values is not feasible, which is why components with standard values will be used in the setup. Simulations with standardized components were also done to compare the mathematically-ideal setup to the one in practice.

TIA version	v2 (Ideal)	v2 (Real.)	v1
C_1 (pF)	0,969 04	1	1,6
f_c (kHz)	22,0768	21,167	11,2422
Q	0,577 35	0,559 96	0,354 87

Table 5.2: Photodetector parameters with a 10 MΩ feedback resistor R_1

Table 5.2 contains the MATLAB calculation results and shows the values of f_c and Q when only C_1 changes. R_1 is kept the same, as it is equivalent the DC transimpedance Z_{tDC}[4]. Importantly, the results show that the previous iteration had an overdamped response. Although the quality factor with realistic component values is also lower than the ideal $Q = \frac{\sqrt{3}}{3}$, the system sill behaves approximately like a Bessel filter.

Using these values, the system model was calculated and plotted in Figure 5.11. Certain differences can be seen between the two iterations. Most notably, the cutoff frequency f_c of the first iteration is somewhat smaller than that of the second iteration. Furthermore, it can be observed that both the ideal and realistic versions of the system behave similarly.

Figure 5.12 confirms the calculations, with some slight differences that can be attributed to the Simulation Program with Integrated Circuit Emphasis (SPICE) models. As predicted, the frequency response of the circuit shows an increased cutoff frequency of 19,69 kHz, 8,92 kHz more than the previous iteration. Up to the cutoff frequency, the transimpedance gain is 160,83 dB,

---

[4]For realistic op-amps, $\frac{A_o}{A_o+1} \to 1$, which means $Z_{tDC} \equiv R_1$ can be assumed to simplify explanations
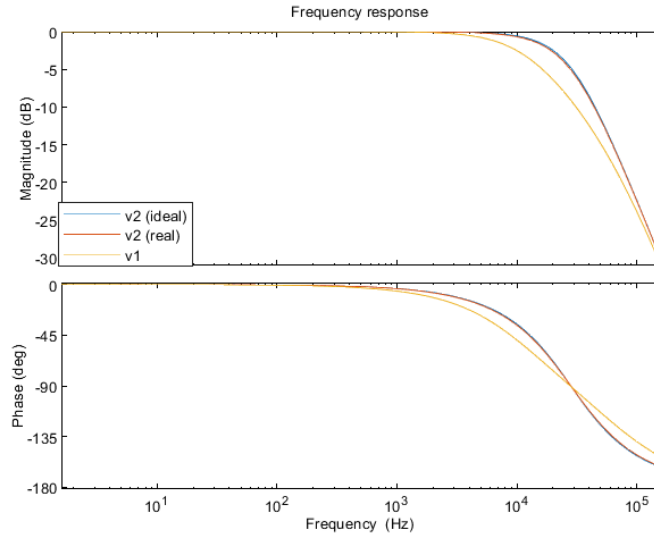
Figure 5.11: Bode plot of the system with different capacitors

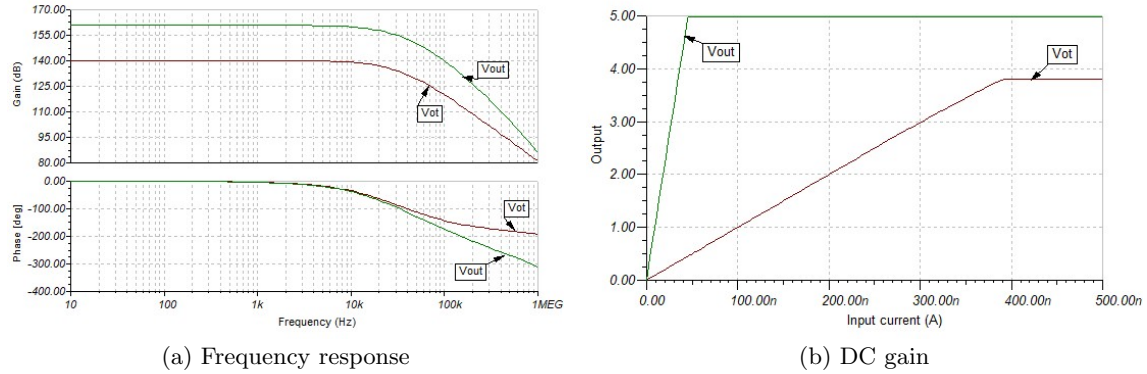(a) Frequency response   (b) DC gain

Figure 5.12: Simulation of the second iteration of the photodetector

which is almost the same as the former gain of 160,82 dB. Furthermore, Figure 5.12b shows that, similarly to the first iteration, the system displays linearity in the range of 0 to 45,86 nA. This is a reduction of 450 pA compared to the previous version and also 4,14 nA less than the ideal upper limit of the linear region.

5.3.3   Third iteration

Before deciding on a power supply system, the power requirements needed to be compared to the specifications listed in Table 5.1. A MATLAB script, which can be found in Appendix A, was written and used to calculate that, given a ± 5 V supply, the system requires 2,3021 mA of current. All solutions can provide that amount of current with reasonable overhead, which is why the choice was based on size, complexity and stability.

5.4   Photodetection implementation

Creating the physical PCB is a more straightforward process than the design and simulation, but it still needs to be discussed.

5.4.1   First iteration

Figure 5.13 shows the back side of the PCB. It hosts all components, except for the photodiode, which sits unobstructed on the front side. Requirements for the physical dimensions were also set by the client. The PCB needs to fit the Thorlabs mount standard, since the rest of the setup also uses it.
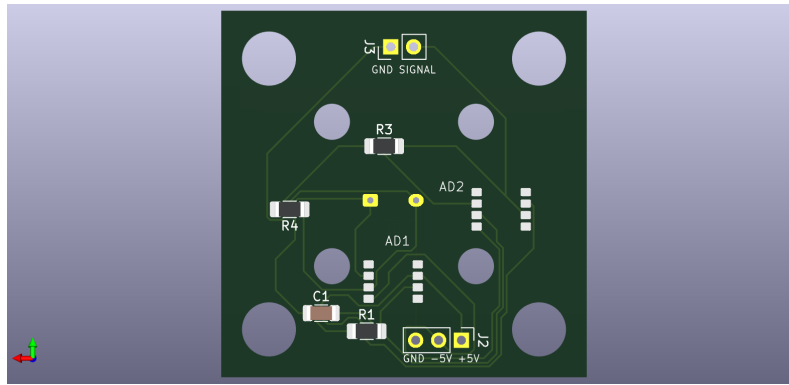
Figure 5.13: First iteration of the photodetection PCB

5.4.2 Second iteration

Due to the fact that the topology of the first iteration was kept, the PCB does not need any modifications in order to work with the circuit parameters. However, after a discussion with the client, a ground plane was added. Usually such an addition would be needed in high-speed and/or high-power use cases, but the circuit only deals with low-speed, very-low-power signals. The reason for the ground plane in the updated design is broader quantum protocol support. While CW-ODMR operates at low frequencies, future expansion of the sensing setup might require support for different high-speed pulsing sequences. In that case, the component values can be modified again, without the need for making a new board. Figure 5.14 shows the new board design. The updated board also has better routing than the previous version, which was done to optimize the noise performance.
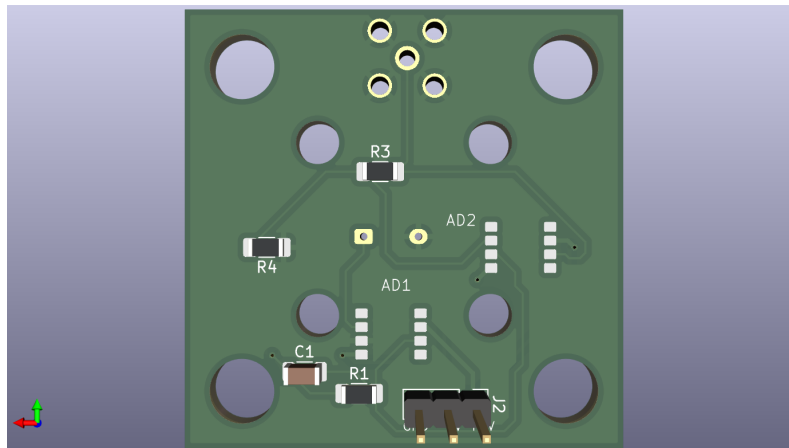
Figure 5.14: Second iteration of the photodetection PCB

5.4.3 Third iteration

The third version of the board has more components, which combined with the fixed board dimensions, made it more difficult to do the placing and routing. Additionally, the clearance of the power and ground nets was increased. Figure 5.15 shows the ECAD view of the third iteration of the photodetector.
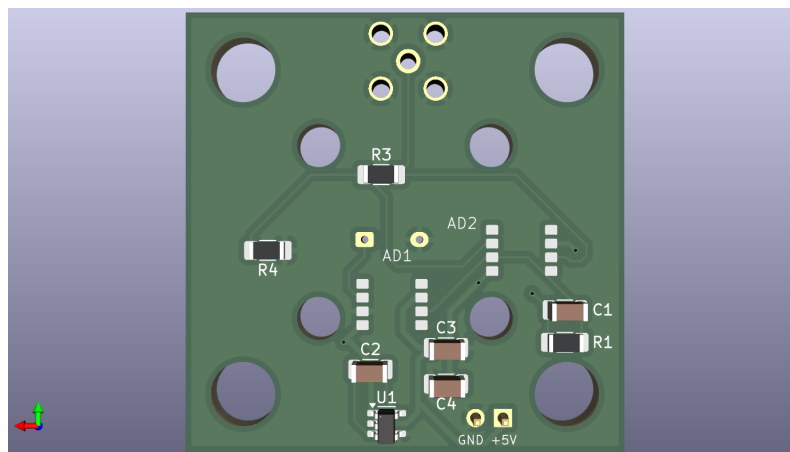
Figure 5.15: Third iteration of the photodetection PCB

Chapter 6

Testing and results

Table 6.1 shows all the tasks discussed in Chapter 3.4.2 and their current status. While working on the project, the client decided that purchasing a lock-in amplifier is more worthwhile than implementing OLIA, which is why tasks associated with its implementation are marked as canceled. The items with a reassigned status were given over to two students, who are also involved with the quantum sensing setup. The laser driver was reassigned, because a completely new design was needed, the making of which would require too much time. The pulse sequence script was finished and it was passed on to one of the other students for further refinement and possibly for the implementation of different protocols.

Number	Task	Status
1.1	Design photodetector	Completed
1.2	Build OLIA	Canceled
1.3	Set up laser driver	Partially completed; reassigned
2.1	Develop data acquisition software	Canceled
2.2	Program pulse sequences	Mostly completed; reassigned
3.1	Compare lock-in amplifiers	Canceled
3.2	Test quantum sensing setup	In-progress

Table 6.1: List of tasks and their completion status

6.1   Test goals

6.1.1   TIA

Two methods of characterizing the TIA were used. As the main point of interest is the linearity of the amplifier, the first characterization method is to simulate photodiode readings at different light intensities by using a current source. By varying the DC current level, either by sweeps or manually, the performance of the physical device can be compared to the simulations.

  The second method supplements the results of the first one. It involves measuring the scattering parameters S_{11} and S_{21} and calculating the frequency response of the system. Equation (6.1) shows the formula used to approximate the transimpedance Z_t for a load resistor R_o [18]. While this method offers insight into the Alternating Current (AC) characteristics of the circuit, its accuracy is worse than that of the first method, due to the approximative nature of the test.

$$Z_t(f) \approx \frac{S_{21}(f)R_o}{1 - S_{11}(f)} \tag{6.1}$$

6.2   Test setup

Characterizing the system requires precision, due to the small currents at its input. As the diode detects ambient light and cannot be set to one current level reliably, a model needs to be used instead.

6.3 Results and discussion

6.4 Known limitations

Chapter 7

Conclusion

Chapter 8

Recommendations

Appendix A

Code

```matlab
 1  % requirements
 2  V_o_tia = .5; % out voltage of tia
 3  V_o = 5; % out voltage of non-inverting amp
 4
 5  % OPA795 specs
 6  GBW = 1.5*10^6;
 7  A_o_dB = 100; % DC open-loop gain in dB
 8  A_o = 10^(A_o_dB/20); % DC open-loop gain
 9  f_o = GBW/A_o; % open-loop cutoff frequency
10
11  % BPW34 specs
12  I_f = 50*10^-9; % max current from diode
13  C_d = 25*10^-12; % diode capacitance
14
15  % parasitic capacitances
16  C_cm = 2.2*10^-12; % common-mode capacitance
17  C_df = 2*10^-12; % differential capacitance
18  C_i = C_d + C_cm + C_df; % total parasitic capacitance
19
20  % R1 calculations
21  gain = V_o_tia/I_f;
22  gain_db = 20*log10(gain); % uncomment if you need gain in dB
23  R_1 = gain*(1 + A_o)/A_o;
24  %R_1 = 10e6; % realistic value for BPW34
25
26  % C1 calculation
27  omega_o = 2*pi*f_o;
28  C_1_num = -2*omega_o*R_1*C_i + 1 + sqrt(12*A_o*C_i*R_1*omega_o - 3)
        ;
29  C_1_den = 2*omega_o*R_1*(1 + A_o);
30  C_1 = C_1_num/C_1_den;
31
32  % system transfer function
33  divisor = (C_i + C_1)*R_1; % normalization coefficient
34  num = A_o*omega_o/divisor; % numerator
35  s1 = (1 + omega_o*R_1*(C_i + (1 + A_o)*C_1))/divisor; % first-order
        s
36  s0 = omega_o*(1 + A_o)/divisor; % zero-order s
37  den = [1, s1, s0];
38  tia = tf(num, den);
39  Q = sqrt(s0)/s1;
40
41  % cutoff calculations
42  omega_c = bandwidth(tia);
43  f_c = omega_c/(2*pi);
44
45  % noise transfer function
46  num = A_o*omega_o*[1 1/divisor];
47  s1 = 1/divisor + omega_o*(1 + A_o*C_1*(C_1 + C_i));
48  s0 = omega_o*(1 + A_o)/divisor; % 0th oreder term remains the same
49  den = [1 s1 s0];
50  tia_noise = tf(num, den);
51  tia_noise1 = tia_noise;
```

Figure A.1: TIA calculations

```matlab
1   % results and plots
2   disp('Component values')
3   disp('R1: ' + R_1/10^6 + ' MOhm')
4   disp('C1: ' + C_1*10^12 + ' pF')
5   disp('System characteristics')
6   disp('fc = ' + f_c/10^3 + ' kHz = ' +  omega_c/10^3 + ' krad/s')
7   disp('Q (actual): ' + Q)
8   disp('Q (ideal) : ' + 1/sqrt(3) + ' = sqrt(3)/3')
9   % bode plot system
10  figure
11  opts = bodeoptions;
12  opts.FreqUnits = 'Hz';
13  opts.Title.String = 'Frequency response';
14  bodeplot(tia, {10, 10^6}, opts);
15  % impulse plot system
16  figure
17  ip = impulseplot(tia);
18  % bode plot noise
19  figure
20  opts_n = bodeoptions;
21  opts_n.FreqUnits = 'Hz';
22  opts_n.Title.String = 'Noise frequency response';
23  bodeplot(tia_noise, {10, 10^6}, opts_n);
24  grid on
```

Figure A.2: Plots of the TIA calculations

```matlab
% values from the TIA calculation script
R_1 = 10e6; % transimpedance (DC component)
V_o_tia = .5; % out voltage of tia
V_o = 5; % out voltage of non-inverting amp

V_cc = 5; % positive rail
V_ee = -5; % negative rail
I_q_a = 1.5e-3; % maximum quiescent current of AD795
I_q_b = 0.8e-3; % maximum quiescent current of AD820
R_2 = 11e3; % see report
R_3 = 110e3; % see report

% TIA-stage power
I_f_a = V_o_tia/R_1;
P_l_a = (V_cc - V_o_tia)*I_f_a; % load power
P_q_a = (V_cc - V_ee)*I_q_a; % quiescent power
P_a = P_l_a + P_q_a; % total power

% NIA-stage power
I_f_b = (V_o - V_o_tia)/R_3;
P_l_b = (V_cc - V_o)*I_f_b; % load power
P_q_b = (V_cc - V_ee)*I_q_b; % quiescent power
P_b = P_l_b + P_q_b; % total power

% R2 power
P_R2 = V_o_tia*I_f_b;

% total power
P = P_a + P_b + P_R2;
I = P/(V_cc - V_ee);
disp('TIA power     : ' + P_a*10^3 + ' mW')
disp('NIA power     : ' + P_b*10^3 + ' mW')
disp('R2 power      : ' + P_R2*10^3 + ' mW')
disp('Total power   : ' + P*10^3 + ' mW')
disp('Supply current: ' + I*10^3 + 'mA')
```

Figure A.3: Power calculation

```matlab
tp = 20e-6; % pulse duration
scaling_f = 1500;
td = 10; % normalized dark  time
T = scaling_f * tp; % period
t = 1:scaling_f;
y = zeros(1, length(t));
y(1) = 1; % init pulse cancel
y(scaling_f/2 + 1) = 1; % init pulse
y(scaling_f/2 + td + 1) = 1; % readout pulse
plot(t, y)
writematrix(y, 'pulse_20usD.csv')
disp('Pulse duration   : ' + tp*10^6 + ' us')
disp('Dark time        : ' + td*10^6*tp + ' us')
disp('Sequence period  : ' + T*10^3 + ' ms')
```

Figure A.4: Power calculation

Glossary

AC Alternating Current 28

DC Direct Current 19, 20, 24, 28

ECAD Electronic Computer-Aided Design 6, 26

FET Field-Effect Transistor 21

GUI Graphical User Interface 6

IC Integrated Circuit 16, 22, 23

KCL Kirchhoff's Current Law 19, 20

MRI Magnetic Resonance Imaging 11
MW Microwave 5, 6

NV Nitrogen Vacancy 5, 8, 10–12, 14

ODMR Optically Detected Magnetic Resonance 5, 36
   CW-ODMR Constant-Wave ODMR 5, 6, 11–13, 19, 26
   P-ODMR Pulsed ODMR 5, 11, 13, 18
OLIA Open Lock-In Amplifier 6–8, 28

PCB Printed Circuit Board 7, 8, 14, 22, 25, 26

SNR Signal-to-Noise Ratio 7, 19
SPICE Simulation Program with Integrated Circuit Emphasis 24

TIA Transimpedance Amplifier 14–16, 19–21, 28
TTL Transistor-Transistor Logic 5, 6, 13, 14, 18, 19

USB Universal Serial Bus 22

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