

J4A CPU

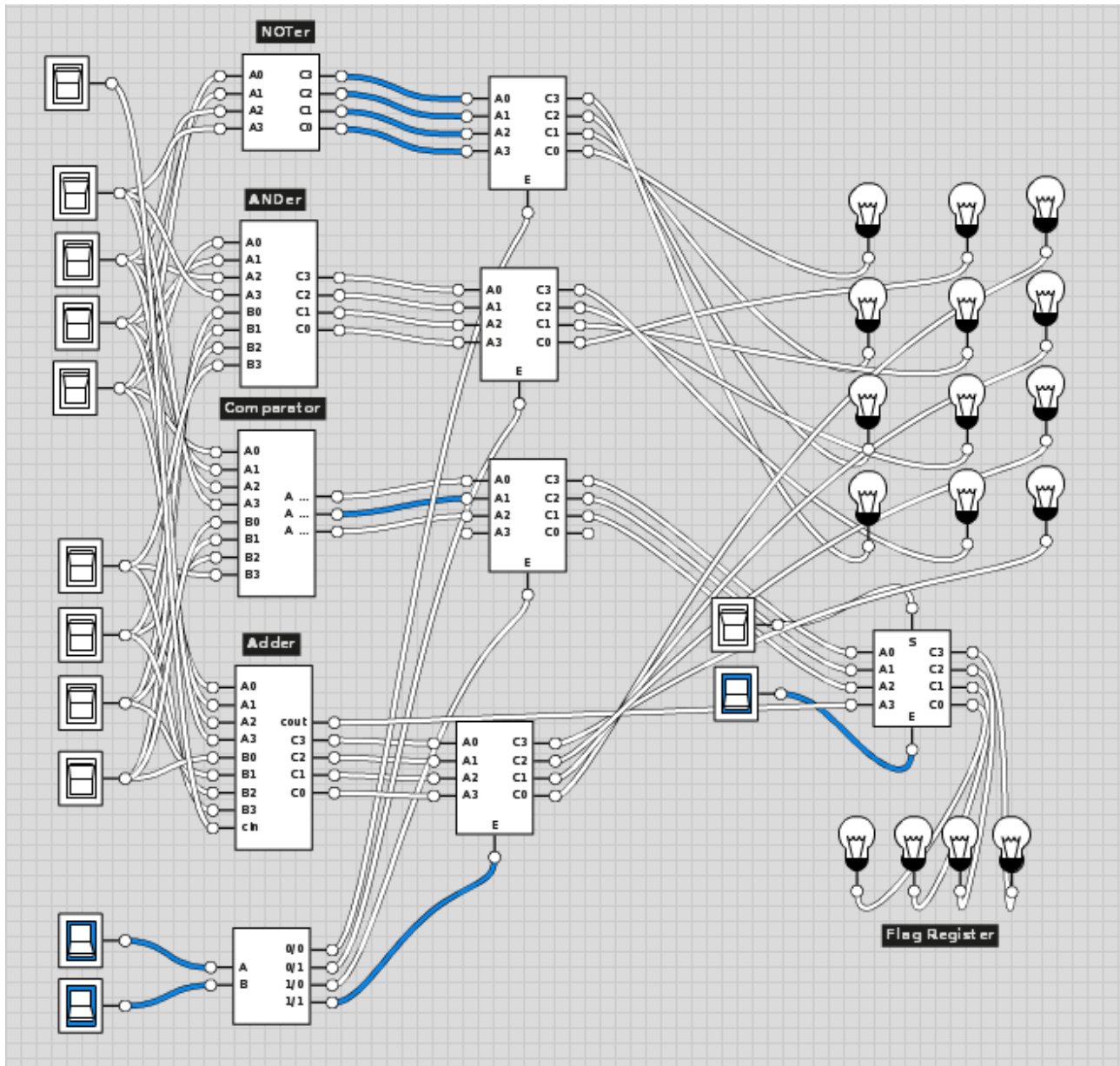
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About

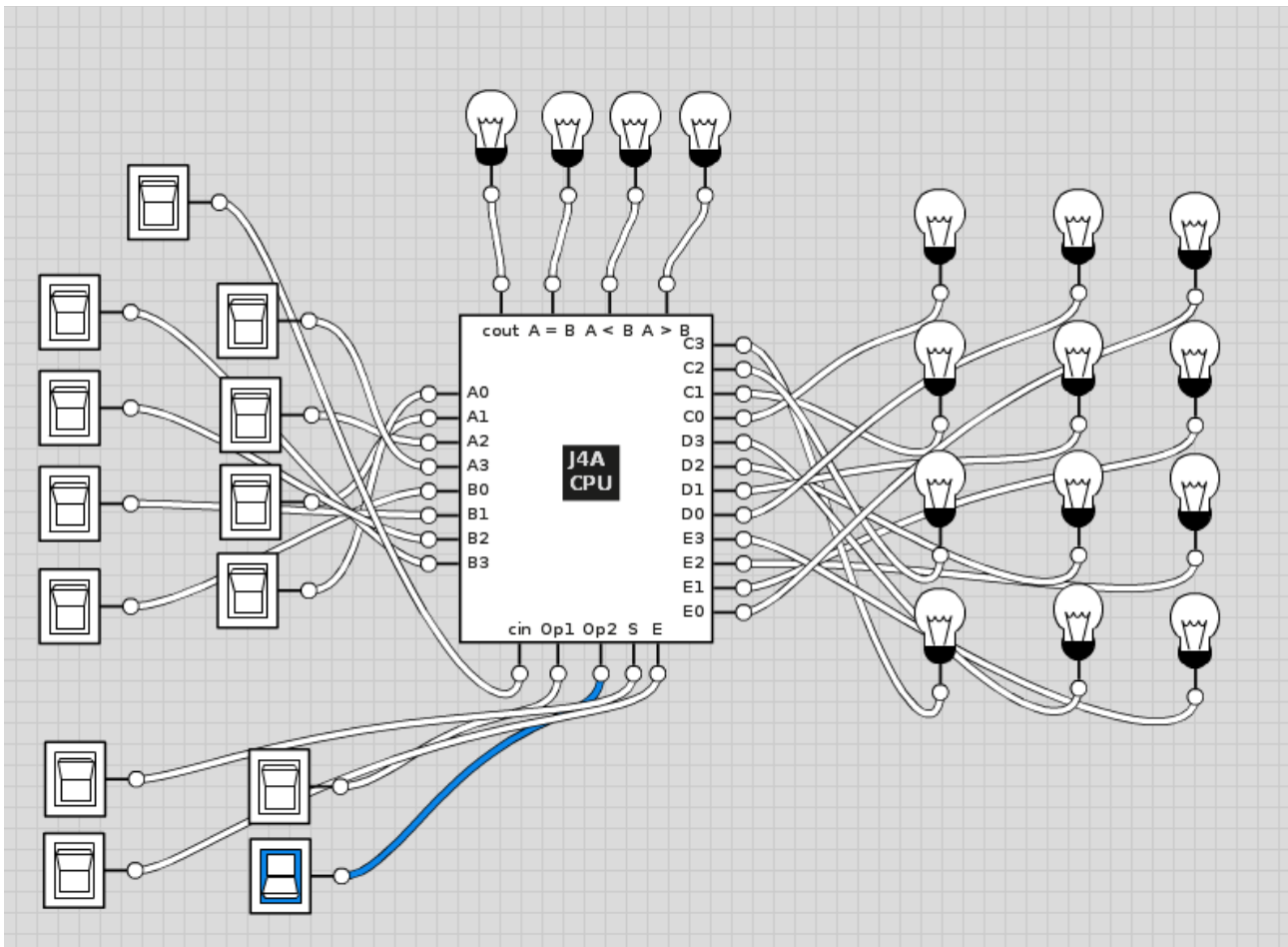
J4 family, is a family of computer processors, 4 bit RISC (Reduced Instruction Set Computer), and *J4A* is the first generation of this family.

These processors are designed and implemented by **Muhammadsreza Haghiri** in **Jabir Project**.

ALU Block Diagram:



ALU is made up of *ADD*, *CMP*, *NOT* and *AND* instructions. This little instruction set helps us understand J4 assembly better.



This is How we assemble a J4 system, we've used Logicy software for design and implementation of J4A.

Assembly

J4A has only 4 instructions, with four 2 bit operation codes :

Operation Code	Instruction
00	NOT
01	AND
10	CMP
11	ADD