



Runtime randomization and perturbation for virtual machines.

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Chapter 1

Background & State of the art

This chapter discusses state of the art in the areas of *WebAssembly* and *Software Diversification*. In Section 1.1 we discuss the WebAssembly language, its motivation, how WebAssembly binaries are generated, language specification, and security-related issues. In Section 1.2, we present a summary of Software Diversification, its foundational concepts and highlighted related works. We select the discussed works by their novelty, critical insights, and representativeness of their techniques. In Section 1.3, we finalize the chapter by stating our novel contributions and comparing them against state-of-the-art related works.

1.1 WebAssembly overview

Over the past decades, JavaScript has been used in the majority of the browser clients to allow client-side scripting. However, due to the complexity of this language and to gain in performance, several approaches appeared, supporting different languages in the browser. For example, Java applets were introduced on web pages late in the 90's, Microsoft made an attempt with ActiveX in 1996 and Adobe added ActionScript later on 1998. All these attempts failed to persist, mainly due to security issues and the lack of consensus on the community of browser vendors.

In 2014, Emscripten proposed with a strict subset of JavaScript, asm.js, to allow low level code such as C to be compiled to JavaScript itself. Asm.js was first implemented as an LLVM backend. This approach came with the benefits of having all the ahead-of-time optimizations from LLVM, gaining in performance on browser clients [31] compared to standard JavaScript code. The main reason why asm.js is faster, is that it limits the language features to those that can be optimized in the LLVM pipeline or those that can be directly translated from the source code. Besides, it removes the majority of the dynamic characteristics of the language, limiting it to numerical types, top-level functions, and one large array in the memory directly accessed as raw data. Since asm.js is a subset of JavaScript it

was compatible with all engines at that moment. Asm.js demonstrated that client-code could be improved with the right language design and standarization. The work of Van Es et al. [25] proposed to shrink JavaScript to asm.js in a source-to-source strategy, closing the cycle and extending the fact that asm.js was mainly a compilation target for C/C++ code. Despite encouraging results, JavaScript faces several limitations related to the characteristics of the language. For example, any JavaScript engine requires the parsing and the recompilation of the JavaScript code which implies significant overhead.

Following the asm.js initiative, the W3C publicly announced the WebAssembly (Wasm) language in 2015. WebAssembly is a binary instruction format for a stack-based virtual machine and was officially stated later by the work of Haas et al. [24] in 2017. The announcement of WebAssembly marked the first step of standarizing bytecode in the web environment. Wasm is designed to be fast, portable, self-contained and secure, and it outperforms asm.js [24]. Since 2017, the adoption of WebAssembly keeps growing. For example; Adobe, announced a full online version of Photoshop¹ written in WebAssembly; game companies moved their development from JavaScript to Wasm like is the case of a full Minecraft version²; and the case of Blazor³, a .Net virtual machine implemented in Wasm, able to execute C# code in the browser.

From source to Wasm

All WebAssembly programs are compiled ahead-of-time from source languages. LLVM includes Wasm as a backend since version 8.0.0, supporting a broad range of frontend languages such as C/C++, Rust, Go or AssemblyScript⁴. The resulting binary, works similarly to a traditional shared library, it includes instruction codes, symbols and exported functions. In Figure 1.1, we illustrate the workflow from the creation of Wasm binaries to their execution in the browser. The process starts by compiling the source code program to Wasm (Step ①). This step includes ahead-of-time optimizations. For example, if the Wasm binary is generated out of the LLVM pipeline, all optimizations in the LLVM

The step ② builds the standard library for Wasm usually as JavaScript code. This code includes the external functions that the Wasm binary needs for its execution inside the host engine. For example, the functions to interact with the DOM of the HTML page are imported in the Wasm binary during its call from the JavaScript code. The standard library can be manually written, however, compilers like Emscripten, Rust and Binaryen can generate it automatically, making this process completely transparent to developers.

¹<https://twitter.com/Adobe/status/1453034805004685313?s=20&t=Zf1N7-WmzecAOK4V8R69lw>

²<https://satoshinm.github.io/NetCraft/>

³<https://dotnet.microsoft.com/en-us/apps/aspnet/web-apps/blazor>

⁴subset of the TypeScript language

Finally, the third step (Step ③), includes the compilation and execution of the client-side code. Most of the browser engines compile either the Wasm and JavaScript codes to machine code. In the case of JavaScript, this process involves JIT and hot code replacement during runtime. For Wasm, since it is closer to machine code and it is already optimized, this process is a one-to-one mapping. For instance, in the case of V8, the compilation process only applies simple and fast optimizations such as constant folding and dead code removal. Once V8 completes the compilation process, the generated machine code for Wasm is final and is the same used along all its executions. This analysis was validated by conversations with the V8's dev team and by experimental studies in our previous contributions.

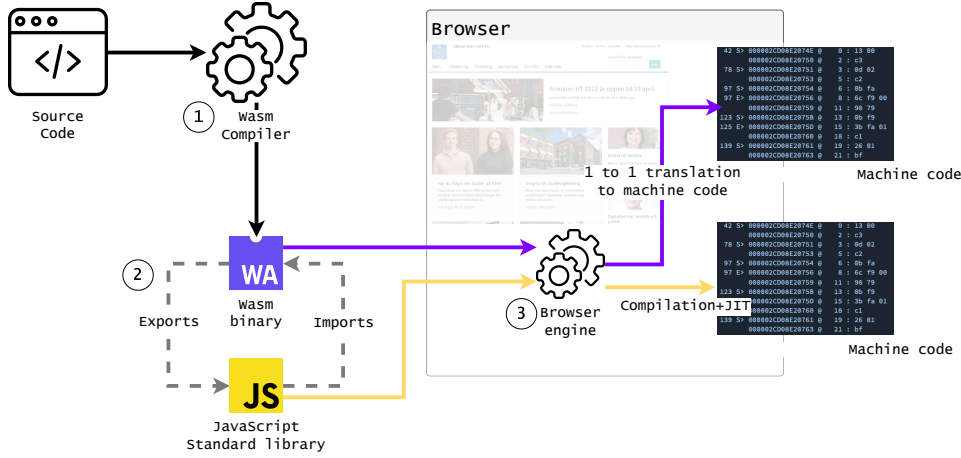


Figure 1.1: WebAssembly building, compilation in the host engine and execution.

Wasm can execute directly and is platform independent, making it useful for IoT and Edge computing [2, 11]. For instance, Cloudflare and Fastly adapted their platforms to provide Function as a Service (FaaS) directly with WebAssembly. In this case, the standard library, instead of JavaScript, is provided by any other language stack that the host environment supports. In 2019, the Bytecode Alliance⁵ proposed WebAssembly System Interface (WASI). WASI is the foundation to build Wasm code outside of the browser with a POSIX system interface platform. It standardizes the adoption of WebAssembly outside web browsers [13] in heterogeneous platforms.

WebAssembly specificities

WebAssembly defines its own Instruction Set Architecture (ISA) [21]. It is an abstraction close to machine code instructions but agnostic to CPU architectures. Thus, Wasm is platform independent. The ISA of Wasm includes also the necessary

⁵<https://bytecodealliance.org/>

components that the binary requires to run in any host engine. A Wasm binary has a unique module as its main component. A module is composed by sections, corresponding to 13 types, each of them with an explicit semantic and a specific order inside the module. This makes the compilation to machine code faster.

In Listing 1.1 and Listing 1.2 we illustrate a C program and its compilation to Wasm. The C function contains: heap allocation, external function declaration and the definition of a function with a loop, conditional branching, function calls and memory accesses. The code in Listing 1.2 is in the textual format for the generated Wasm. The module in this case first defines the signature of the functions (Line 2, Line 3 and Line 4) that help in the validation of the binary defining its parameter and result types. The information exchange between the host and the Wasm binary might be in two ways, exporting and importing functions, memory and globals to and from the host engine (Line 5, Line 35 and Line 36). The definition of the function (Line 6) and its body follows the last import declaration at Line 5.

The function body is composed by local variable declarations and typed instructions that are evaluated in a virtual stack (Line 7 to Line 32 in Listing 1.2). Each instruction reads its operands from the stack and pushes back the result. The result of a function call is the top value of the stack at the end of the execution. In the case of Listing 1.2, the result value of the main function is the calculation of the last instruction, `i32.add` at Line 32. A valid Wasm binary should have a valid stack structure that is verified during its translation to machine code. The stack validation is carried out using the static types of Wasm, `i32`, `i64`, `f32` and `f64`. As the listing shows, instructions are annotated with a numeric type.

Wasm manages the memory in a restricted way. A Wasm module has a linear memory component that is accessed as `i32` pointers and should be isolated from the virtual stack. The declaration of the linear data in the memory is showed in Line 37. The memory access is illustrated in Line 15. This memory is usually bound in browser engines to 2Gb of size, and it is only shareable between the process that instantiate the Wasm binary and the binary itself (explicitly declared in Line 33 and Line 36). Therefore, this ensures the isolation of the execution of Wasm code.

Wasm also provides global variables in their four primitive types. Global variables (Line 34) are only accessible by their declaration index, and it is not possible to dynamically address them. For functions, Wasm follows the same mechanism, either the functions are called by their index (Line 30) or using a static table of function declarations. This latter allows modeling dynamic calls of functions (through pointers) from languages such as C/C++; however, the compiler should populate the static table of functions.

In Wasm, all instructions are grouped into blocks, being the start of a function the root block. Two consecutive block declarations can be appreciated in Line 10 and Line 11 of Listing 1.2. Control flow structures jump between block boundaries and not to any position in the code like regular assembly code. A block may specify the state that the stack must have before its execution and the result stack value coming from its instructions. Inside the Wasm binary the blocks explicitly define where they start and end (Line 25 and Line 28). By design, each block executes

Listing 1.1: Example C function.

```
// Some raw data
const int A[250];

// Imported function
int ftoi(float a);

int main() {
    for(int i = 0; i < 250; i++) {
        if (A[i] > 100)
            return A[i] + ftoi(12.54);
    }

    return A[0];
}
```

Listing 1.2: WebAssembly code for Listing 1.1.

```
1 (module
2   (type (;0;) (func (param f32) (result i32)))
3   (type (;1;) (func))
4   (type (;2;) (func (result i32)))
5   (import "env" "ftoi" (func $ftoi (type 0)))
6   (func $main (type 2) (result i32)
7     (local i32 i32)
8     i32.const -1000
9     local.set 0 ;loop iteration counter;
10    block ;label = @1;
11      loop ;label = @2;
12        i32.const 0
13        local.get 0
14        i32.add
15        i32.load
16        local.tee 1
17        i32.const 101
18        i32.ge_s ;loop iteration condition;
19        br_if 1 ;@1;
20        local.get 0
21        i32.const 4
22        i32.add
23        local.tee 0
24        br_if 0 ;@2;
25      end
26      i32.const 0
27      return
28    end
29    f32.const 0x1.9147aep+3 ;=12.54;
30    call $ftoi
31    local.get 1
32    i32.add)
33 (memory (;0;) 1)
34 (global (;4;) i32 (i32.const 1000))
35 (export "memory" (memory 0))
36 (export "A" (global 2))
37 (data $data (0) "\00\00\00\00...")
38 )
```

independently and cannot execute or refer to outer block values. This is guaranteed by explicitly annotating the state of the stack before and after the block. Three instructions handle the navigation between blocks: unconditional break, conditional break (Line 19 and Line 24) and table break. Each break instruction can only jump to one of its enclosing blocks. For example, in Listing 1.2, Line 19 forces the execution to jump to the end of the first block at Line 10 if the value at the top of the stack is greater than zero.

We want to remark that the description of Wasm in this section follows the version 1.0 of the language and not its proposals for extended features. We follow those features implemented in the majority of the vendors according to the Wasm roadmap [22]. On the other hand we excluded instructions for datatype conversion, table accesses and the majority of the arithmetic instructions for the sake of simplicity.

WebAssembly’s security

As we described, WebAssembly is deterministic and well-typed, follows a structured control flow and explicitly separates its linear memory model, global variables and the execution stack. This design is robust [12] and makes easy for compilers and engines to sandbox the execution of Wasm binaries. Following the specification of Wasm for typing, memory, virtual stack and function calling, host environments should provide protection against data corruption, code injection, and return-oriented programming (ROP).

However, WebAssembly is vulnerable under certain conditions, at the execution engine’s level [17]. Implementations in both browsers and standalone runtimes [2] are vulnerable. Genkin et al. demonstrated that Wasm could be used to exfiltrate data using cache timing-side channels [19]. One of our previous contributions trigger a CVE⁶ on the code generation component of wasmtime, highlighting that even when the language specification is meant to be secure, the underlying host implementation might not be. Moreover, binaries itself can be vulnerable. The work of Lehmann et al. [9] proved that C/C++ source code vulnerabilities can propagate to Wasm such as overwriting constant data or manipulating the heap using stackoverflow. Even though these vulnerabilities need a specific standard library implementation to be exploited, they make a call for better defenses for WebAssembly. Several proposals for extending WebAssembly in the current roadmap could address some existing vulnerabilities. For example, having multiple memories could incorporate more than one memory, stack and global spaces, shrinking the attack surface. However, the implementation, adoption and settlement of the proposals are far from being a reality in all browser vendors.

1.2 Software Diversification

Software Diversification has been widely studied in the past decades. This section discusses its state of the art. Software diversification consists in synthesizing, reusing, distributing, and executing different, functionally equivalent programs. According to the survey of Baudry and Monperrus [30], the motivation for software diversification can be separated in five categories: reusability [47], software testing [40], performance [37], fault tolerance [56] and security [54]. Our work contributes to the latter two categories. In this section we discuss related works by highlighting how they generate diversification and how they use the generated diversification.

Artificial Software Diversity.

There are two primary sources of software diversification: Natural and Artificial Diversity [30]. This work contributes to the state of the art of Artificial Diversity,

⁶<https://www.fastly.com/blog/defense-in-depth-stopping-a-wasm-compiler-bug-before-it-became-a-problem>

which consists of artificially synthesizing software. We have found that the foundation for artificial software diversity has barely changed since Cohen in 1993 [54]. Therefore, the work of Cohen is the cornerstone of this dissertation. According to their seminal work, whatever two programs are equal if they are semantically equivalent, thus, one program can be considered a diversified version of the other. We use their definition on semantic/functional equivalence as input-output equivalence. Two programs are equivalent if, given identical input, they produce the identical output.

Cohen et al. proposed to generate artificial software diversification through mutation strategies. A mutation strategy is a set of rules to define how a specific component of software development should be changed to provide a different yet functionally equivalent program. Cohen et al. proposed 10 concrete transformation strategies that can be applied at coarse-grained or fine-grained scales. We summarize them, complemented with the work of Baudry and Monperrus [30] and the work of Jackson et al. [35], in 5 strategies.

(S1) *Equivalent instructions replacement* Pieces of programs can be replaced by semantically equivalent code such as equivalent arithmetic expressions, including the adding of garbage instructions, *i.e.*, instructions that do not affect the computation result. This strategy is simple but powerful since the complexity of program variants dramatically increases. In terms of overhead, the size of the program variant increases with the size of the replacement. Usually, the replacement rules are written by hand as code templates representing a piece of code and a valid replacement for it, similar to compiler optimization rules. For example, Jackson et al. [38] highlighted the usage of the optimization flags of several compilers to generate program variants. On the same topic, Cleemput et al. [36] and Homescu et al. [33] introduce the usage of inserting NOP instructions to generate statically different variants out of the compilation process.

Exhaustive Exploration is another approach to generate equivalent instructions. This technique is based on sampling or constructing all possible programs for a specific language. Once a program is found, it is checked for semantic equivalence against the original program, reporting it as a variant if is the case. Jacob et al. [42] proposed the technique called superdiversification for x86 binaries. They generate semantically equivalent transformations at basic block level that outperform transformations written by human experts. Similarly, Tsoupidi et al. [8] introduced Diversity by Construction, a constraint-based compiler to generate software diversity for MIPS32 architecture. Their technique relies in using a constraint solver to generate program variants that by construction are semantically equivalent. Compared to other techniques, the works of Jacob et al. and Tsoupidi et al. do not need the writing of transformation strategies by hand, but they are limited by the reach of theorem solvers and the generation of variants can only be static.

(S2) *Instruction reordering* This strategy reorders instructions or entire program blocks if they are independent. The location of variable declarations might change

as well if compilers resort them in the symbol tables. It prevents static examination and analysis of parameters and alters memory locations. The strategy should not affect the size of program variants neither their execution time. In this field, Bhatkar et al. [51, 48] proposed the random permutation of the order of variables and routines for ELF binaries.

(S3) *Adding, changing, removing jumps and calls* This strategy creates program variants by adding, changing, or removing jumps and calls in the original program. Cohen [54] mainly illustrated the case by inserting bogus jumps in programs. Pettis and Hansen [55] proposed to split basic blocks and functions for the PA-RISC architecture, inserting jumps between splits. Similarly, Crane et al. [29] de-inline basic blocks of code as an LLVM pass. In their approach, each de-inlined code is transformed into semantically equivalent functions that are randomly selected at runtime to replace the original code calculation. On the same topic, Bhatkar et al. [48] extended their previous approach [51], replacing function calls by indirect pointer calls in C source code, allowing post binary reordering of function calls. Recently, Romano et al. [1] proposed an obfuscation technique for JavaScript in which part of the code is replaced by calls to complementary Wasm function.

(S4) *Program memory and stack randomization* This strategy changes the layout of programs in the host memory. Also, it can randomize how a program variant operates its memory. The work of Bhatkar et al. [51, 48] also proposed to randomize the base addresses of applications and the library memory regions, and the random introduction of gaps between memory objects in ELF binaries. Tadesse Aga and Autin [16] and Lee et al. [3] recently proposed a technique to randomize the local stack organization for function calls using a custom LLVM compiler. Younan et al. [45] proposed to separate a conventional stack into multiple stacks where each stack contains a particular class of data. On the same topic, Xu et al. [7] transform programs to reduce memory exposure time, improving the time needed for frequent memory address randomization.

(S5) *ISA randomization and simulation* This strategy encodes the original program binary. Once encoded, the program can be decoded only once at the target client, or it can be interpreted in the encoded form using a custom virtual machine implementation. This technique is strong against attacks involving the examination of code. It does not affect the size of program variants or their execution times. Kc et al. [50] and Barrantes et al. [52] proposed seminal works on instruction-set randomization to create a unique mapping between artificial CPU instructions and real ones. On the same topic, Chew and Song [53] target operating system randomization. They randomize the interface between the operating system and the user applications. Couroussé et al. [26] implement an assembly-like DSL to generate equivalent code at runtime in order to increase protection against side-channel attacks. Their technique generates a different program during execution using an interpreter for their DSL.

The mentioned techniques can be applied at any layer of the software lifecycle, at coarse-grained or fine-grained levels. At high-level, Harrand et al. [10], propose to merge several Java decompiler variants to provide an extended and improved meta-decompiler. On the same topic, Sengupta et al. [23] shift several database engines and backends for web applications. Their idea makes known CVEs to be available only in certain time window, making potential attackers to not always success. Moreover, Roi et al. [14] proposed to use several machine learning algorithms with the same task to tackle adversarial attackers, using a different algorithm every time the system is queried. These works used preexisting software diversity to provide improved systems, both for reliability and security.

For fine-grained diversification, the before mentioned mutation strategies can be applied directly to the basecode at the instruction level, during the compilation of programs or directly to the generated binaries. As we previously mentioned Homescu et al. [33], Jackson et al. [35, 38], Jacob et al. [42], Crane et al. [29], Aga et al. [16] and Tsoupidi et al. [8] placed compilers in their diversification techniques. Similarly, Bathkar et al. [51, 48], Chew and Song [53], El-Khalil and Keromytis [49], and Cohen [54] itself mostly proposed binary to binary transformations. We have observed that fine-grained techniques are mainly motivated because they provide more robust diversification in terms of preservation. For example, to apply diversification techniques closer to the final execution avoid removing of code transformations by later optimization or compilation stages. Our contributions are fine-grained based.

Usages of Software Diversity

After program variants are generated, they can be used in two main scenarios: Randomization or Multivariant Execution(MVE) [35]. In Figure 1.2a and Figure 1.2b we illustrate both scenarios.

(U1) *Randomization*: In the first scenario, a program is selected from the collection of variants (program’s variant pool) and at each deployment it is assigned to a random client. This strategy prevents the usage of one variant to exploit another clients. When clients run two program variants, a potential attacker must create one attack for each variant. Therefore, the attacker must spend more time and effort to get the same return on reward for an attack. Jackson et al. [35] call this a herd immunity, in their work they demonstrated that this usage can prevent against ROP and JIT-ROP attacks [?]. Similarly, Amarilli et al. [39] drastically increase the number of execution traces required by a side-channel attack

(U2) *Multivariant Execution(MVE)*: In the second scenario, multiple program variants are composed in one single binary (multivariant binary) that is randomly deployed to a client. Once in the client, the multivariant binary executes program variants during runtime, either in parallel to check for inconsistencies or a single program to randomize execution paths [51]. In 2006, security researchers at the

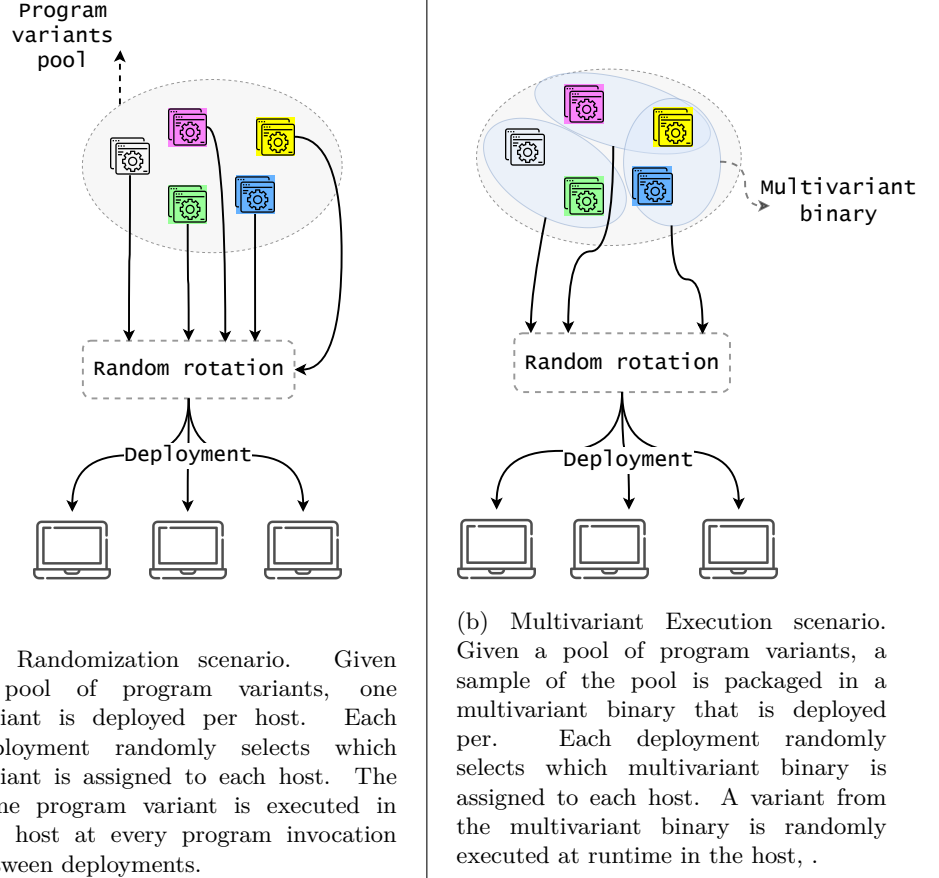


Figure 1.2: Software Diversification usages.

University of Virginia laid the foundations of a novel approach to security that consists in executing multiple variants of the same program, [46]. Bruschi et al. [44] extended the idea of executing the variants in parallel. Simultaneously, Salamat et al. [43] created a C compiler and modified a standard library that generates 32-bit Intel variants where the stack grows in the opposite direction. Their approach prevents against memory corruption.

Neatly exploiting the limit case of executing only two variants has demonstrated to harden systems against attackers [41, 34, 27, 18]. Notably, Davi et al. proposed Isomeron [28], an approach for execution-path randomization. Isomeron simultaneously loads the original program and a variant. While the program is running, Isomeron continuously flips a coin to decide which copy of the program should be executed next at the level of function calls. With Davi et al. approach, a potential attacker cannot predict whether the original or the variant of a program

will execute. On the same topic, Agosta et al. [32] and Crane et al. [29] used more than two generated programs, to randomize software control flow at runtime, trackling power side-channels.

1.3 Statement of Novelty

We contribute to Software Diversification for WebAssembly using Artificial Diversification, for Randomization and Multivariant Execution usages (U1, U2). In Table 1.1 we listed related work on Artificial Software Diversification that support our work. The table is composed by the authors and the reference to their work, followed by one column for each strategy and usage (S1, S2, S3, S4, S5, U1 and U2). The last column of the table summarize their technical contribution and the reach of their work. Each cell in the table contains a checkmark if the strategy or the usage of the work match the previously mentioned classifications. The rows are sorted by the year of the work in ascending order. The last two rows locate our contributions.

WebAssembly is a novel technology, and the adoption of defenses for it is still under development [2, 4]. The current limitations on security and the lack of software diversity approaches for WebAssembly motivate our work. Our first contribution, CROW [6] generates multiple program variants for WebAssembly using the LLVM pipeline. It contributes to state of the art in artificially creating randomization for WebAssembly (U1). Because of the specificities of code execution in the browser (mentioned in Section 1.1), this can be considered a randomization approach. For example, since WebAssembly is served at each page refreshment, every time a user asks for a WebAssembly binary, she can be served a different variant provided by CROW. Notably, researching on MVE in a distributed setting like the Edge [?] has been less researched. With MEWE [5], our second contribution, we randomly select from several variants at runtime, creating a multivariant execution scheme(U2) that randomizes the observable behaviors at each run of the program.

Conclusions

In this chapter, we presented the background on the WebAssembly language, including its security issues and related work. This chapter aims to settle down the foundation to study automatic diversification for WebAssembly. We highlighted related work on Artificial Software Diversification, showing that it has been widely researched, not being the case for WebAssembly. We placed our contributions in the field of artificial diversity. In ?? we describe the technical details that lead our contributions. The results of our contributions are obtained by following the methodology described in ??.

Authors	S1	S2	S3	S4	S5	U1	U2	Main technical contribution
Pettis and Hansen [55]		✓		✓		✓		Custom Pascal compiler for PA-RISC architecture
Chew and Song [53]			✓			✓		Linux Kernel recompilation.
Kc et al. [50]					✓			Linux Kernel recompilation.
Barrantes et al. [52]					✓	✓		x86 to x86 transformations using Valgrind
Bhatkar et al. [51]	✓	✓		✓		✓		ELF binary transformations
El-Khalil and Keromytis [49]						✓		custom GCC compiler for x86 architecture
Bhatkar et al. [48]	✓	✓		✓		✓		C/C++ source to source transformations and ELF binary transformations
Younan et al. [45]				✓				custom GCC compiler
Bruschi et al. [44]				✓		✓		ELF binary transformations.
Salamat et al. [43]			✓				✓	Custom GNU compiler
Jacob et al. [42]	✓	✓						x86 to x86 transformations
Salamat et al. [41]				✓			✓	x86 to x86 transformations
Amarilli et al. [39]	✓				✓	✓		Polymorphic code generator for ARM architecture
Jackson [35]	✓					✓	✓	LLVM compiler, only backend for x86 architecture
Cleemput et al. [49]	✓					✓		x86 to x86 transformations
Homescu et al. [33]	✓					✓		LLVM 3.1.0 [†]
Crane et al. [29]	✓	✓	✓				✓	LLVM, only backend for x86 architecture
Davi et al. [28]						✓		Windows DLL instrumentation
Couroussé et al. [26]	✓	✓			✓	✓		Custom GCC compiler targeting micro-controllers
Lu et al. [18]				✓			✓	GNU assembler for Linux kernel
Belleville et al. [20]	✓			✓		✓		Only C language frontend, LLVM 3.8.0 [†]
Aga et al. [16]				✓		✓		Data layout randomization, LLVM 3.9 [†]
Österlund et al. [15]				✓			✓	Linux Kernel recompilation.
Xu et al. [7]				✓		✓		Custom kernel module in Linux OS
Lee et al. [3]				✓		✓		LLVM 12.0.0 backend for x86
Cabrera Arteaga et al. [6]	✓	✓	✓	✓		✓		Any frontend language for LLVM version 12.0.0 targeting Wasm backend
Cabrera Arteaga et al. [5]	✓	✓	✓	✓			✓	Any frontend and backend language for LLVM version 12.0.0

[†] Notice that LLVM only supports WebAssembly backend from version 8.0.0

Table 1.1: The table is composed by the authors and the reference to their work, followed by one column for each strategy and usage (S1, S2, S3, S4, S5, U1 and U2). The last column of the table summarize their technical contribution. Each cell in the table contains a checkmark if the strategy or the usage of the work match the previously mentioned classifications. The rows are sorted by the year of the work in ascending order. The last two rows locate our contributions.

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