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|--------------------------------------------------|----------------------------|----------------------------|
|                                                  |                            |                            |
| <b>Planning</b>                                  | <i>October 17th - 21st</i> | <i>October 24th - 28st</i> |
| <i>Specification Document</i>                    |                            |                            |
| <i>Specification Revision</i>                    |                            |                            |
| <i>Design Document</i>                           |                            |                            |
| <i>Wire Circuit Components</i>                   |                            |                            |
| <i>Design Enclosure</i>                          |                            |                            |
| <i>Code LCD</i>                                  |                            |                            |
| <i>Revise Previous Libraries For Components</i>  |                            |                            |
| <i>Complete Must-Do Requirments</i>              |                            |                            |
| <i>Encorporate Major &amp; Minor Requirments</i> |                            |                            |
| <i>Debugging &amp; Final Revision</i>            |                            |                            |
| <i>Project Demonstration &amp; Validation</i>    |                            |                            |
| <i>Final Revised Design Document</i>             |                            |                            |

## Final Project Timeline

| October 31st - November 4th | November 7th - 11th | November 14th - 18th | November 21st - 25th |
|-----------------------------|---------------------|----------------------|----------------------|
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