Yevhenii Oleksandrovych Horbul
Junior Software Engineer

Executive Summary

Junior Software Engineer with experience in software engineering for embedded systems, informational and control systems. Proactive and self-motivated. Goal-oriented and quick learning ability.

- Overall 2 years of work experience in full life cicle developing informational and control embedded systems (software and hardware).
- Experience in developing medical systems.
- Mentoring skills: overall 2 years of work experience as teacher of robotics for children, overall 5 years of work experience as coach of the taekwondo.

Technical Skills

Skill	Skill Sub Category
C	Embedded
Embedded	1Wire, ARM, AVR, CAN, FAT, FAT32, GPRS, GSM, I2C, MOTOR CONTROL, SD/MMC, SPI, TCP/IP, UART
Embedded security	Embedded security
Hardware	PCB Design
Hardware Development	Hardware Development
Java	Core Java
Linux Kernel	Linux Kernel

Foreign Language Skills

Language	Written	Spoken
German	Intermediate	Intermediate
English	Basic	Basic

Education

Institution:	NTUU KPI (National Technical University of Ukraine Kiev Polytechnic Institute), Ukraine
Dates Attended:	September 2013 - December 2018
Qualification(s):	Master Degree
Major Subject(s):	Telecommunications and Radio Engineering

Professional Experience

Company:	UKRAVTOSERVICE
Duration:	Dec 2017 - Mar 2019 (about 1 year)
Role:	Design Engineer (Electronic)
Project Description:	Development and implementation of solutions for security and dispatching systems. This systems via GSM-module use mobile network for calls to dispatchers and communication from server via TCP/IP. Devices can get information about sensors state and power state, switch power for other devices (for example sirens), read RFID-keys, send SMS, make outcoming calls and accept incoming calls.

Responsibilities:	- Hardware development(circuit diagrams development, PCB)
	- Software development for microcontrollers (C language)
	- Protocol stack development
	- Circuit modeling
	- Prototyping and testing system
Technologies:	C, MPLAB X, AVR Studio, System Work Bench for STM32, DipTrace, Proteus, TCP/IP, GSM, hardware interfaces(SPI, I2C, Wiegand, UART, CAN)

Company:	GEOS Electronics, Kiev, Ukraine
Duration:	Dec 2016 - Dec 2017 (about 1 year)
Role:	Embedded engineer
Project Description:	Development and implementation of solutions for smart home and industrial automation. This project include next devices: winding machine, supply blocks tester, soldering station, controllers of boilers, controller ACS, metal detector.
Responsibilities:	- Hardware development(circuit diagrams development, PCB).
	- Software development for microcontrollers (C language)
	- Protocol stack development
	- Circuit modeling
	- Prototyping and testing system
Technologies:	C, MPLAB X, hardware interfaces (SPI, I2C, OneWire, Wiegand, UART), hardware (displays, indicators, motors, heaters, relays)