AAA: High Agile Adaptive Application-awareness Network for SDN

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Abstract—**The abstract goes here.**

1. Introduction

With the data traffic and network scale rapidly increaing, there exists huge demand for scalable network management. Meanwhile, network monitoring and application awareness play a increasingly critical role in Quality of Service(Qos),Traffic Engineering(TE) and cyber security. Briefly, application awareness is a basic technology to enhance automation and intelligence of the network. It is divided into two processes: packet acquisition and traffic identification. Packet acquisition refers to capturing packets from switches through a mechanism or an algorithm. Traffic identification refers to parsing the five-tuple information of packets from different layer according to OSI model ,then recognizing the application layer protocol with the help of DPI tools. Application-aware network can improve the visibility of itself , promote integration of different business and eliminate faults quickly .However, the application awareness need integrate the high precision,high efficiency with real time, which is still a challange owing to the volume and variety of data in the large-scale network.

Software-defined network (SDN) is a new technical architecture which decouple the network control plane from the data-forwarding plane. It advocates building an open and programmable network to provide flexible, central controlled(or centralized) and globally visible network services, through which SDN can facilitate the operation and maintenance of the data center(DC) network. In a software-defined network, packet acquisition depends on OpenFlow(OF) protocol,which is varied from the Netflow and Sflow used in traditional networks.

Based on port, payload, and traffic behavior characteristics, DPI can identify a variety of information including the application layer protocol of a data flow, and be applied in application-aware network. In traditional networks, DPI devices are bound to the data plane, which makes it impossible to visualize global fine-grained traffic in real time. Therefore,many people are concerned about the research and optimization of the combination of SDN and application awareness. However, most of the current solutions are to deploy DPI in the SDN controller.In this case, parsing each single packet will be computationally heavy for controller. In addition, network scale, number of sampling nodes, sampling frequency and repetition rate of packet all increase

performance consumption of controllers.On the other hand, in order to improve the accuracy of application recognition, the system must be able to capture continuous packets of the same flow regarding to the characteristics of DPI. To solve the above problems, a agile, adaptive and cooperative sampling mechanism which can be applied to large-scale data center network is urgently needed.
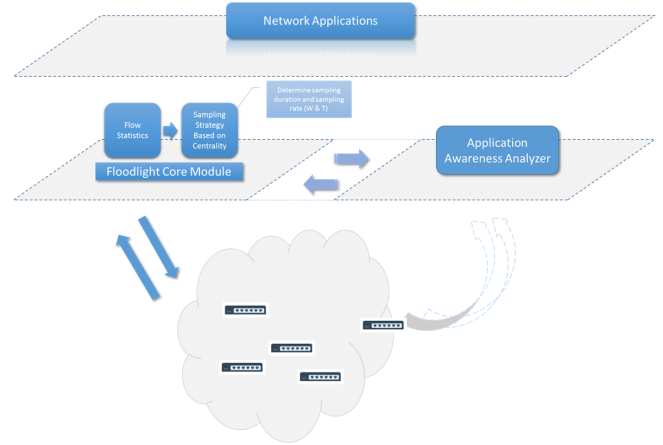

Figure 1: The System Architecture Of AAA

2. Related Work

- A detailed introduction to the background: Why build an application-aware network - the contrast between traditional and SDN networks.
- Introducting the problems existing in building a large-scale application-aware network (a reasonable collection of traffic in a large-scale network environment is a basic problem in the field of traffic monitoring and traffic engineering).
- And including the solutions already on the basic problem, and the various problems that exist.
- Introducting the Intermediary centrality algorithm.
- Outline the algorithms and strategies we use.

3. System Model and Design

3.1. Overview of Model

After selecting the nodes with high influence and covering all the traffic through the algorithms in 3.1 and 3.2, and determining the sampling duration and sampling rate of each switch, the controller will send the sampling instruction, and the switch will follow the set Time to sample. However, in addition to the previous two steps, the sampling strategy includes the final collaborative strategy, that is, the relationship between the traffic and the topology between the switches, and the network is coordinated with each other in a certain time or space. The flow in the stream is accurate and efficient (reducing the meaningless repeating packets). We present a variety of collaborative strategies, and finally compare their accuracy in sampling, packet repetition rate, and friendliness to the collector.

This is a simple sampling method. When the sampling point and sampling rate are determined, the controller unifies the sampling instruction and the sampling instruction to stop sampling. That is to say, in this mode, each sampling point is performed at the same pace and without additional complicated control. Shows in Figure.**??**.

Because R is the same, they can sample or stop at the same rhythm.

$$\max \quad \sum_i^n \left(\alpha \cdot \left(\frac{v_i \cdot \left| \widetilde{S}_i \right|}{|F^c|} \right) \right) + \frac{\left| \widetilde{S}_i \right|}{T/t} \cdot (\beta \cdot S_i + \gamma \cdot H_i)$$
$$- \frac{\alpha}{|F^c|} \cdot \sum_{f_k \in F^c} \sum_{l=1}^{T/t} \left(P \left\{ N_p^k(t) > 0 \right\} \cdot \psi \left(f_k, s^l \right) \right)$$
$$\tag{1}$$

subject to:

$$v_i = \sum_{f_k \in F^C} P \left\{ N_p^k(t) > 0 \right\} \tag{2}$$

$$U = \left\{ R_i, f_k \in F_i^c \wedge s^l \in \widetilde{S}_i \right\} \tag{3}$$

$$\psi \left(f_k, s^l \right) = \begin{cases} |U| - 1, & |U| \geq 1 \\ 0, & |U| = 0 \end{cases} \tag{4}$$

$$\alpha + \beta + \gamma = 1 \tag{5}$$

$$\sum_i^n f(\widetilde{S}_i) \leq K, \quad f(\widetilde{S}_i) = \begin{cases} 1, & \left| \widetilde{S}_i \right| \geq 1 \\ 0, & \left| \widetilde{S}_i \right| = 0 \end{cases} \tag{6}$$

$$\sum_i^n w_i \cdot \varphi(\widetilde{S}_i, s^l) \leq C \cdot t, \forall s^l \wedge \varphi(\widetilde{S}_i, s^l) = \begin{cases} 0, & s^l \notin \widetilde{S}_i \\ t, & s^l \in \widetilde{S}_i \end{cases} \tag{7}$$

$$l \leq T/t, \forall s^l \wedge t \leq T \tag{8}$$

$$K \leq n, \forall R_i \tag{9}$$

$$\left| \widetilde{S}_i \right| \leq T/t, \forall i \tag{10}$$

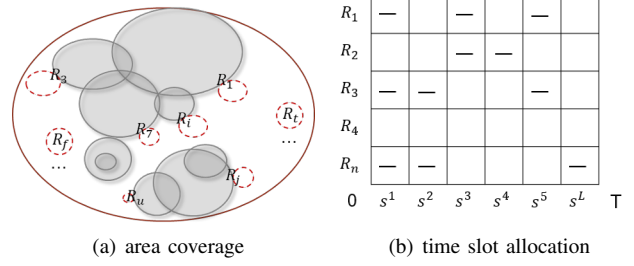(a) area coverage   (b) time slot allocation

Figure 2: overview of model

3.2. Sampling Point Selection

In large-scale DC networks, while all the switches sample flows, the excessive frequency of sending flow tables and group tables will cause extra overhead for SDN controller. Therefore, we need a strategy that can select a small number of most influential sampling nodes so that the number of flows sampled by these nodes is close to or equal to the global nodes. We may find the situationis is very similar to the graph theory. In a subnet topology, based on the concept of graph theory, we regard switches as nodes and regard flow paths as edges. Therefore, we define a flow information matrix. Then we calculate betweenness centrality and the most influential nodes has the maximal betweenness centrality. As show in Algorithm 1, the strategy is called sampling node election algorithm based on betweenness centrality.

The principle of the algorithm will be stated next, with respect to the notations in Table I being used throughout the paper.

Firstly, initialize the matrix M=$[m_{ij}]$ and the betweenness centrality c_j. Fig.3-a shows a subnet topology, where there are 6 switch nodes and 6 flows. And we define: if f_i passes through sw_j, the m_{ij}=1, otherwise m_{ij}=0. After initialization,as Fig.3-b shows,we get a $I * J$ two-dimension matrix. Then calculate c_j and c_{max}.

$$c_{max} = \max\{c_j \mid c_j = \sum_i m_{ij}, i \in [1, I], j \in [1, J] \wedge i, j \in Z\} \tag{11}$$

$$D_i^k = \left| F_i - \bigcup_c^{k-1} \widetilde{F_c} \right| / \left| F - \bigcup_c^{k-1} \widetilde{F_c} \right| \tag{12}$$

$$H_i = TF_i/TF \tag{13}$$

$$S_i = C_i / \sum_i^n C_i \tag{14}$$

$$I_i^k = \alpha \cdot D_i^k + \beta \cdot S_i + \gamma \cdot H_i \tag{15}$$

Senondly, elect the node with highest betweenness centrality as the sampling node and change m_{ij} until each m_{ij} = 0. As shown in Fig.4, $c_{max} = c_3$. Hence,the sw_3 is the first sampling node. Owing to f_1, f_2, f_4, f_6 pass through the sw_3, make $m_{ij} = 0 (i=1,2,4,6, j \in [1, J])$. Then we can get a new marrix M and calculate new c_j and c_{max} used for the next election. Repeating the above method, and electing the sampling node sw_4. Finally,we get $S=sw_3, sw_4$,when each m_{ij}=0.

TABLE 1: table

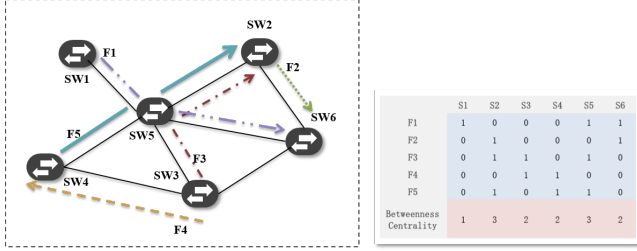| Nonation	Explanation
M	the current flow information matrix
S	selected switches set
sw_j	the j-th switch
f_i	the i-th flow
m_{ij}	the each value for f_i and sw_j in M,either 1 or 0
c_j	the betweenness centrality of sw_j
c_{max}	the max betweenness centrality of sw_j
I	the current number of flows
J	the current number of switches

Figure 3: Intermediary center based on the number of streams
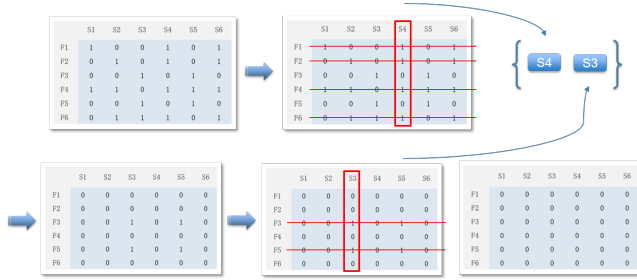

Figure 4: Sampling Point Selection

3.3. Allocation of Time Slot

3.4. Order of Time Slot

4. Experiments and results

- Schematic diagram of strategy
- Algorithm
- Union/Find Grouping

5. Conclusion

- Lab environment

TABLE 2: 110 Switches & 22 Hosts & 1400 Flows Comparison In Real Topology

Strategy	Captured	Sampling Accuracy	Repeat Rate
Single Time Sampling	1069	0.764	36.05%
Even-Divsion Time Sampling	1094	0.7814	12.86%
One More Back-up Sw	1152	0.7979	15.78%

Algorithm 1 Sampling Point Selection

Input:
  The set of routers: R
  The size of node will be selected: K
  The current flow information matrix: M
1: define $R^s = \{\}$ // The Set of Selected Routers
2: **for** $k = 1$; $k < K$; $k + +$ **do**
3:   **for** each $R_i \in R - R^s$ **do**
4:     **if** $I_i^k > max$ **then**
5:       $max = I_i^k$
6:       $SR = R_i$
7:     **end if**
8:   **end for**
9:   put SR to R^s
10:   mark SR as R_k in R^s
11: **end for**
12: **return** R^s

Algorithm 2 Allocation of Time Slot Based on XXX

Input: K ,L ,C , weight $W = \{w_1, w_2..., w_k\}$,value $V = \{v_1, v_2..., v_k\}$
1: **for** $i \leftarrow 1$ to K **do**
2:   **for** $j \leftarrow 1$ to C **do**
3:     **if** $w_i \leq j$ **then**
4:       $F_{i,j} \leftarrow F_{i-1,j}$
5:     **else**
6:       $M \leftarrow \min\{L, j/w_i\}$
7:       **for** $n_i \leftarrow 1$ to M **do**
8:         $F_{i,j-1} \leftarrow \max\{F_{i-1,j}, F_{i-1,j-w_i \cdot n_i} + v_i \cdot n_i\}$
9:       **end for**
10:     **end if**
11:   **end for**
12: **end for**
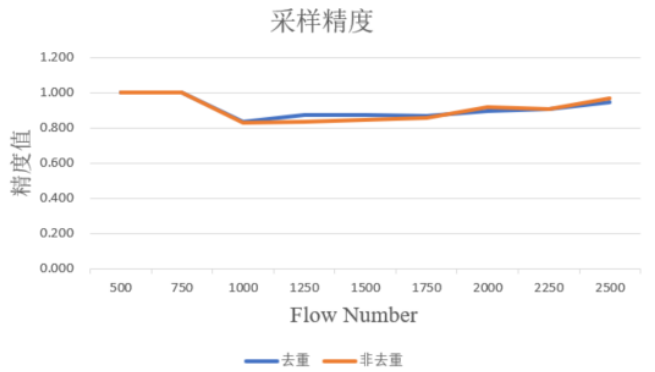13: **return** reverse iteration,output $N = \{n_1, n_2..., n_k\}$

Figure 5: The definition of agile application-aware network

- Sampling accuracy comparison
- Sampling repetition rate comparison
- Greedy centrality algorithm experimental results
- Deduplication rate algorithm comparison
- Experimental comparison of adaptive co-sampling

Algorithm 3 Order of Time Slot Based on XXXXX

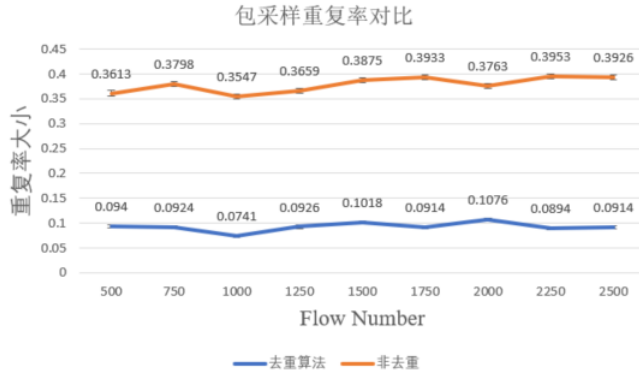**Input:** M , S , c_j

1: **return** S

Figure 6: The definition of agile application-aware network

algorithm

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