

Design Assignment 4

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Primary Github address: <https://github.com/JackOfSpades-7/UNLV-Embedded-Systems>

Directory: <https://github.com/JackOfSpades-7/UNLV-Embedded-Systems/tree/main>

Video Playlist:

<https://www.youtube.com/playlist?list=PLoASw0sToF2VSxvcRVV4onrNnoOzf-Dnl>

Submit the following for all Labs:

1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.
2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

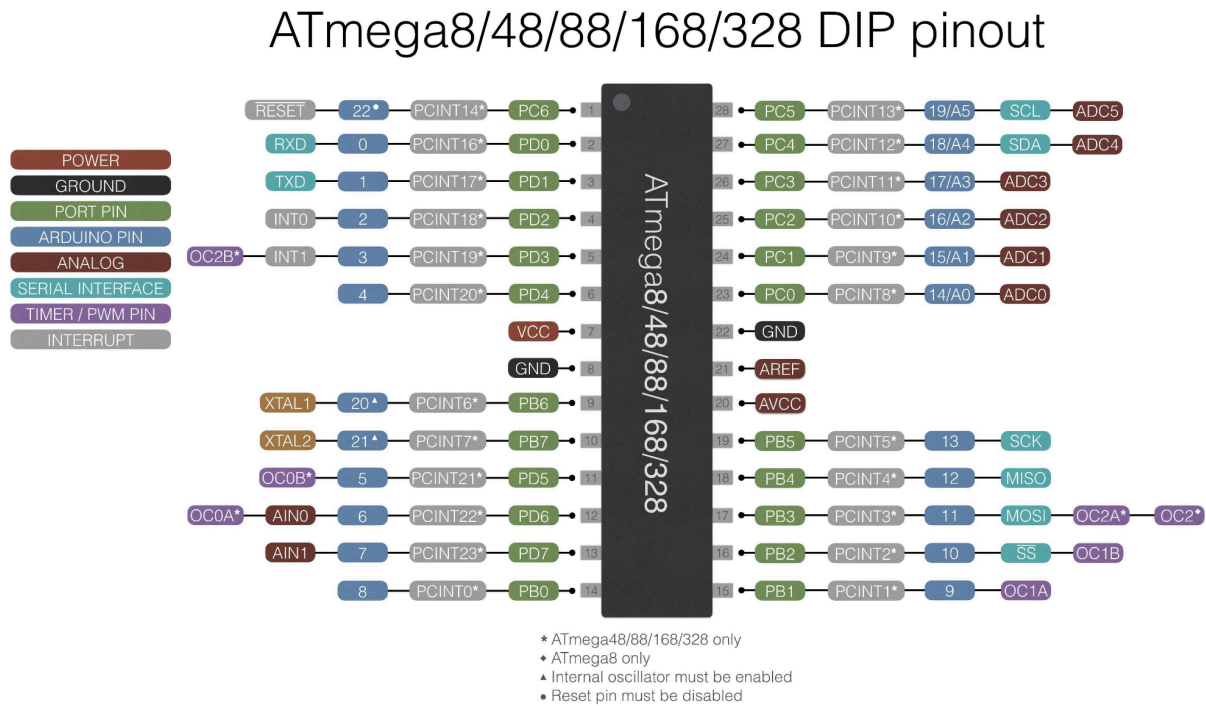
List of Components used

Block diagram with pins used in the Atmega328PB (only)

- Atmega328PB Xplained mini microcontroller board
- Arduino compatible external multifunction development shield
- Male-to-male jumper cables
- Logic analyzer
- Female-to-female ribbon cable
- PC

Block diagrams and pins:

Atmega328PB Micro controller:



Arduino compatible multifunction development shield:


```

char out_str[51];

int main (void)
{
    memset(out_str, ' ', 49);
    out_str[50] = '\0';
    ADC_Init(); // Initialize ADC
    DDRB = 0xFF; // Portb as outputs
    sei(); // Enable Global interrupts

    USART_Init(MYUBRR); //Initializing USART

    while (1) {
        ADCSRA |= (1<<6); // start conversion
        transmit_str("* \n"); // Print star to the serial terminal
        _delay_ms(100); // Delay 0.1s before printing again
    }

    return 0;
}

ISR (TIMER1_OVF_vect)
{
    ADCSRA |= (1 << ADSC);
    while((ADCSRA&(1<<ADIF))==0);

    ADCSRA |= (1<<ADIF);

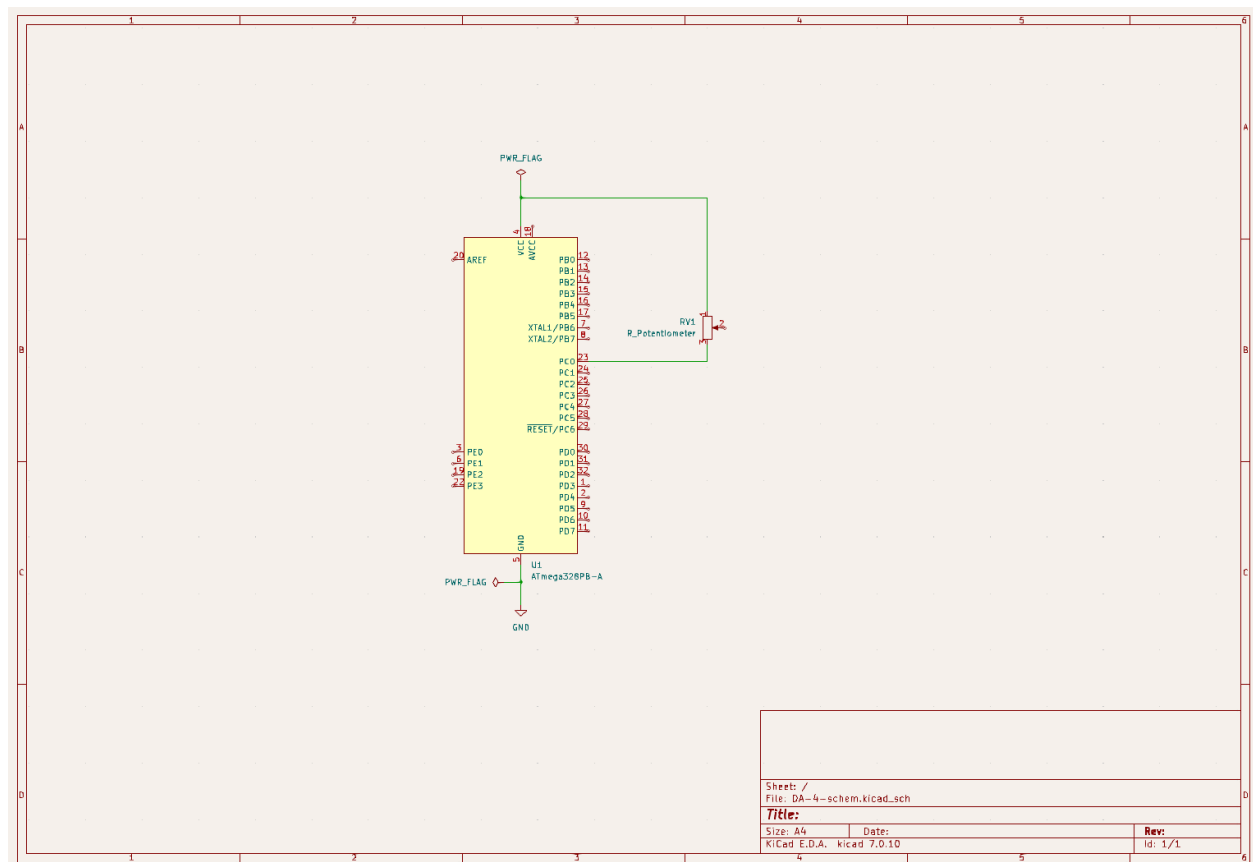
    int a = ADCL;
    a = a | (ADCH<<8);
    int8_t index = a/20.48;
    out_str[index] = ' ';
    TCNT1 = 49911;
}

void ADC_Init() {
    ADMUX = 0b01100000; //AVCC reference voltage (5V), ADC0 input, result left adjusted
    ADCSRA = 0b10001111; //ADC enabled, interrupt enabled, prescaler 128
    ADCSRB = 0x00; //free running mode
}

ISR(ADC_vect) //ADC conversion complete interrupt
{
    adcResult = ADCH; // outputting to 8 LEDS so only taking top 8 bits
    PORTB = adcResult; // outputting ADC result to LEDS
}

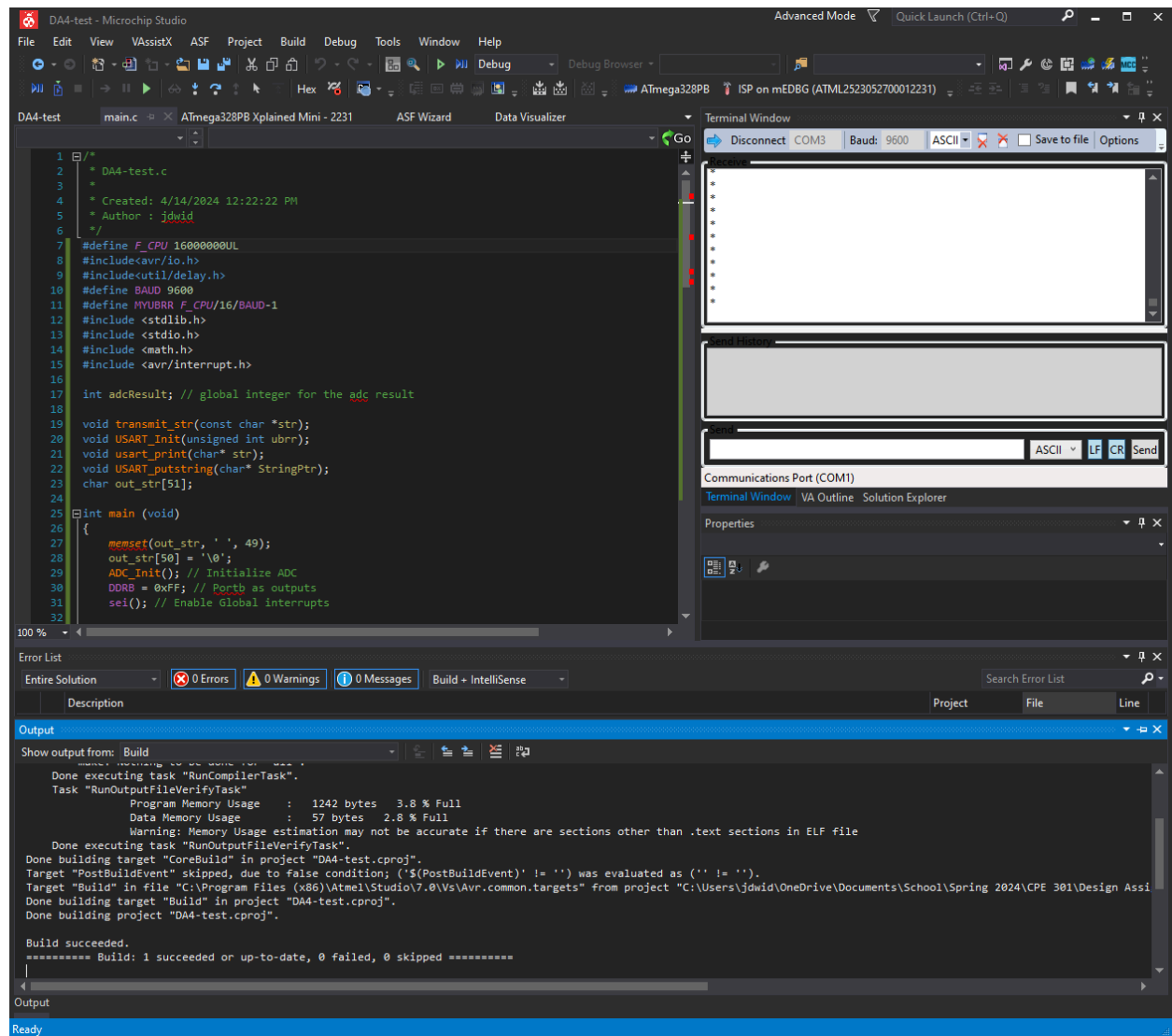
void USART_Init(unsigned int ubrr)
{
    // Set baud rate
    UBRR0H = (unsigned char)(ubrr>>8);

```



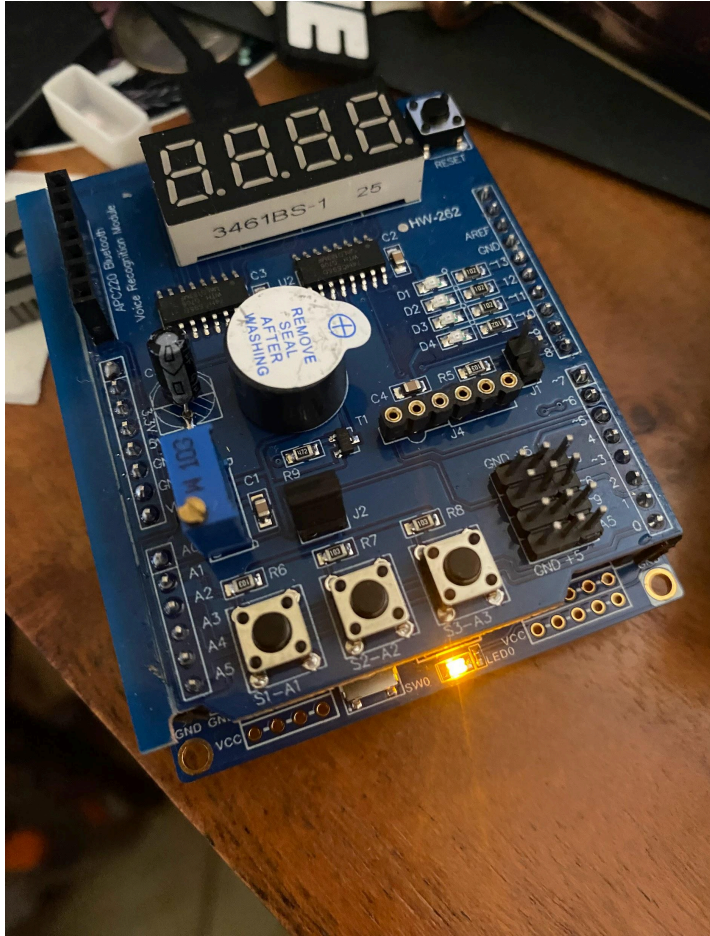
4. SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)

Task 1:



5. SCREENSHOT OF EACH DEMO (BOARD SETUP)

Task 1:



6. VIDEO LINKS OF EACH DEMO

Task 1: https://youtu.be/dLk8oB640_M

7. GITHUB LINK OF THIS DA

Task 1:

<https://github.com/JackOfSpades-7/UNLV-Embedded-Systems/tree/main/Design%20Assignment%204>

Student Academic Misconduct Policy

<http://studentconduct.unlv.edu/misconduct/policy.html>

"This assignment submission is my own, original work".

Johnathan Widney