
STM32
CubeMX

1. Description

1.1. Project

Project Name	Ex3
Board Name	custom
Generated with:	STM32CubeMX 6.6.1
Date	10/04/2022

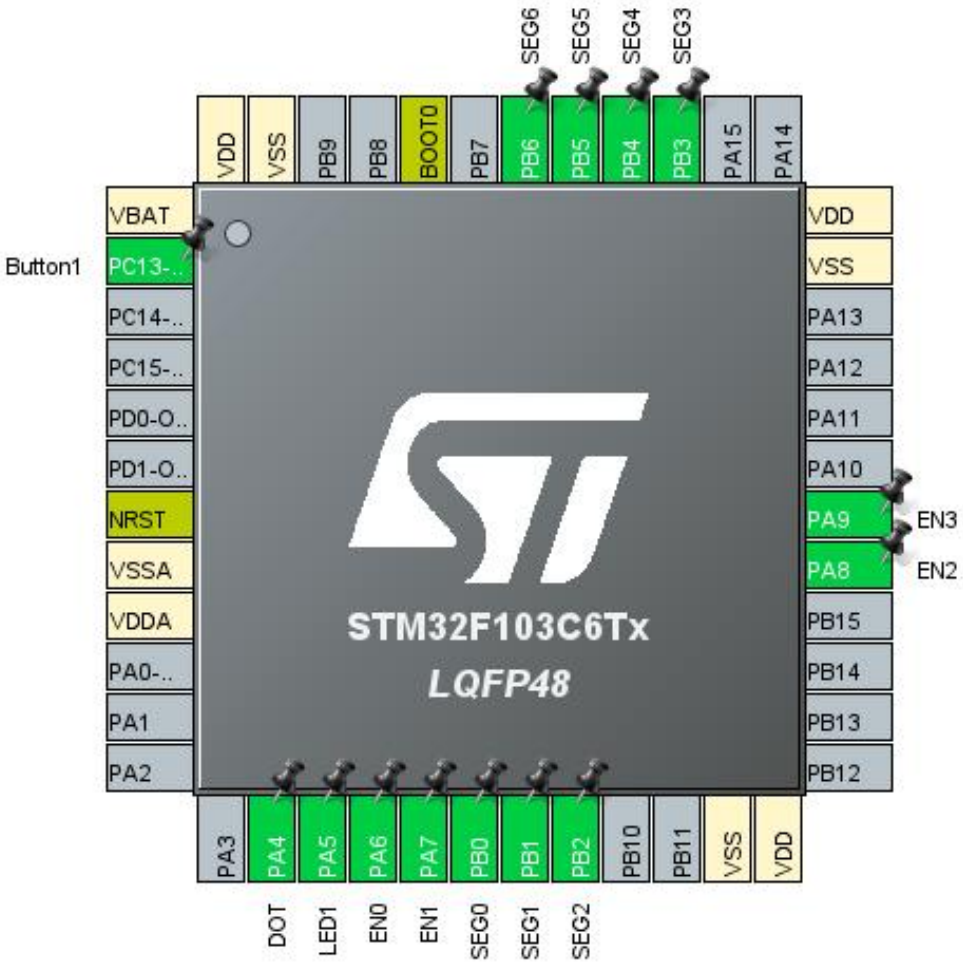
1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C6Tx
MCU Package	LQFP48
MCU Pin number	48

1.3. Core(s) information

Core(s)	Arm Cortex-M3

2. Pinout Configuration

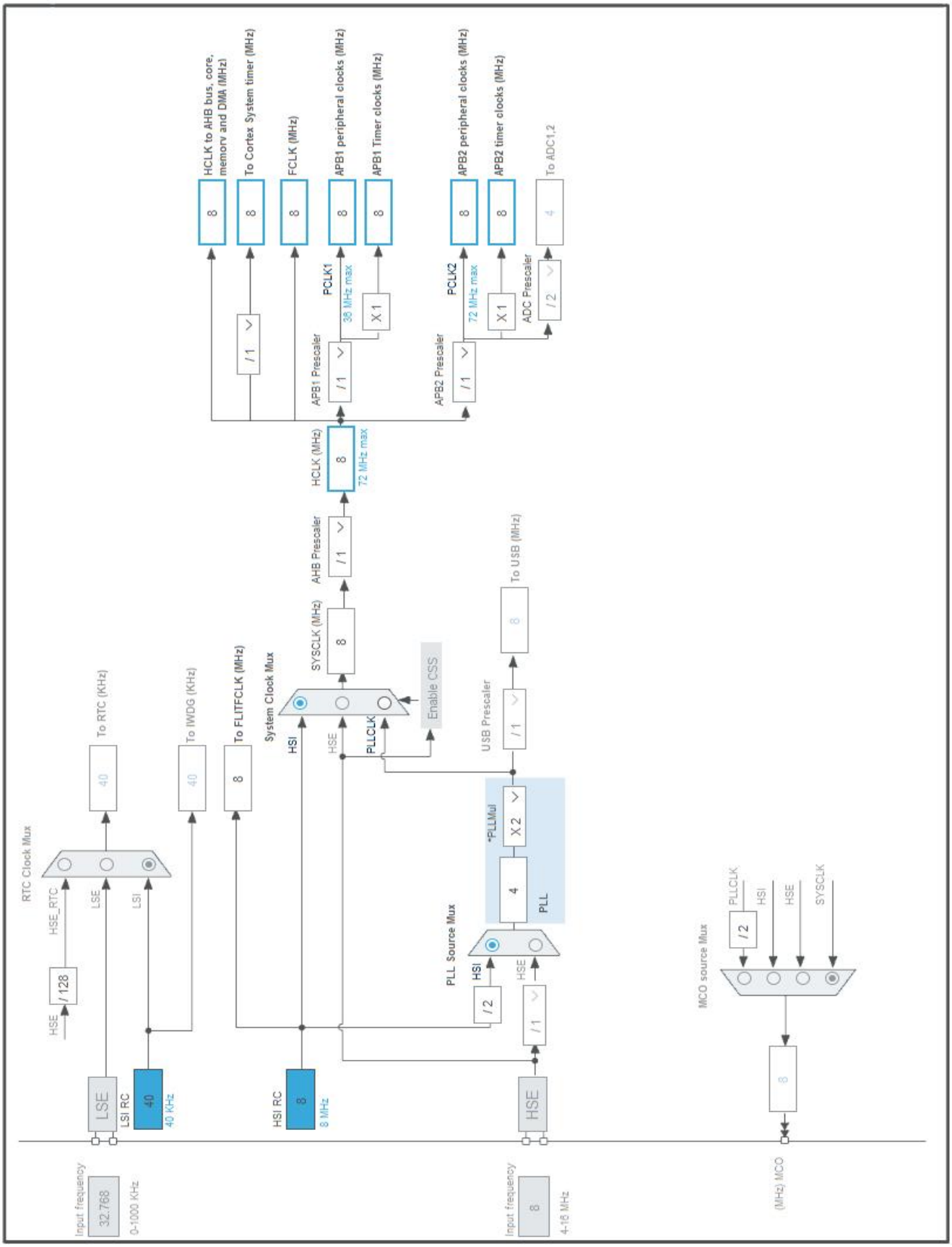


3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13-TAMPER-RTC *	I/O	GPIO_Input	Button1
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
14	PA4 *	I/O	GPIO_Output	DOT
15	PA5 *	I/O	GPIO_Output	LED1
16	PA6 *	I/O	GPIO_Output	EN0
17	PA7 *	I/O	GPIO_Output	EN1
18	PB0 *	I/O	GPIO_Output	SEG0
19	PB1 *	I/O	GPIO_Output	SEG1
20	PB2 *	I/O	GPIO_Output	SEG2
23	VSS	Power		
24	VDD	Power		
29	PA8 *	I/O	GPIO_Output	EN2
30	PA9 *	I/O	GPIO_Output	EN3
35	VSS	Power		
36	VDD	Power		
39	PB3 *	I/O	GPIO_Output	SEG3
40	PB4 *	I/O	GPIO_Output	SEG4
41	PB5 *	I/O	GPIO_Output	SEG5
42	PB6 *	I/O	GPIO_Output	SEG6
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	Ex3
Project Folder	C:\Users\ASUS\Downloads\Documents\221_Vi xu ly\ThiNghiem\LAB2\EX3
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.4
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_TIM2_Init	TIM2

6. Power Consumption Calculator report

6.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103C6Tx
Datasheet	DS5936_Rev7

6.2. Parameter Selection

Temperature	25
Vdd	3.3

6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

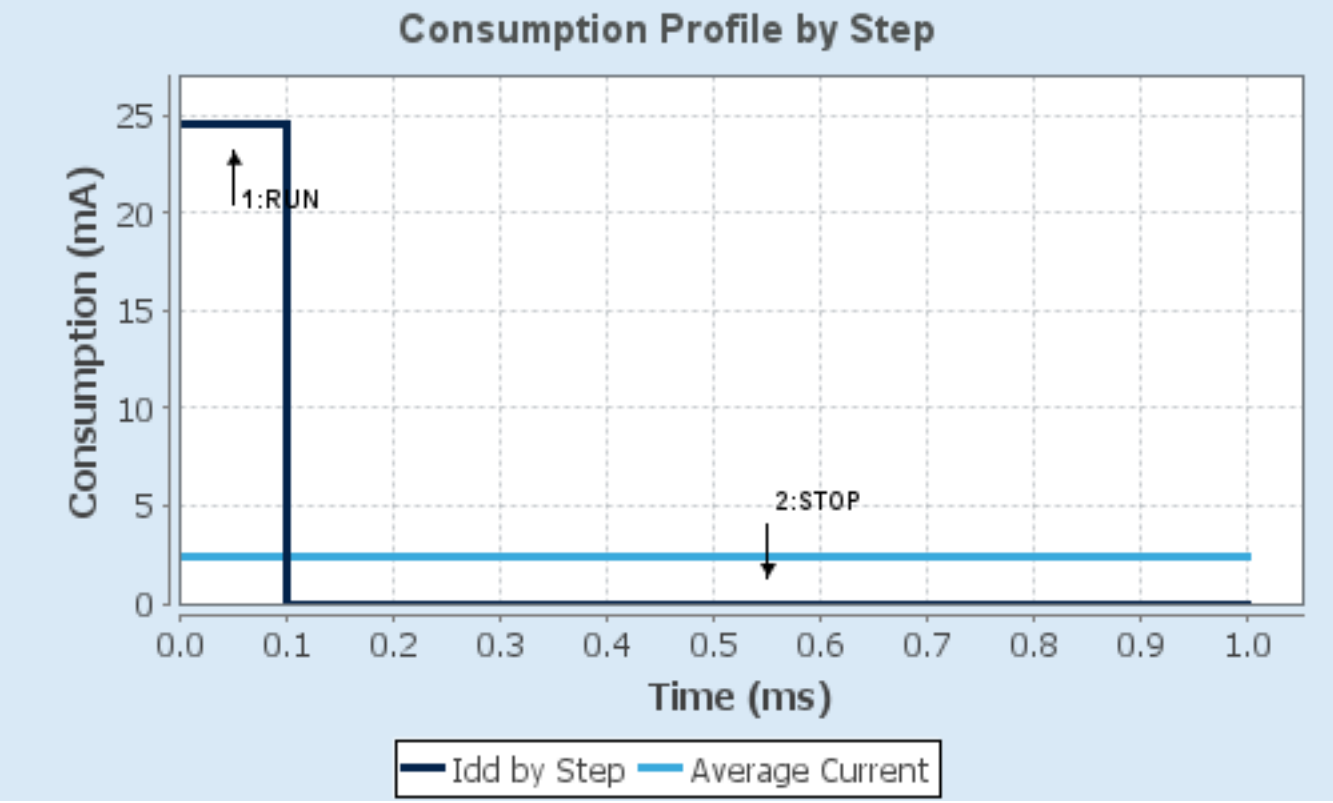
6.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	72 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator_LP
Clock Source Frequency	8 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	24.5 mA	11.7 µA
Duration	0.1 ms	0.9 ms
DMIPS	90.0	0.0
Ta Max	100.55	105
Category	In DS Table	In DS Table

6.5. Results

Sequence Time	1 ms	Average Current	2.46 mA
Battery Life	1 month, 27 days, 1 hour	Average DMIPS	61.0 DMIPS

6.6. Chart


Consumption Profile by Step
Consumption (mA)
25
20
15
10
5
0
1:RUN
2:STOP
0.0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1.0
Time (ms)
Idd by Step
Average Current

7. Peripherals and Middlewares Configuration

7.1. RCC

7.1.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
HSE Startup Timout Value (ms)	100
LSE Startup Timout Value (ms)	5000

7.2. TIM2

Clock Source : Internal Clock

7.2.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	**7999 ***
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	**9 ***
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

*** User modified value**

8. System Configuration

8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
GPIO	PC13-TAMPER-RTC	GPIO_Input	Input mode	**Pull-up ***	n/a	Button1
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	DOT
	PA5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LED1
	PA6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	EN0
	PA7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	EN1
	PB0	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG0
	PB1	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG1
	PB2	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG2
	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	EN2
	PA9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	EN3
	PB3	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG3
	PB4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG4
	PB5	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG5
	PB6	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SEG6

8.2. DMA configuration

nothing configured in DMA service

8.3. NVIC configuration

8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	15	0
TIM2 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		

8.3.2. NVIC Code generation

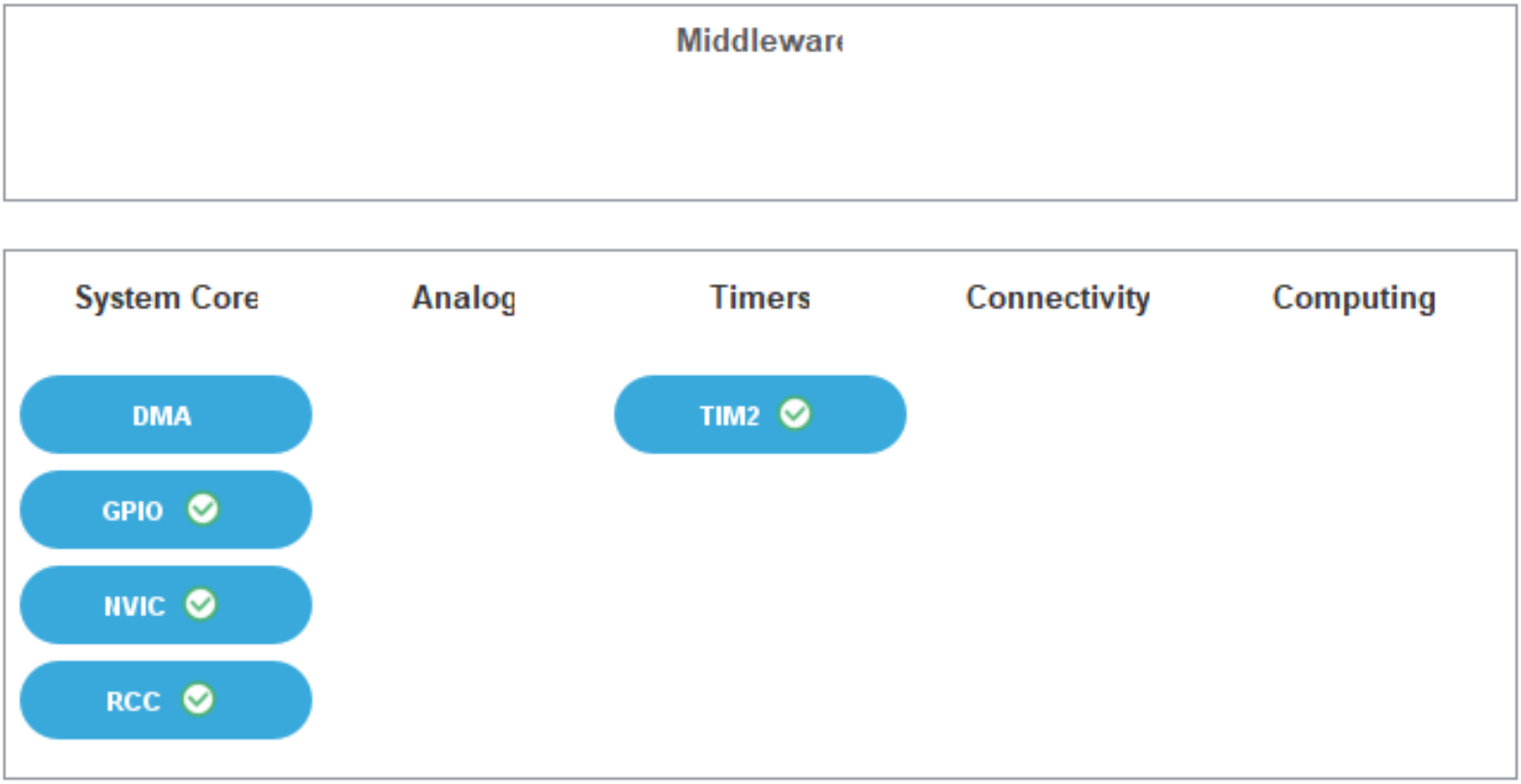
Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
TIM2 global interrupt	false	true	true

*** User modified value**

9. System Views

9.1. Category view

9.1.1. Current



10. Docs & Resources

Type	Link