



বরেন্দ্র বিশ্ববিদ্যালয়
VARENDRA UNIVERSITY



Department of Computer Science and Engineering

21st Batch

Lab Report

Course title : Digital Signal Processing Lab
Course Code : CSE-414

Submitted By	Submitted To
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Signature

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SL	Problem Statement	Date
1	Basics about Prolog	14-05-2022
2	Creating the family tree with prolog	21-07-2022
3	Get Fibonacci series using prolog	21-07-2022
4	Determining the sports background of a person depending on their height and weight	04-08-2022
5	Create a basic data set as a CSV file then perform EDA analysis on that data set.	25-08-2022