

Computer Architecture HW2 Report

電機二 梁程捷(B11901136)

Register Name	Type	Width	Bus	MB	AR	AS	SR	SS	ST
cnt_reg	Flip-flop	5	Y	N	N	N	N	N	N
s_reg_reg	Flip-flop	64	Y	N	N	N	N	N	N
state_reg	Flip-flop	4	Y	N	Y	N	N	N	N
operand_b_reg	Flip-flop	32	Y	N	Y	N	N	N	N