

Recitation Class 7

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Outline

Chapter 10 - Fundamentals of the Metal–Oxide–Semiconductor Field-Effect Transistor

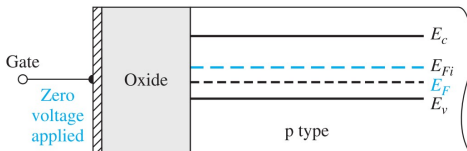
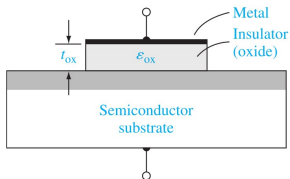
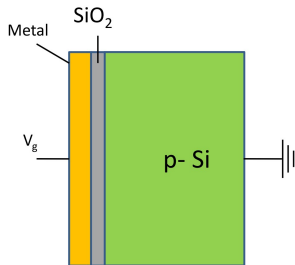
- The Two-Terminal MOS Structure
- Capacitance–Voltage Characteristics
- Non-Ideal Effects

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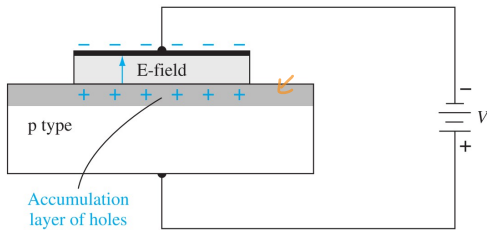
Chapter 10 - Fundamentals of the Metal–Oxide–Semiconductor Field-Effect Transistor

- The Two-Terminal MOS Structure
- Capacitance–Voltage Characteristics
- Non-Ideal Effects

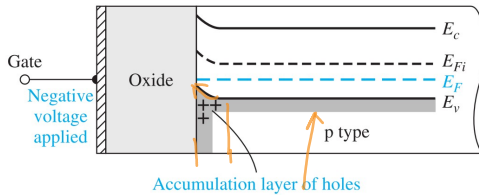
Metal–Oxide–Semiconductor



Negative Gate Voltage

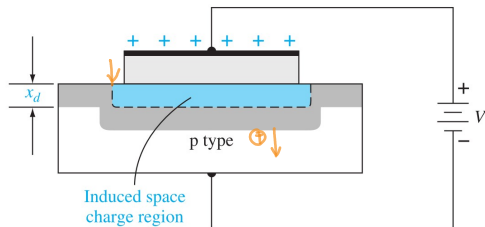


Negative Gate Voltage

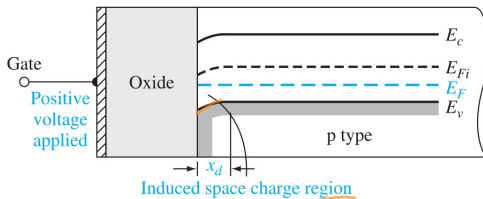


$$p > p_0$$
$$E_F \text{ close to } E_v$$

Positive Gate Voltage



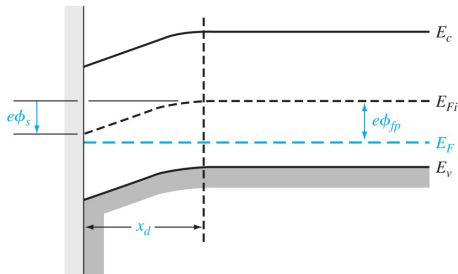
Positive Gate Voltage



P

p^+n
M S

Depletion Layer Thickness



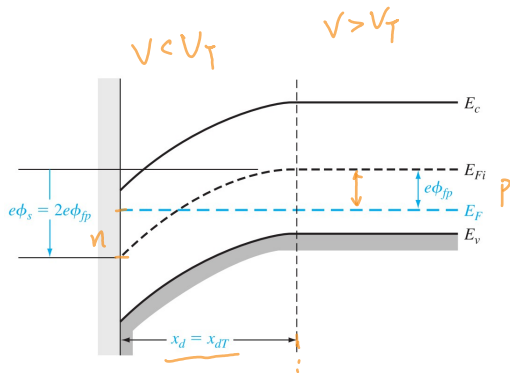
$$\phi_{fp} = V_t \ln \left(\frac{N_a}{n_i} \right)$$

$$x_d = \left(\frac{2\epsilon_s \phi_s}{eN_a} \right)^{1/2}$$

Note: In the original image, an orange arrow points from $2\phi_{fp}$ to ϕ_s in the second equation, indicating that $\phi_s = 2\phi_{fp}$ for an intrinsic semiconductor.

ϕ_s : the surface potential, is the difference (in V) between E_{Fi} measured in the bulk semiconductor and E_{Fi} measured at the surface.

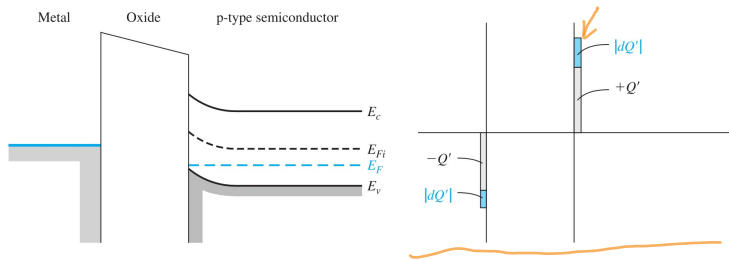
Threshold Inversion Point



$$\phi_s = 2\phi_{fp}$$

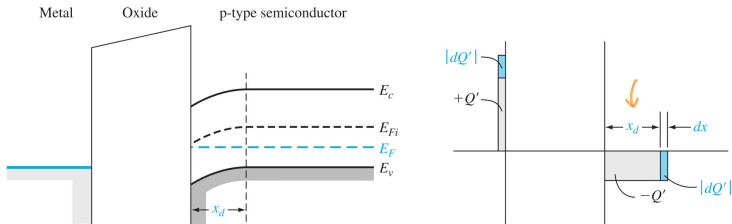
$$x_{dT} = \left(\frac{4\epsilon_s \phi_{fp}}{eN_a} \right)^{1/2}$$

Accumulation



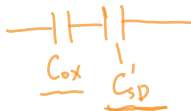
$$C'(\text{acc}) = C_{ox} = \frac{\epsilon_{ox}}{t_{ox}}$$

Depletion



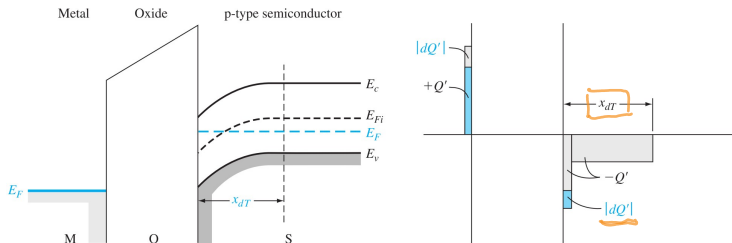
$$C'(\text{depl}) = \frac{C_{ox} C'_{SD}}{C_{ox} + C'_{SD}}$$

$$= \frac{\epsilon_{ox}}{t_{ox} + \left(\frac{\epsilon_{ox}}{\epsilon_s} \right) x_d}$$



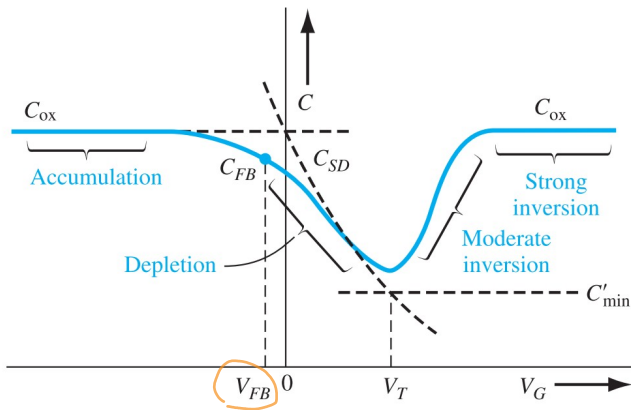
$$C'_{min} = \frac{\epsilon_{ox}}{t_{ox} + \left(\frac{\epsilon_{ox}}{\epsilon_s} \right) x_d}$$

Inversion



$$C'(\text{inv}) = C_{ox} = \frac{\epsilon_{ox}}{t_{ox}}$$

Ideal Low-Frequency C-V Curve



$$C'_{FB} = \frac{\epsilon_{ox}}{t_{ox} + \left(\frac{\epsilon_{ox}}{\epsilon_s} \right) \sqrt{\left(\frac{kT}{e} \right) \left(\frac{\epsilon_s}{eN_a} \right)}}$$

Exercise

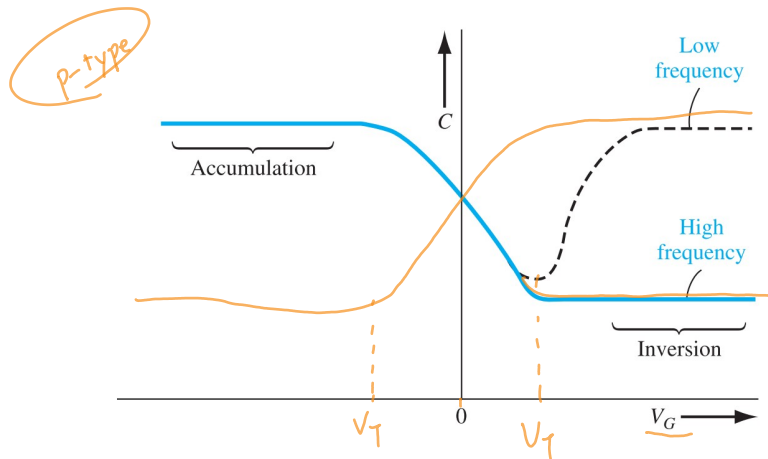
Objective Calculate C_{ox} , C'_{min} and C'_{FB} for a MOS capacitor. Consider a p-type silicon substrate at $T = 200K$ doped to $N_a = 10^{16} cm^{-3}$. The oxide is silicon dioxide with a thickness of $t_{ox} = 18nm = 180\text{\AA}$, and the gate is aluminum. (Example 10.6 on textbook)

Answer:

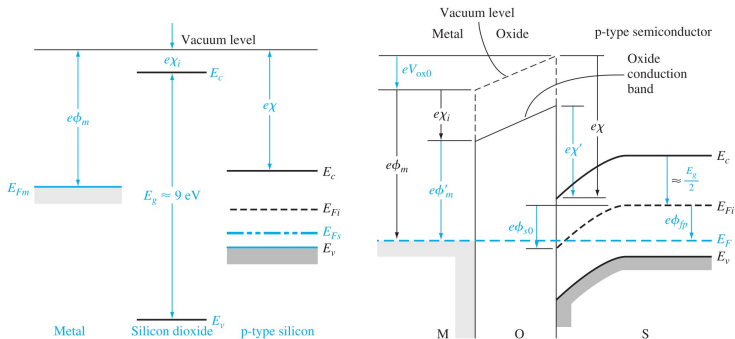
Frequency Effects

Two sources of electrons

1. Diffusion of minority carrier electrons.
2. Thermal generation of electron-hole pairs within the space charge region. G



Work Function Difference

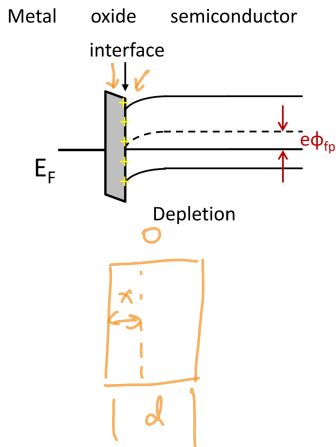
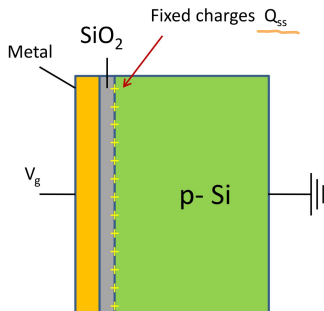


$$\phi_{ms} = \phi'_m - \left(\chi' + \frac{E_g}{2e} + \phi_{fp} \right)$$

$$\phi_{ms} = \phi'_m - \left(\chi' + \frac{E_g}{2e} - \phi_{fn} \right)$$

Not required.

Fixed Charge



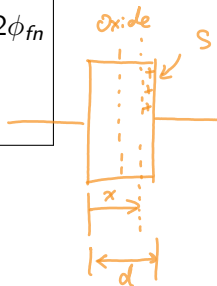
Adjustment on V_T

$$|Q'_{SD}(\max)| = eN_a x_{dT} = 2\sqrt{e\epsilon_s N_a \phi_{fp}}$$

$$V_{TN} = \frac{|Q'_{SD}(\max)|}{C_{ox}} + V_{FB} + 2\phi_{fp}$$

$$V_{TP} = -\frac{|Q'_{SD}(\max)|}{C_{ox}} + V_{FB} - 2\phi_{fn}$$

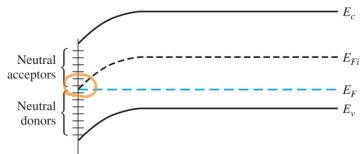
$$V_{FB} = \phi_{ms} - \frac{Q'_{ss}}{C_{ox}} \cdot \frac{x}{d}$$



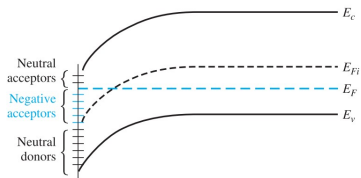
Surface States \mathcal{S}



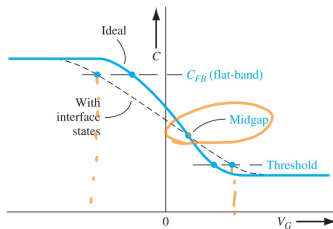
(a)



(b)



(c)



Example

Objective Calculate the threshold voltage of a MOS system using an aluminum gate.

Consider a p-type silicon substrate at $T = 300K$ doped to $N_a = 10^{15}cm^{-3}$. Let $Q'_{ss} = 10^{10}cm^{-2}$, $t_{ox} = 12nm = 120\text{\AA}$, and assume the oxide is silicon dioxide. $\phi_{ms} = -0.88V$. (Example 10.4 on textbook)

Answer:

End