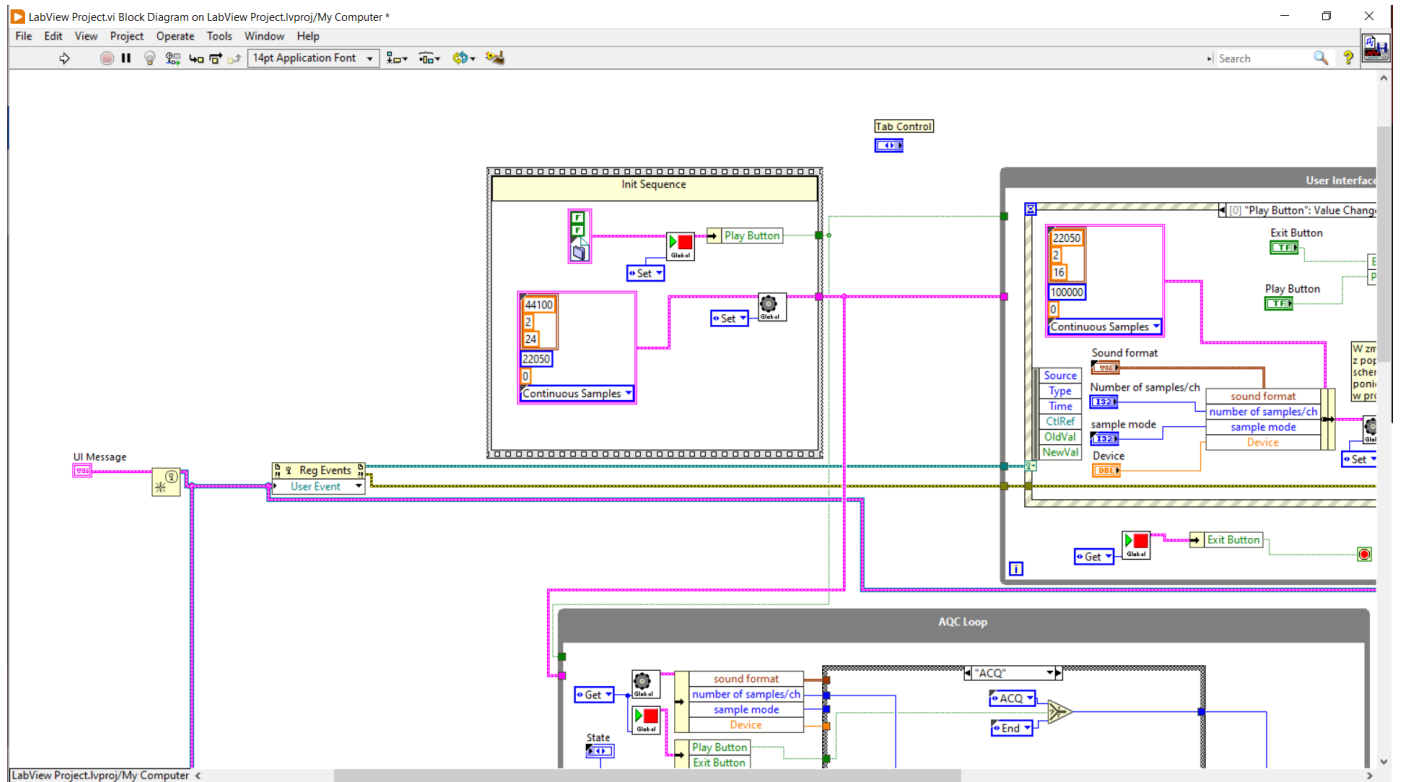
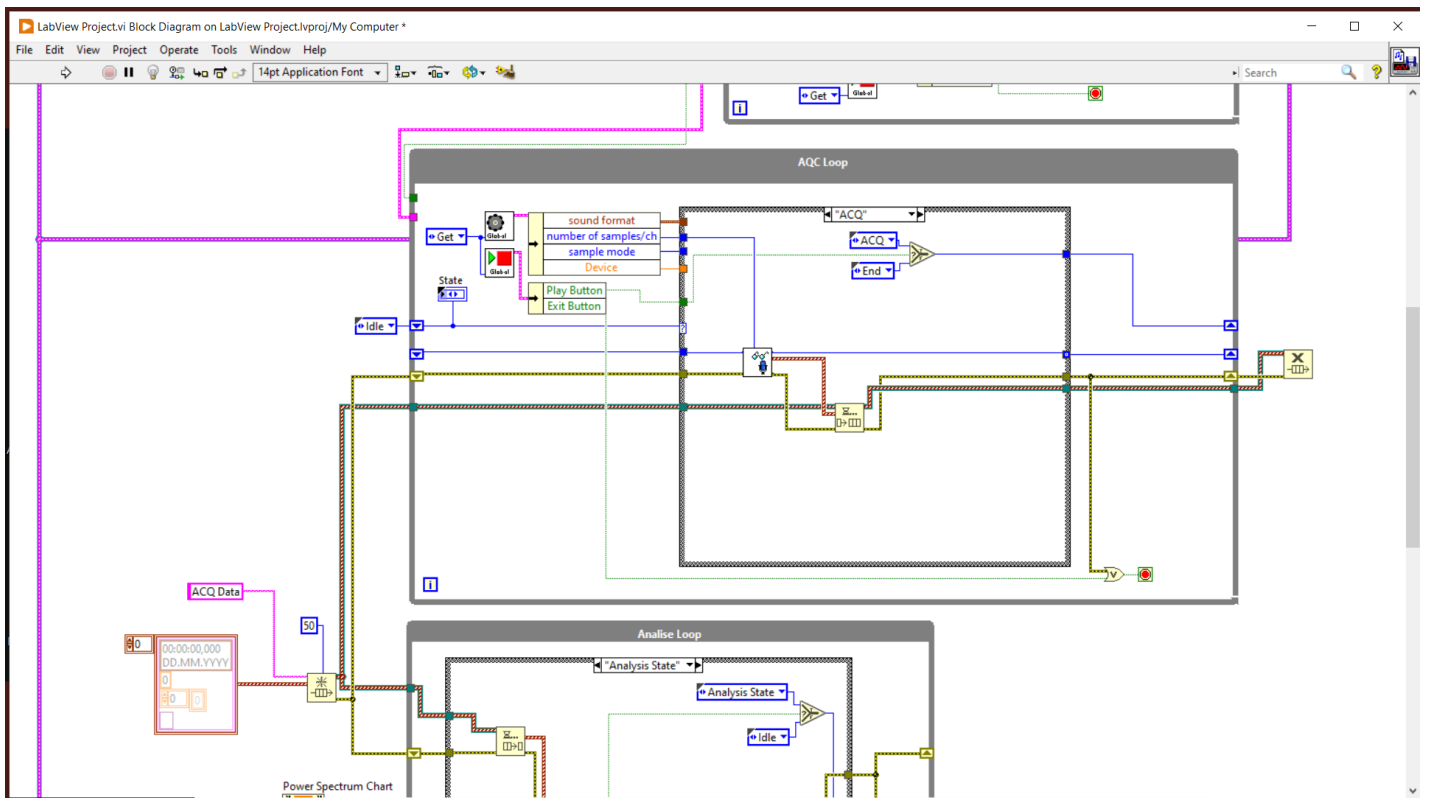


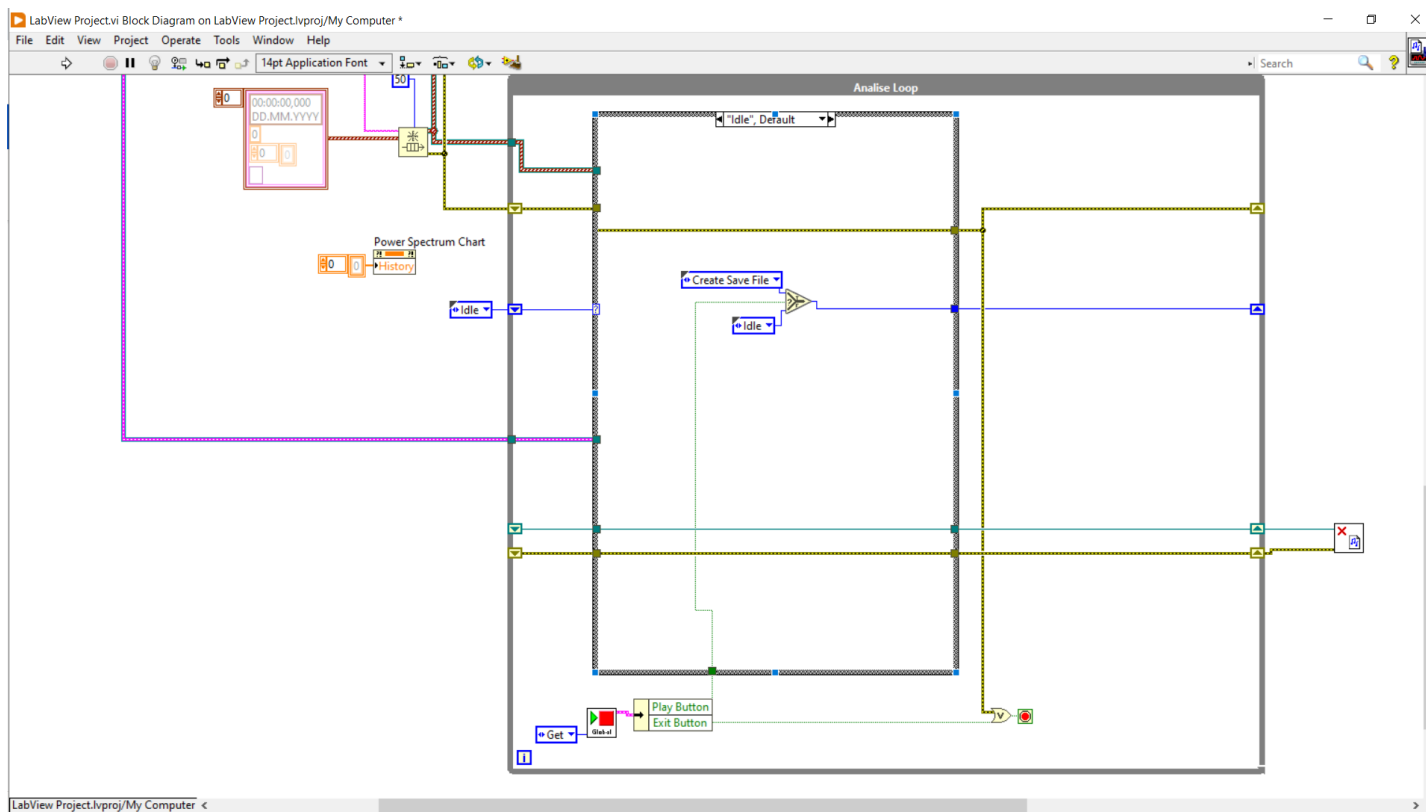
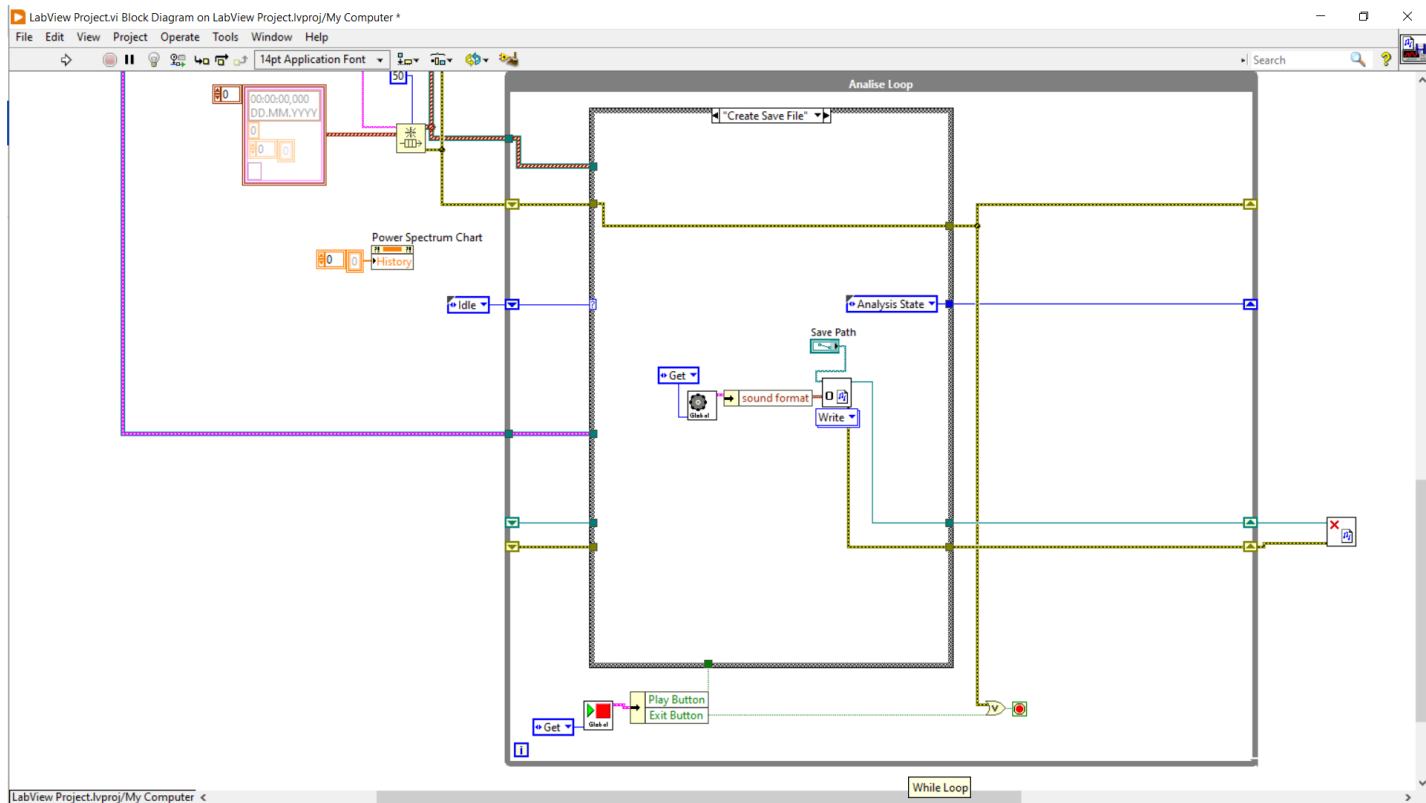
Projekt - Jakub Gucik

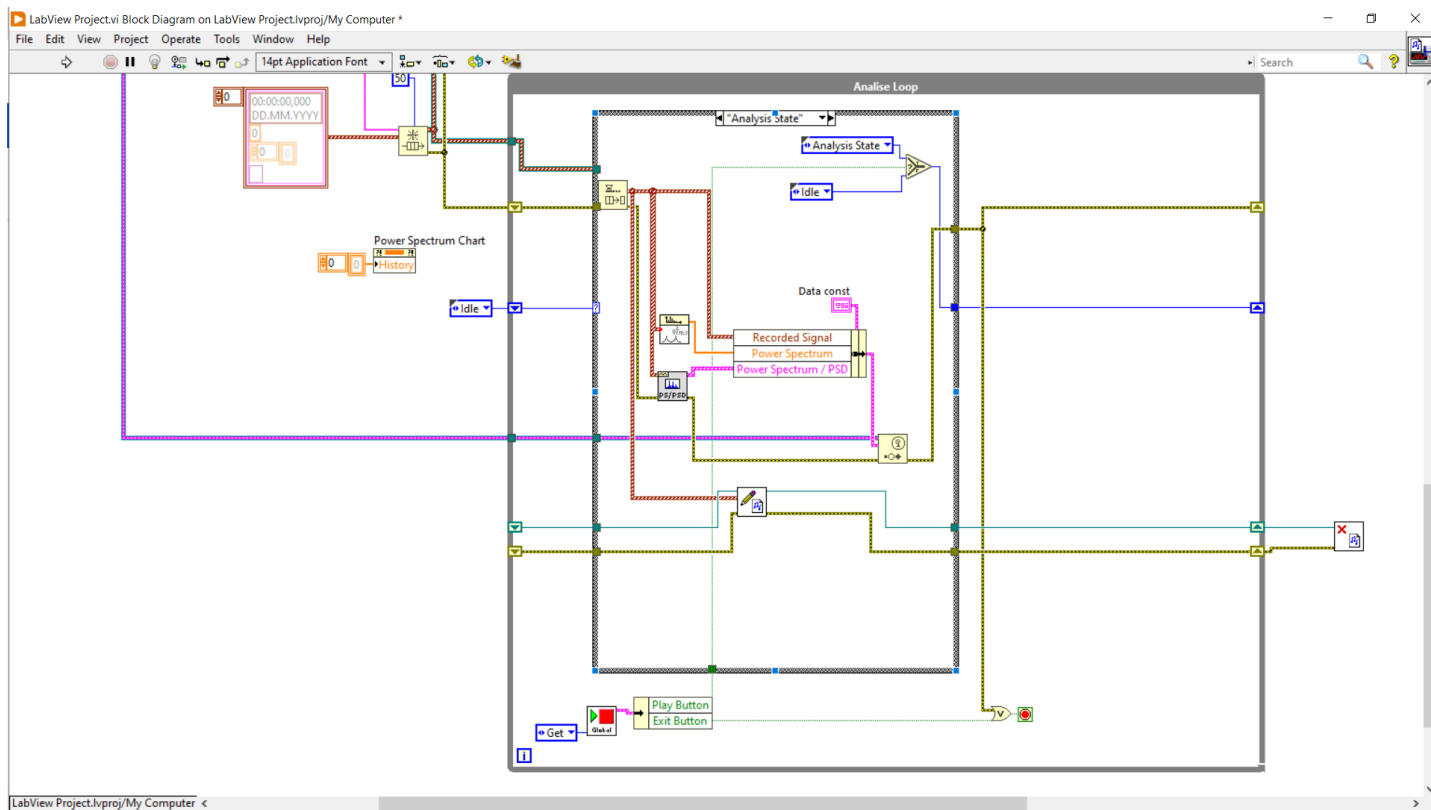


Obraz nr.1 - Block Diagram projektu (Init Sequence)

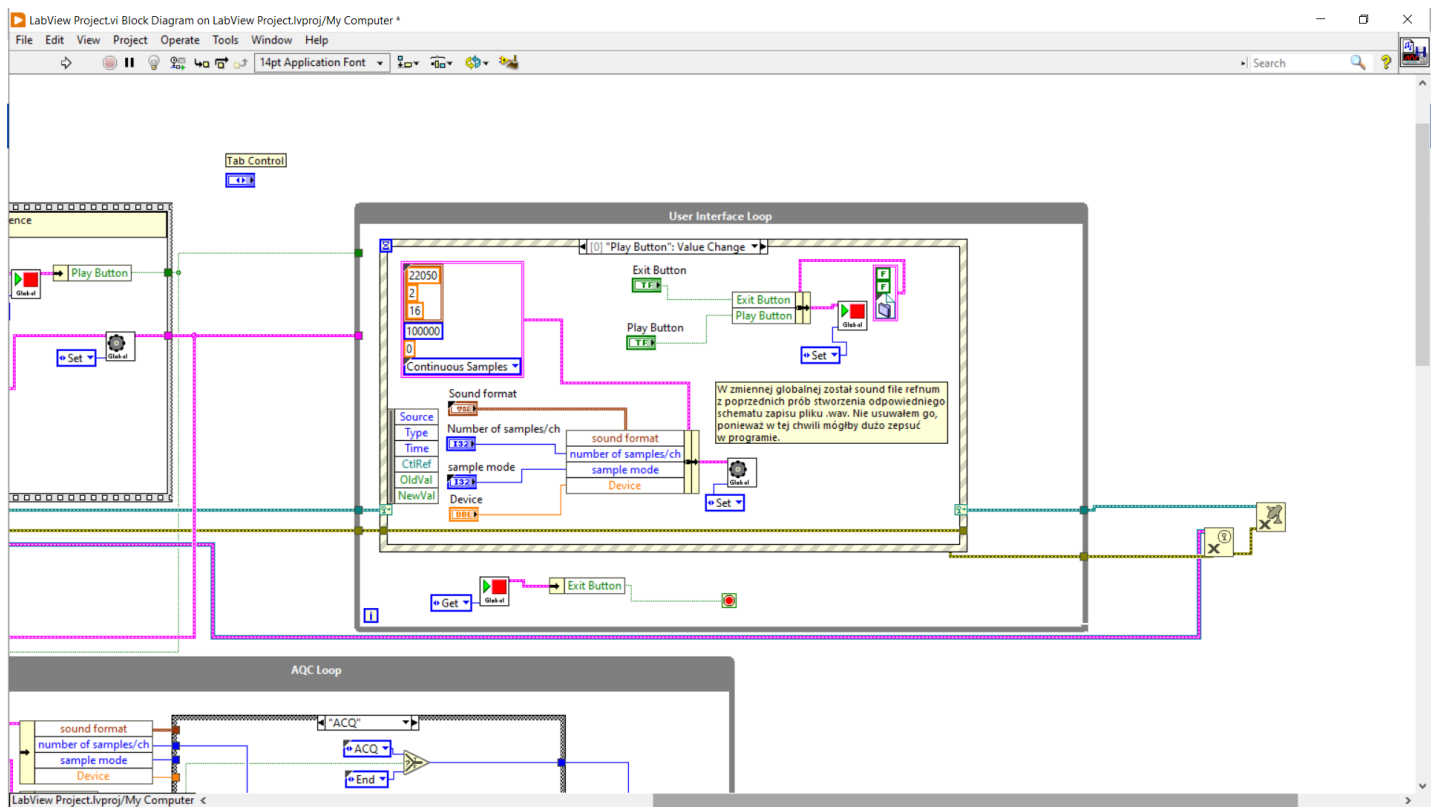


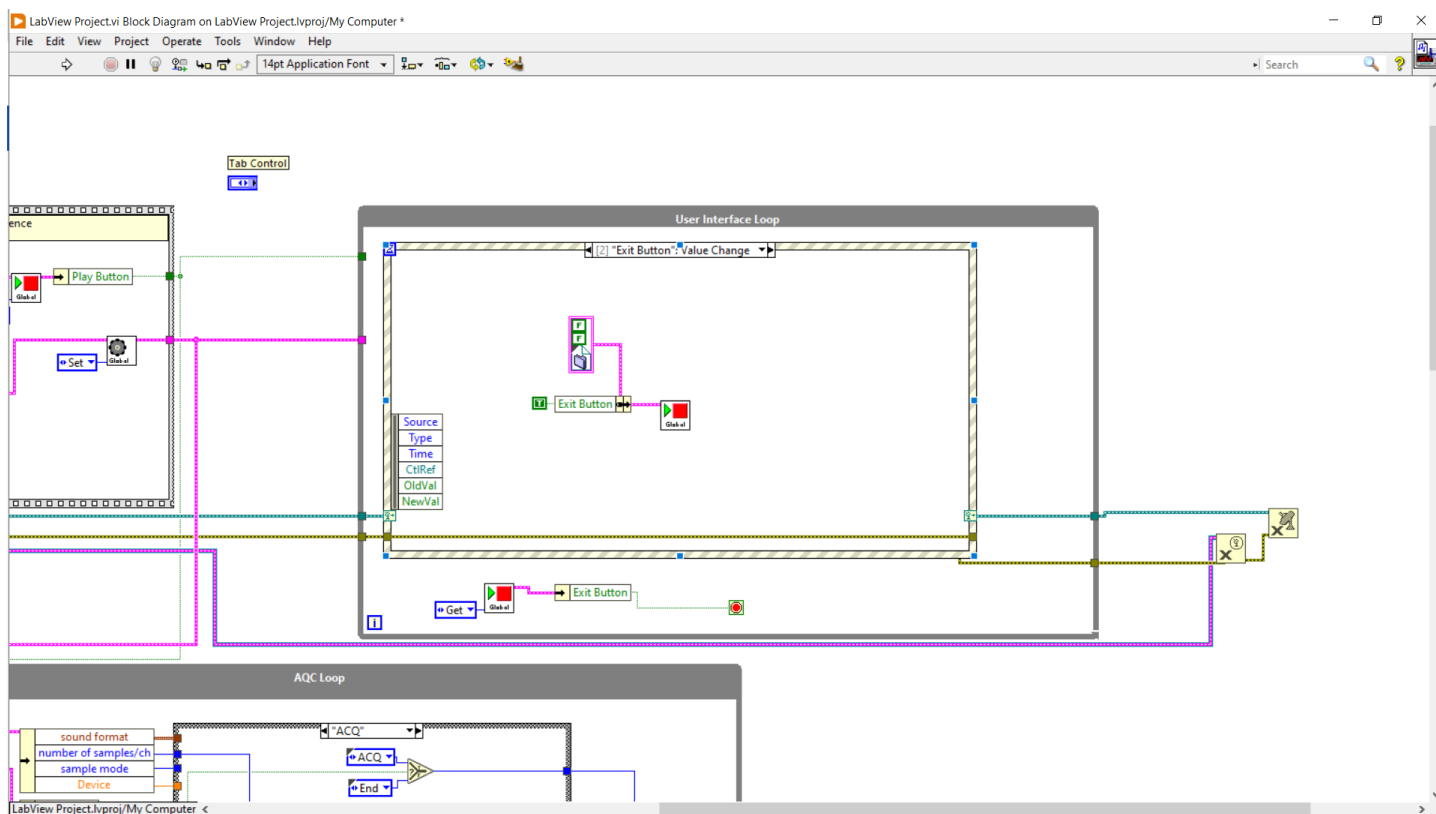
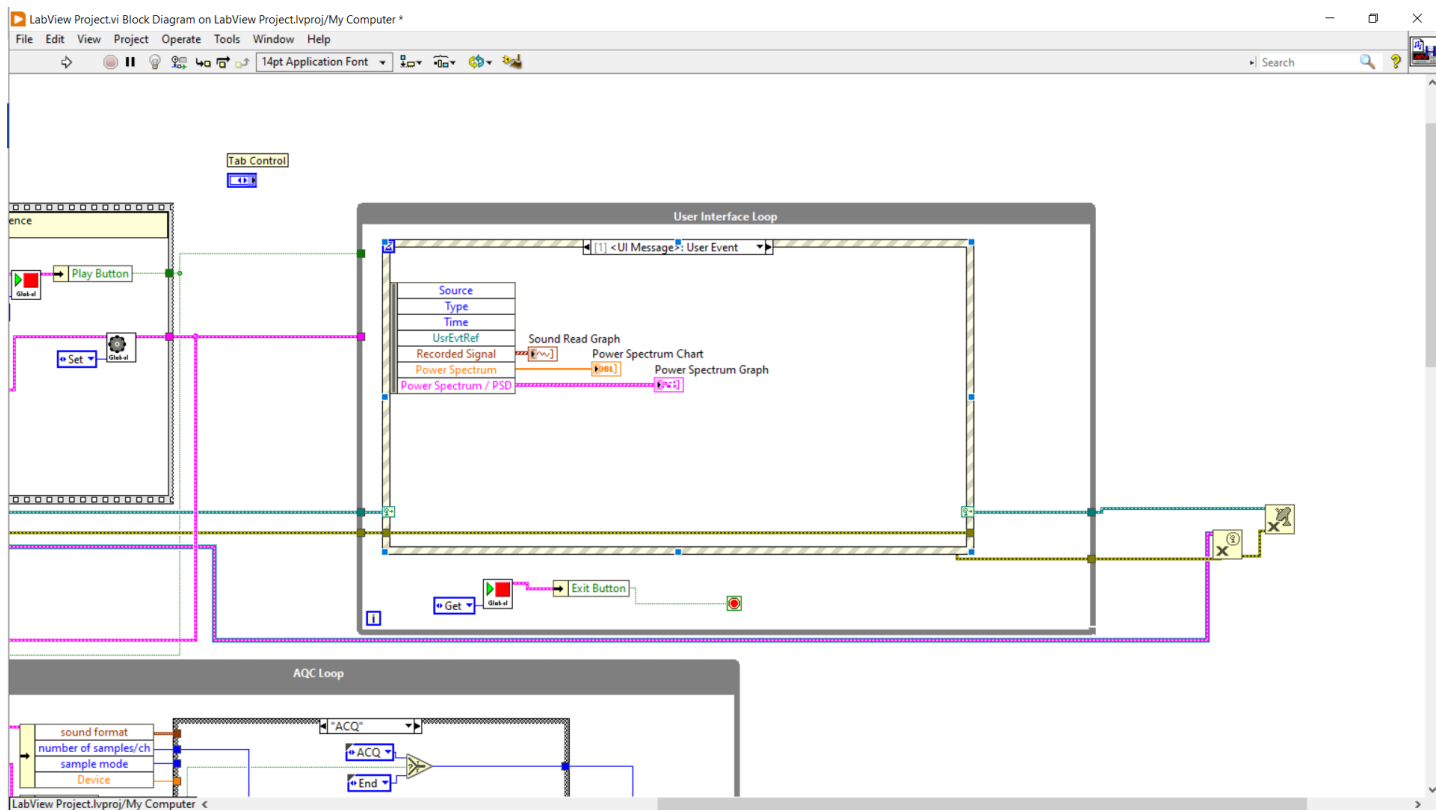
Obraz nr.2 - Block Diagram projektu (AQC Loop)



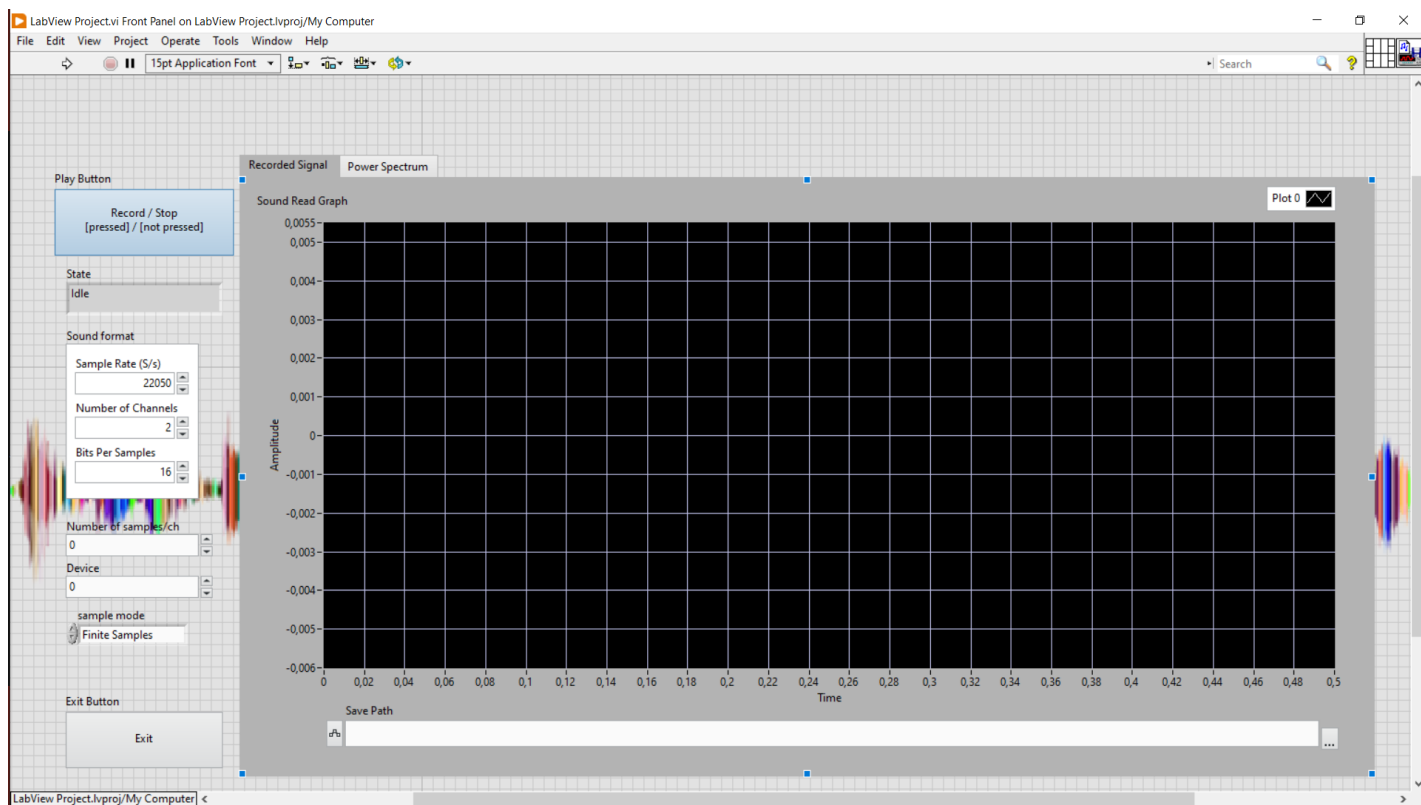


Obraz nr.3 - Block Diagram projektu (Analise Loop)





Obraz nr.4 - Block Diagram projektu (User Interface Loop)



Obraz nr.5 - Front Panel projektu