Symbolic Execution of Toffoli-Hadamard Quantum Circuits

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1 Introduction

The general state of a quantum bit (qubit) is mathematically modeled using an equation parameterized by two angles θ and ϕ as follows:

$$\cos\frac{\theta}{2}\,|0\rangle + e^{i\phi}\sin\frac{\theta}{2}\,|1\rangle$$

The description models the fact that the qubit is in a superposition of false $|0\rangle$ and true $|1\rangle$. The angle θ determines the relative amplitudes of false and true and the angle ϕ determines the relative phase between them. A particular case when $\theta = \pi/2$ and $\phi = 0$ is ubiquitous in quantum algorithms. In those cases, the general representation reduces to:

$$1/\sqrt{2}(|0\rangle + |1\rangle)$$

which represents a qubit in an equal superposition of false and true.

The reason this particular case is distinguished is because a rather common template for quantum algorithms is to start with qubits initialized to $|0\rangle$ and immediately apply a Hadamard H transformation whose action is $|0\rangle \mapsto 1/\sqrt{2}(|0\rangle + |1\rangle)$. This superposition is then further manipulated depending on the algorithm in question.

Our observation is that a qubit in the special superposition $1/\sqrt{2}(|0\rangle + |1\rangle)$ is, computationally speaking, indistinguishable from a symbolic boolean variable with an unknown value in the same sense used in symbolic evaluation of classical programs. First, the superposition is not observable. The only way to observe the qubit is via a measurement which collapses the state to be either false or true with equal probability. Second, and more significantly, this remarkably simple observation is quite robust even in the presence of multiple, possibly entangled, qubits.

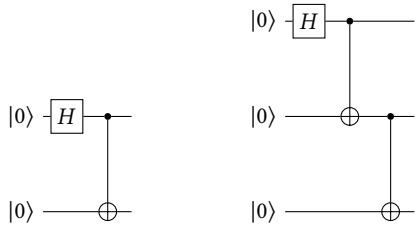

Figure 1. Bell and GHZ States

To see this, consider the conventional quantum circuits for creating the maximally entangled Bell and GHZ states in Fig. 11. On the left, the circuit generates the Bell state $(1/\sqrt{2})(|00\rangle + |11\rangle)$ as follows. First the state evolves from $|00\rangle$ to $(1/\sqrt{2})(|00\rangle + |10\rangle)$. Then we apply the CX-gate whose action is to negate the second qubit when the first one is true. By using the symbol x for $H\,|0\rangle$, the input to the CX-gate is $|x0\rangle$. A simple case analysis shows that the action of CX-gate on inputs $|xy\rangle$ is $|x(x \oplus y)\rangle$ where $\oplus$ is the exclusive-or boolean operation. In other words, the CX-gate transforms $|x0\rangle$ to $|xx\rangle$. Since any measurement of the Bell state must produce either 00 or 11, by producing a symbolic state that shares the same name in two positions, we have accurately represented the entangled Bell state. Similarly, for the GHZ circuit on the right, the state after the Hadamard gate is $|x00\rangle$ which evolves to $|xx0\rangle$ and then to $|xxx\rangle$ again accurately capturing the entanglement correlations.

The introduction to symbolic variables opens a host a new exciting possibilities. Consider again the Bell circuit in Fig. 9a with an arbitrary initial value for the second qubit. The right subfigure Fig. 9b removes the explicit use of $H\,|0\rangle$ and replaces the top qubit with another symbolic variable. At this point, we can "partially evaluate" the circuit under various regimes. For example, we can set $y_1 = 0$ and $y_2 = 1$ and ask about values of x_1 and x_2 that would be consistent with this setting. We can calculate backwards from $|x_2 1\rangle$ as follows. The state evolves to $|x_2(1 \oplus x_2)\rangle$ which can be reconciled with the initial conditions yielding the constraints $x_1 = x_2$ and $1 \oplus x_2 = 0$ whose solutions are $x_1 = x_2 = 1$.

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(a) Bell circuit **(b)** Symbolic variant

Figure 2. A conventional quantum circuit for generating a Bell state (a); its classical symbolic variant.

There are three common ways to compute with *unknown* values:

1. by treating as symbolic variables,
2. by using distributions,
3. by using unitary operators (i.e. qubits).

As a zeroth-order approximation, one can think of the "unitary operators" of quantum computing as $\mathbb{C}$-valued distributions. The extra "phase" involved allows tracking of simple relations between inputs (i.e. *entanglement*).

What we will do is to look at the classical reversible circuits at the heart of quantum computation, replacing the qubits by symbolic quantities. We look at a particular class of circuits with some *inputs* and some *ancillae* on one side, along with some *outputs* and the same ancillae restored to their initial values.

Furthermore, all the circuits look like ???

What a quantum circuit does with H on the ancilla is to transform a known input to (the equivalent of) the uniform distribution on all possible values. The result is basically *maximally unknown*.

What we do instead is to remove the H, and treat the ancillae as dynamic inputs, i.e. fully unknown. However, these circuits are all *reversible*, so we can also start from a partial measurement (static), treating the rest of the state as dynamic. We then execute the circuit backwards "symbolically".

While this sounds like the perfect setup for partial evaluation (PE) or slicing[1]. Since our circuits are reversible, for simplicity we're going to look at retrodictive execution as forward execution of the reverse program, which puts us in the setup of PE.

Except that in PE, the result is a *residual program* that obeys the Futumura equations. In our case, we have "both sides", i.e. input, output and ancillae, in our hands. So the result is going to be a set of equations constraining the "possible pasts" of the output to be equal to the actual inputs. So rather than obtaining a residual program as output, we obtain a *system of equations*.

It all works because of two key things: we're only dealing with boolean values and with circuits with operations (not, CX, CCX, and CCCX) that have simple symbolic forms. Furthermore, boolean expressions (over AND and XOR) themselves have a normal form. So we can propagate that through.

[1]These are basically the same idea, but PE propagates known information forwards through the program, and slicing propagates it backwards from the exit points

And now we give actual details.
Questions:

- Where do we introduce the language over which we work (circuits)
- How much time do we spend on background (aka quantum vs reversible)

Another try at 'the story'.

1. We have some particular circuits, coming from Quantum computation, that we're interested in studying
2. These have a particular "shape", and use quantum superposition to basically create free unknowns, but in a reversible way
3. measurements give us some knowledge of the output,
4. this places us in a setting combining having forward static and dynamic knowledge of the input, in the context of a particular circuit, which is the domain of Partial Evalution (PE),
5. this also places us in a setting combining having backward static and dynamic knowledge of the output, in the context of a particular circuit, which is the domain of Slicing,
6. as the values we're dealing with (booleans) are simple, and the circuits only do simple, reversible operations, we can keep track of symbolic expressions for all values over the whole circuit. In other words, we never need to residualize a program, we get "closed forms" representations (as boolean expressions),
7. as we have knowledge forwards and backwards, the result of evaluation will be *systems of constraints*.
8. our test cases consist of circuits that form the heart *classical*, *well-known* quantum circuits (Simon, ..., Shor)
9. some of these circuits are extremely large, yet we process them quite quickly.
10. The exact circuits we use are *generalized Toffoli* gates (which are universal for reversible computation, and also ???)
11. There exists a normal form for boolean functions ideally suited to our problem: ANF (https://en.wikipedia.org/wiki/Algebraic_normal_form)
12. Implementation details:
    - tagless for value creation language (true, false, not, xor, and)
    - variables are references to values
    - the representation of a formula abstracts over variable evaluation and rep of formulas

- GToff are lists of control wires (flipped or straight) and a target wire.
- circuits are sequences of GT gates; we mark our circuits with input/ancilla-in/ancilla-out/output
- (continue)

13. evaluation on
    - present test cases in more detail
    - show results of running
      - semantically
      - timings

So, all in all, I see the outline (and who seems in good position to write)

1. Introduction [Amr]
   - items 1-11 above, but 10-11 will be done in more depth later
2. Circuits and Boolean Functions [Jacques will try]
   - redo 10 and 11 in more detail. Other background here
3. Symbolic Evaluation of Circuits [Jacques]
   - Define the problem we're tackling
4. Design and Implementation [Jacques]
   - Give the design (lots of polymorphism, tagless, etc)
   - Talk about dreadful timings
   - Design of better data-structures for ANF
5. Evaluation [Amr and Jacques]
   - Explain each of the circuits we're using [Amr]
   - Explain how to 'read off' the answer from the constraints [Amr and Jacques]
   - Show some timings [Jacques]
6. Conclusion [?]

2 Circuits and Boolean Functions

TODO: Make sure to document the "Circuit Abstraction" that's in comments at the top of `Circuits.hs`, along with the picture. Either here or ensure it occurs before.

Algebraic Normal Form (ANF). The semantics of a generalized Toffoli gate with n control qubits: $a_{n_1}, \cdots, c_0$ and one target qubit b is $b \oplus \bigwedge_i a_i$, the exclusive-or of the target b with the conjunction of all the control qubits. That is precisely the definition of the algebraic normal form of boolean expressions. We note that circuits that only use X and CX-gates never generate any conjunctions and hence lead to formulae that efficiently solvable classically [30, 34].

3 Symbolic Evaluation of Circuits

For quantum circuits over H, CCX restrict to $|0\rangle$ inputs; look at state after the initial group of H gates

```
classical wires treated as known
superpositions |0> + |1> treated as symbols x
run circuit with known values and symbol for superpositions
```

4 Design and Implementation

Our exposition of the design and implementation of our system will follow Parnas' advice on *faking it* [**?**]: a reconstruction of the requirements as we should have had them if we'd been all-knowing, and a design that fits those requirements. The version history in our github repository can be inspected for anyone who wants to see our actual path.

As we experimented with the idea of partial evaluation and symbolic execution of circuits, we ended up writing a lot of variants of essentially the same code, but with minor differences in representation. From these early experiments, we could see the major variation points:

- representation of *boolean values* and *boolean functions*,
- representation of ANF,
- representation of circuits.

We also wanted to write out circuits only once, and have them be valid across these representation changes.

A number of our examples involve arbitrary boolean functions (given as black boxes at "compile time") for which we want to, offline, generate an equivalent reversible circuit. In other words for $f : \mathbb{B}^n \to \mathbb{B}$, we wish to generate the circuit for $g : \mathbb{B}^{n+1} \to \mathbb{B}^{n+1}$ such that for $\overline{x} : \mathbb{B}^n$, then $g(\overline{x}, y) = (\overline{x}, y \oplus f(\overline{x}))$.

This leads us to the requirements our code must fulfill.

4.1 Requirements

We need to be able to deal with the following variabilities:

1. multiple representations of *boolean values*,
2. multiple representations of *boolean formulas*,
3. different evaluation means (directly and symbolically),

It must also be possible to implement the following:

4. a reusable representation of circuits composed of generalized Toffoli gates,
5. a reusable representation of the inputs, outputs and ancillas associated to a circuit,
6. a *synthesis* algorithm for circuits implementing a certain boolean function,
7. a reusable library of circuits (such as Deutsch, Deutsch-Jozsa, Bernstein-Vazirani, Simon, Grover, and Shor).

From those, we can make a set of design choices that drive the eventual solution.

We eventually want some non-functional characteristics to hold:

8. evaluation of reasonably-sized circuits should be relatively efficient.

4.2 Design

To meet the first requirement, we use *finally tagless* [**?**] to encode a *language of values*:

```
class (Show v, Enum v) => Value v where
  zero :: v
  one  :: v
```

```
  snot :: v -> v
  sand :: v -> v -> v
  sxor :: v -> v -> v

  -- has a default implementation
  snand :: [v] -> v -- n-ary and
  snand = foldr sand one
```

which is then implemented 4 times, once for `Bool` and then multiple times for different symbolic variations. As a side-effect, this gives us requirement 3 "for free" if we can write a sufficiently polymorphic evaluator (which we will present below).

Unlike for value representation which can be computed from context, we want to explicitly choose formula representation (requirement 2) ourselves. Thus we use an explicit record instead of an implicit dictionary:

```
data FormulaRepr f r = FR
  { fromVar  :: r -> f
  , fromVars :: Int -> r -> [ f ]
  }
```

The main methods are about *variable representation* `r` and how to insert them into the current *formula representation* `f`, singly or *n* at once.

A Generalized Toffoli gates can be represented by a list of representation of value accessors `br` (short for boolean representation) along with a list of *controls* that tell us whether to use the bit directly or negated, along with which value will potentially be flipped. The implementation of very common gates (negation and controlled not) are also shown.

```
data GToffoli br = GToffoli [Bool] [br] br

xop :: br -> GToffoli br
xop = GToffoli [] []

cx :: br -> br -> GToffoli br
cx a = GToffoli [True] [a]
```

The core of a circuit (requirement 4) is then implemented as a sequence of these (where `Seq` is from Data.Sequence).

```
type OP br = Seq (GToffoli br)
```

Mainly for efficiency reasons, we model circuits as manipulating *locations holding values* rather than directly acting on values. We use `STRefs` (aliased to `Var`) for that purpose. Putting this together with the model of circuits of **??**, we get

```
data Circuit s v = Circuit
  { op          :: OP (Var s v)
  , xs          :: [Var s v]
  , ancillaIns  :: [Var s v]
  , ancillaOuts :: [Var s v]
  , ancillaVals :: [v]
  }
```

which lets us achieve requirement 5.

For requirement 6, we implement a straightforward version of the algorithm of [**?**]. Our implementation is *language agnostic*, in other words it works via the `Value` interface, so that the resulting circuits are all of type `OP br` for a free representation `br`. As circuit synthesis is only done for generating examples, we are not worried about its efficiency.

The arithmetic circuit generators are also based on classical algorithms, and are not optimized in any way, neither for running time nor for gate count. Neither are the code for the classical quantum algorithms. They are, however, representation polymorphic.

Above, we said we had 3 different symbolic evaluators. These were not driven by having different levels of *precision* but rather by requirement 8, efficiency. Our first evaluator (`FormAsList`) uses xor-lists of and-lists of literals (as strings, i.e. `"x0"`, `"x1"`, ... in lexicographical order of the wires). ANF is then easy: and-lists are sorted, and duplicates removed. Xor-lists are sorted, grouped, even length lists are removed, and then made unique. This is whoefully inefficient, and was the clear bottleneck in our profiles.

A less naïve approach uses a set of bits for representing literals, an `IntSet` for and-lists, and a normalized multiset for xor maps. We found more efficient to use a multiset for intermediate computations with xor maps which is normalized at the end instead of trying to track even/odd number of occurences. Only computing Cartesian products in this representation requires some thought for finding a reasonably efficient algorithm.

While significantly faster, this was still not sufficiently efficient. Our final representation uses Natural numbers as and-maps where the encoding of literals is now positional, and xor maps are again multisets of these "bitmaps".

As a last optimization, our circuits have a very particular property: the control wires are not written to, so that they are all literals. This can be used to further optimize the evaluation of single gates.

4.3 Implementation

The final code consists of 18 modules that implement various services, see Fig. 3 for a full listing. It consists of only 1449 lines of Haskell code, of which 646 lines are blank, import or comments, module declaration, so that 809 are "code". Testing and printing utilities are not counted in the above.

The code that occupies the most volume is that for running the examples, as each circuit needs its own setup for the input and ancilla wires. Next is the implementation of symbolic representations of formulas in ANF. This is largely because there are a lot of pieces that need to be defined, including many instances; the algorithmic aspect rarely span more than 15 lines in total. The code for generating arithmetic circuits is voluminous as well as largely computational, but is a re-implementation of known material, as is the synthesis code.

Module	Service
`Value`	representation of a *language of values* (as a typeclass) and some constructors
`FormulaRepr`	abstract representation of formulas, as a mapping from abstract variables to abstract formulas
`Variable`	variables as locations holding values and their constructors
`ModularArith`	modular arithmetic utilities useful in implementing certain algorithms, like Shor's
`BoolUtils`	function to interpret a list of booleans as an Integer
`GToffoli`	representation of generalized Toffoli gates and some constructors
`Circuits`	representation of circuits (sequences of gates) and of the special "wires" of our circuits
`Synthesis`	synthesis algorithm for circuits with particular properties
`ArithCirc`	creation of arithmetic circuits
`EvalZ`	evaluation of circuits on concrete values
`FormAsList`	representation of formulas as xor-lists of and-lists of literals-as-strings
`FormAsMaps`	representation of formulas as xor-maps of and-maps of literals-as-Int
`FormAsBitmaps`	representation of formulas as xor-maps of bitmaps
`SymbEval`	Symbolic evaluation of circuits
`SymbEvalSpecialized`	Symbolic evaluation of circuits specialized to the representation from `FormAsBitmaps`
`QAlgos`	generating the circuits themselves
`RunQAlgos`	running the actual circuits
`Trace`	utilities for tracing and debugging

Figure 3. Modules and their services

A few comments on further implementation details. Sharp readers might have noticed `snand` as defined in class `Value` instead of as a polymorphic function outside the class; we do this to enable its implementation to be overridden. Lastly, `GToffoli`'s implementation relies on an unexpressed invariant: that its two lists are of equal length. We really ought to refactor the code to use a single list of tuples, but this is a pervasive change that would not bring much benefit as we use combinators to build circuits, and these already maintain that invariant.

5 Evaluation

Algorithms.

We implement six well-known quantum algorithms: Deutsch, Deutsch-Jozsa, Bernstein-Vazirani, Simon, Grover, and Shor. We highlight the salient results for the first five algorithms, and then discuss the most interesting case of Shor's algorithm in detail.

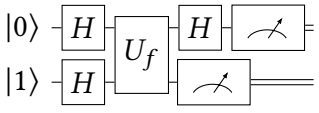


Figure 4. Quantum Circuit for the Deutsch-Jozsa Algorithm ($n = 1$)

De-Quantization. We abbreviate the set $\{0, 1, \ldots, (n-1)\}$ as $[\mathbf{n}]$. In the Deutsch-Jozsa problem, we are given a function $[\mathbf{2^n}] \to [2]$ that is promised to be constant or balanced and we need to distinguish the two cases. The quantum circuit Fig. 4 shows the algorithm for the case $n = 1$. Instead of the conventional execution, we perform a retrodictive execution of the U_f block with an ancilla measurement 0, i.e., with the state $|x_{n-1} \cdots x_1 x_0 0\rangle$. The result of the execution is a symbolic formula r that determines the conditions under which $f(x_{n-1}, \cdots, x_0) = 0$. When the function is constant, the results are $0 = 0$ (always) or $1 = 0$ (never). When the function is balanced, we get a formula that mentions the relevant variables. For example, here are the results of three executions for balanced functions $[\mathbf{2^6}] \to [2]$:

- $x_0 = 0$,
- $x_0 \oplus x_1 \oplus x_2 \oplus x_3 \oplus x_4 \oplus x_5 = 0$, and
- $1 \oplus x_3x_5 \oplus x_2x_4 \oplus x_1x_5 \oplus x_0x_3 \oplus x_0x_2 \oplus x_3x_4x_5 \oplus x_2x_3x_5 \oplus x_1x_3x_5 \oplus x_0x_3x_5 \oplus x_0x_1x_4 \oplus x_0x_1x_2 \oplus x_2x_3x_4x_5 \oplus x_1x_3x_4x_5 \oplus x_1x_2x_4x_5 \oplus x_1x_2x_3x_5 \oplus x_0x_3x_4x_5 \oplus x_0x_2x_4x_5 \oplus x_0x_2x_3x_5 \oplus x_0x_1x_4x_5 \oplus x_0x_1x_3x_5 \oplus x_0x_1x_3x_4 \oplus x_0x_1x_2x_4 \oplus x_0x_1x_2x_4x_5 \oplus x_0x_1x_2x_3x_5 \oplus x_0x_1x_2x_3x_4 = 0$.

In the first case, the function is balanced because it produces 0 exactly when $x_0 = 0$ which happens half of the time in all possible inputs; in the second case the output of the function is the exclusive-or of all the input variables which is another easy instance of a balanced function. The last case is a cryptographically strong balanced function whose output pattern is balanced but, by design, difficult to discern [10].

An important insight is that we actually do not care about the exact formula. Indeed, since we are promised that the function is either constant or balanced, then any formula that refers to at least one variable must indicate a balanced function. In other words, the outcome of the algorithm can be immediately decided if the formula is anything other than 0 or 1. Indeed, our implementation correctly identifies all 12870

balanced functions $[2^4] \to [2]$. This is significant as some of these functions produce complicated entangled patterns during quantum evolution and could not be de-quantized using previous approaches [1]. A word of caution though: our results assume a "white-box" complexity model rather than a "black-box" complexity model [24].

The discussion above suggests that the details of the equations may not be particularly relevant for some algorithms. This would be crucial as the satisfiability of general boolean equations is, in general, an *NP*-complete problem [12, 21, 31]. Fortunately, this observation does hold for other algorithms as well including the Bernstein-Vazirani algorithm and Grover's algorithm. In both cases, the result can be immediately read from the formula. In the Bernstein-Vazirani case, formulae are guaranteed to be of the form $x_1 \oplus x_3 \oplus x_4 \oplus x_5$; the secret string is then the binary number that has a 1 at the indices of the relevant variables $\{1, 3, 4, 5\}$. In the case for Grover, because there is a unique input u for which $f(u) = 1$, the ANF formula must include a subformula matching the binary representation of u, and in fact that subformula is guaranteed to be the shortest one as shown in Fig. 5.

Shor's Algorithm. The circuit in Fig. 7 uses a hand-optimized implementation of quantum oracle U_f for the modular exponentiation function $f(x) = 4^x \mod 15$ to factor 15 using Shor's algorithm. The white dot in the graphical representation of the first indicates that the control is active when it is 0. In a conventional forward execution, the state before the QFT block is:

$$\frac{1}{2\sqrt{2}}((|0\rangle + |2\rangle + |4\rangle + |6\rangle)\,|1\rangle + (|1\rangle + |3\rangle + |5\rangle + |7\rangle)\,|4\rangle)$$

At this point, the ancilla register is measured to either $|1\rangle$ or $|4\rangle$. In either case, the computational register snaps to a state of the form $\sum_{r=0}^{3} |a + 2r\rangle$ whose QFT has peaks at $|0\rangle$ or $|4\rangle$ making them the most likely outcomes of measurements of the computational register. If we measure $|0\rangle$, we repeat the experiment; otherwise we infer that the period is 2.

In the retrodictive execution, we can start with the state $|x_2x_1x_0001\rangle$ since 1 is guaranteed to be a possible ancilla measurement (corresponding to $f(0)$). The first CX-gate changes the state to $|x_2x_1x_0x_001\rangle$ and the second CX-gate produces $|x_2x_1x_0x_00x_0\rangle$. At that point, we reconcile the retrodictive result of the ancilla register $|x_00x_0\rangle$ with the initial condition $|000\rangle$ to conclude that $x_0 = 0$. In other words, in order to observe the ancilla at 001, the computational register must be initialized to a superposition of the form $|??0\rangle$ where the least significant bit must be 0 and the other two bits are unconstrained. Expanding the possibilities, the first register needs to be in a superposition of the states $|000\rangle$, $|010\rangle$, $|100\rangle$ or $|110\rangle$ and we have just inferred using purely classical but retrodictive reasoning that the period is 2.

This result does not, in fact, require the small optimized circuit of Fig. 7. In our implementation, modular exponentiation circuits are constructed from first principles using adders and multipliers [32]. In the case of $f(x) = 4^x \mod 15$, although the unoptimized constructed circuit has 56,538 generalized Toffoli gates (controlled^n-not gates for all n), the execution results in just two simple equations: $x_0 = 0$ and $1 \oplus x_0 = 1$. Furthermore, as shown in Fig. 6, the shape and size of the equations is largely insensitive to the choice of 4 as the base of the exponent, leading in all cases to the immediate conclusion that the period is either 2 or 4. When the solution is $x_0 = 0$, the period is 2, and when it is $x_0 = x_1 = 0$, the period is 4.

The remarkable effectiveness of retrodictive computation of the Shor instance for factoring 15 is due to a coincidence: a period that is a power of 2 is clearly trivial to represent in the binary number system which, after all is expressly designed for that purpose. That coincidence repeats itself when factoring products of the (known) Fermat primes: 3, 5, 17, 257, and 65537, and leads to small circuits [16]. This is confirmed with our implementation which smoothly deals with unoptimized circuits for factoring such products. Factoring 3*17=51 using the unoptimized circuit of 177,450 generalized Toffoli gates produces just the 4 equations: $1 \oplus x_1 = 1$, $x_0 = 0$, $x_0 \oplus x_0x_1 = 0$, and $x_1 \oplus x0x1 = 0$. Even for 3*65537=196611 whose circuit has 4,328,778 generalized Toffoli gates, the execution produces 16 small equations that refer to just the four variables x_0, x_1, x_2, and x_3 constraining them to be all 0, i.e., asserting that the period is 16.

Since periods that are powers of 2 are rare and special, we turn our attention to factoring problems with other periods. The simplest such problem is that of factoring 21 with an underlying function $f(x) = 4^x \mod 21$ of period 3. The unoptimized circuit constructed from the first principles has 78,600 generalized Toffoli gates; its execution generates just three equations. But even in this rather trivial situation, the equations span 5 pages of text! (Supplementary Material). A small optimization reducing the number of qubits results in a circuit of 15,624 generalized Toffoli gates whose execution produces still quite large, but more reasonable, equations (Supplementary Material). To understand the reason for these unwieldy equations, we examine a general ANF formula of the form $X_1 \oplus X_2 \oplus X_3 \oplus \ldots = 0$ where each X_i is a conjunction of some boolean variables, i.e., the variables in each X exhibit constructive interference as they must all be true to enable that $X = 1$. Since the entire formula must equal to 0, every $X_i = 1$ must be offset by another $X_j = 1$, thus exhibiting negative interference among X_i and X_j. Generally speaking, arbitrary interference patterns can be encoded in the formulae at the cost of making the size of the formulae exponential in the number of variables. This exponential blowup is actually a necessary condition for any quantum algorithm that can offer an exponential speed-up over classical computation [20].

$u = 0$ $1 \oplus x_3 \oplus x_2 \oplus x_1 \oplus x_0 \oplus x_2x_3 \oplus x_1x_3 \oplus x_1x_2 \oplus x_0x_3 \oplus x_0x_2 \oplus x_0x_1 \oplus x_1x_2x_3 \oplus x_0x_2x_3 \oplus x_0x_1x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 1$ $x_0 \oplus x_0x_3 \oplus x_0x_2 \oplus x_0x_1 \oplus x_0x_2x_3 \oplus x_0x_1x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 2$ $x_1 \oplus x_1x_3 \oplus x_1x_2 \oplus x_0x_1 \oplus x_1x_2x_3 \oplus x_0x_1x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 3$ $x_0x_1 \oplus x_0x_1x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 4$ $x_2 \oplus x_2x_3 \oplus x_1x_2 \oplus x_0x_2 \oplus x_1x_2x_3 \oplus x_0x_2x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 5$ $x_0x_2 \oplus x_0x_2x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 6$ $x_1x_2 \oplus x_1x_2x_3 \oplus x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 7$ $x_0x_1x_2 \oplus x_0x_1x_2x_3$

$u = 8$ $x_3 \oplus x_2x_3 \oplus x_1x_3 \oplus x_0x_3 \oplus x_1x_2x_3 \oplus x_0x_2x_3 \oplus x_0x_1x_3 \oplus x_0x_1x_2x_3$

$u = 9$ $x_0x_3 \oplus x_0x_2x_3 \oplus x_0x_1x_3 \oplus x_0x_1x_2x_3$

$u = 10$ $x_1x_3 \oplus x_1x_2x_3 \oplus x_0x_1x_3 \oplus x_0x_1x_2x_3$

$u = 11$ $x_0x_1x_3 \oplus x_0x_1x_2x_3$

$u = 12$ $x_2x_3 \oplus x_1x_2x_3 \oplus x_0x_2x_3 \oplus x_0x_1x_2x_3$

$u = 13$ $x_0x_2x_3 \oplus x_0x_1x_2x_3$

$u = 14$ $x_1x_2x_3 \oplus x_0x_1x_2x_3$

$u = 15$ $x_0x_1x_2x_3$

Figure 5. Result of retrodictive execution for the Grover oracle ($n = 4$, w in the range $\{0..15\}$). The highlighted red subformula is the binary representation of the hidden input u.

Base	Equations			Solution
$a = 11$	$x_0 = 0$			$x_0 = 0$
$a = 4, 14$	$1 \oplus x_0 = 1$	$x_0 = 0$		$x_0 = 0$
$a = 7, 13$	$1 \oplus x_1 \oplus x_0x_1 = 1$	$x_0x_1 = 0$	$x_0 \oplus x_1 \oplus x_0x_1 = 0$	$x_0 = x_1 = 0$
$a = 2, 8$	$1 \oplus x_0 \oplus x_1 \oplus x_0x_1 = 1$	$x_0x_1 = 0$	$x_1 \oplus x_0x_1 = 0$	$x_0 \oplus x_0x_1 = 0$	$x_0 = x_1 = 0$

Figure 6. Equations generated by retrodictive execution of $a^x \mod 15$ for different values of a, starting from observed result 1 and unknown $x_8x_7x_6x_5x_4x_3x_2x_1x_0$. The solution for the unknown variables is given in the last column.

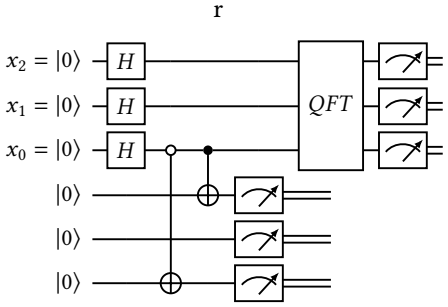


Figure 7. Finding the period of $4^x \mod 15$

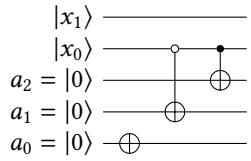


Figure 8. Finding the period of $4^x \mod 21$ using qutrits. The three gates are from left to right are the X, SUM, and $C(X)$ gates for ternary arithmetic [8]. The X gate adds 1 modulo 3; the controlled version $C(X)$ only increments when the control is equal to 2, and the SUM gates maps $|a, b\rangle$ to $|a, a + b\rangle$.

It would however be incorrect to conclude that factoring 21 is inherently harder than factoring 15. The issue is simply that the binary number system is well-tuned to expressing patterns over powers of 2 but a very poor match for expressing patterns over powers of 3. Indeed, we show that by just using qutrits, the circuit and equations for factoring 21 become trivial while those for factoring 15 become unwieldy. The manually optimized circuit in Fig. 8 consists of just three gates; its retrodictive execution produces two equations: $x_0 = 0$ and $x_0 \neq 2$, setting $x_0 = 0$ and leaving x_1 unconstrained. The matching values in the qutrit system as 00, 10, 20 or in decimal 0, 3, 6 clearly identifying the period to be 3. The idea of adapting the computation to simplify the circuit and equations is inspired by the fact that entanglement is relative to a particular tensor product decomposition (Methods).

6 Conclusion

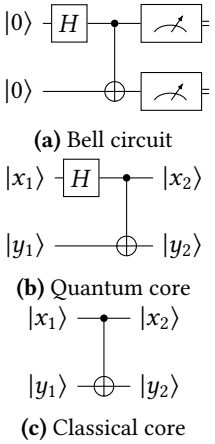
7 Old

Quantum evolution is time-reversible and yet little advantage is gained from this fact in the circuit model of quantum computing. Indeed, most quantum algorithms expressed in the circuit model compute strictly from the present to the future, preparing initial states and proceeding forward with unitary transformations and measurements. In contrast, retrodictive quantum theory [5], retrocausality [2], and the time-symmetry of physical laws [33] all suggest that quantum computation embodies richer –untapped– modes of computation, which could exploit knowledge about the future for a computational advantage.

Here we demonstrate that, in concert with the computational concepts of demand-driven lazy evaluation [18] and symbolic partial evaluation [15], retrodictive reasoning can indeed be used as a computational resource that exhibits richer modes of computation at the boundary of the classical/quantum divide. Specifically, instead of fully specifying the initial conditions of a quantum circuit and computing forward, it is possible to compute, classically, in both the forward and backward directions starting from partially specified initial and final conditions. Furthermore, this mixed mode of computation (i) can solve problems with fewer resources than the conventional forward mode of execution, sometimes even purely classically, (ii) can be expressed in a symbolic representation that immediately exposes global properties of the wavefunction that are needed for quantum algorithms, (iii) can lead to the de-quantization of some quantum algorithms, providing efficient classical algorithms inspired by their quantum counterparts, and (iv) reveals that the entanglement patterns inherent in genuine quantum algorithms with no known classical counterparts are artifacts of the chosen symbolic representation.

The main ideas underlying our contributions can be illustrated with the aid of the small examples in Fig. 9. In the conventional computational mode (Fig. 9a), the execution starts with the initial state $|00\rangle$. The first gate (Hadamard) evolves the state to $1/\sqrt{2}(|00\rangle + |10\rangle)$ which is transformed by the controlled-not (cx) gate to $1/\sqrt{2}(|00\rangle + |11\rangle)$. The measurements at the end produce 00 or 11 with equal probability. Fig. 9b keeps the quantum core of the circuit, removing the measurements, and naming the inputs and outputs with symbolic variables. Now, instead of setting $x_1 = y_1 = 0$ and computing forward as before, we can, for example, set $x_2 = 1$ and $y_2 = 0$ and calculate backwards as follows: $|10\rangle$ evolves in the backwards direction to $|11\rangle$ and then to $1/\sqrt{2}(|01\rangle - |11\rangle)$. In other words, in order to observe $x_2 y_2 = 10$, the variable x_1 should be prepared in the superposition $1/\sqrt{2}(|0\rangle - |1\rangle)$ and y_1 should be prepared in the state $|1\rangle$. More interestingly, we can partially specify the initial and final conditions. For example, we can fix $x_1 = 0$ and $x_2 = 1$ and ask if there are any possible values for y_1 and y_2 that would be consistent with this setting. To answer the question, we calculate, using the techniques of symbolic partial evaluation (Methods), as follows. The initial state is $|0y_1\rangle$ which evolves to $1/\sqrt{2}(|0y_1\rangle + |1y_1\rangle)$ and then to $1/\sqrt{2}(|0y_1\rangle + |1(1 \oplus y_1)\rangle)$ where $\oplus$ is the exclusive-or operation and $1 \oplus y_1$ is the canonical way of negating y_1 in the Algebraic Normal Form (ANF) of boolean expressions (Methods). This final state can now be reconciled with the specified final conditions $1y_2$ revealing that the settings are consistent provided that $y_2 = 1 \oplus y_1$. We can, in fact, go one step further and analyze the circuit without the Hadamard gate as shown in Fig. 9c. The reasoning is that the role of Hadamard is to introduce (modulo phase) uncertainty about whether $x_1 = 0$ or $x_1 = 1$. But, again modulo phase, the same uncertainty can be expressed by just using the variable x_1. Thus, in Fig. 9c, we can set $y_1 = 0$ and $y_2 = 1$ and ask about values of x_1 and x_2 that would be consistent with this setting. We can calculate backwards from $|x_2 1\rangle$ as follows. The state evolves to $|x_2(1 \oplus x_2)\rangle$ which can be reconciled with the initial conditions yielding the constraints $x_1 = x_2$ and $1 \oplus x_2 = 0$ whose solutions are $x_1 = x_2 = 1$.

These insights are robust and can be implemented in software (Methods) to analyze circuits with millions of gates for the quantum algorithms that match the template in Fig. 10 (including Deutsch, Deutsch-Jozsa, Bernstein-Vazirani, Simon, Grover, and Shor's algorithms [7, 13, 14, 17, 26, 28, 29]). The software is completely classical, performing mixed mode executions of the classical core of the circuits, i.e., the U_f block defined as $U_f(|x\rangle\,|y\rangle) = |x\rangle\,|f(x) \oplus y\rangle$. Specifically, in all these algorithms, the top collection of wires (which we will call the computational register) is prepared in a uniform

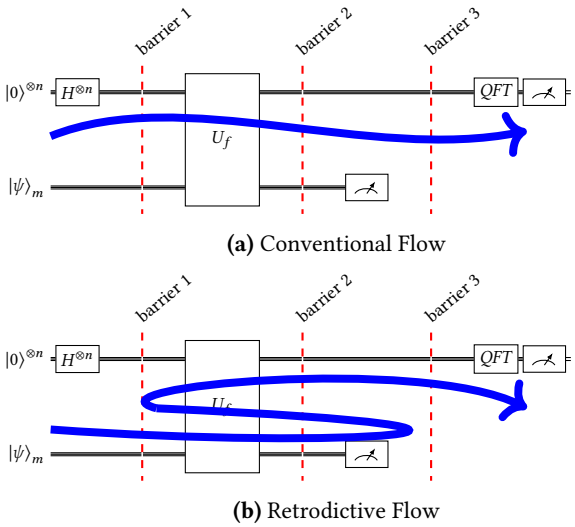


(a) Bell circuit

(b) Quantum core

(c) Classical core

Figure 9. A conventional quantum circuit with initial conditions and measurement (a); its quantum core without measurement and with unspecified initial and final conditions (b); and its classical core without explicit quantum superpositions (c).



(a) Conventional Flow

(b) Retrodictive Flow

Figure 10. Template quantum circuit

superposition which can be represented using symbolic variables. The measurement of the bottom collection of wires (which we call the ancilla register) after barrier 2 provides partial information about the future which is, together with the initial conditions of the ancilla register, sufficient to symbolically execute the circuit. In each case, instead of the conventional execution flow depicted in Fig. 10(a), we find a possible measurement outcome w at barrier (2) and perform a symbolic retrodictive execution with a state $|xw\rangle$ going backwards to collect the constraints on x that enable us to solve the problem in question.

Methods

Symbolic Execution of Classical Programs. A well-established technique to simultaneously explore multiple paths that a classical program could take under different inputs is *symbolic execution* [4, 9, 11, 19, 22]. In this execution scheme, concrete values are replaced by symbols which are initially unconstrained. As the execution proceeds, the symbols interact with program constructs and this typically introduces constraints on the possible values that the symbols represent. At the end of the execution, these constraints can be solved to infer properties of the program under consideration.

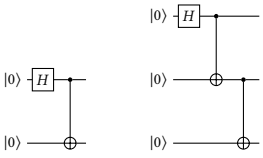


Figure 11. Bell and GHZ States

Entanglement. A symbolic variable represents a boolean value that can be 0 or 1; this is similar to a qubit in a superposition $(1/\sqrt{2})(|0\rangle \pm |1\rangle)$. Thus, it appears that $H|0\rangle$ could be represented by a symbol x to denote the uncertainty. Surprisingly, this idea scales to even represent maximally entangled states. Fig. 11 (left) shows a circuit to generate the Bell state $(1/\sqrt{2})(|00\rangle + |11\rangle)$. By using the symbol x for $H|0\rangle$, the input to the cx-gate is $|x0\rangle$ which evolves to $|xx\rangle$. By sharing the same symbol in two positions, the symbolic state accurately represents the entangled Bell state. Similarly, for the circuit in Fig. 11 (right), the state after the Hadamard gate is $|x00\rangle$ which evolves to $|xx0\rangle$ and then to $|xxx\rangle$ again accurately capturing the entanglement correlations.

Given a maximally entangled state defined with respect to a particular tensor product decomposition, the same state may become unentangled in a different tensor product decomposition. Given the state:

$$|\Psi\rangle = |0\rangle + |3\rangle + |6\rangle + |9\rangle + |12\rangle + |15\rangle\,,$$

one can find a 4-qubit representation ($\mathcal{H} = \bigotimes_{i=1}^{4} \mathbb{C}^2$)

$$|\Psi\rangle = |0000\rangle + |0011\rangle + |0110\rangle + |1001\rangle + |1100\rangle + |1111\rangle\,,$$

where we used the following map $|m\rangle = \sum_{i=0}^{3} x_i 2^i$, with $m \in \mathbb{Z}$ and $x_i = 0, 1$. One can use the purity [6]

$$P_{|\Psi\rangle} = \frac{1}{4} \sum_{i=1}^{4} \sum_{\mu=x,y,z} \langle\Psi|\sigma_i^{\mu}|\Psi\rangle^2$$

and confirm that the state $|\Psi\rangle$ is maximally entangled, i.e., has $P_{|\Psi\rangle} = 0$. In contrast, in a qutrit basis ($\mathcal{H} = \bigotimes_{i=1}^{4} \mathbb{C}^3$), given the map $|m\rangle = \sum_{i=0}^{3} x_i 3^i$, with $x_i = 0, 1, 2$, the state

$$\begin{aligned} |\Psi\rangle &= |0000\rangle + |0010\rangle + |0020\rangle + |0100\rangle + |0110\rangle + |0120\rangle \\ &= |0\rangle \otimes (|0\rangle + |1\rangle) \otimes (|0\rangle + |1\rangle + |2\rangle) \otimes |0\rangle\,, \end{aligned}$$

is a product (unentangled) state.

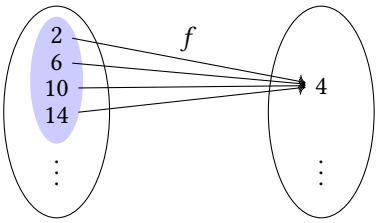


Figure 12. The pre-image of 4 under $f(x) = 7^x \mod 15$.

Complexity Analysis. Given finite sets A and B, a function $f : A \to B$ and an element $y \in B$, we define $\{\cdot \xleftarrow{f} y\}$, the pre-image of y under f, as the set $\{x \in A \mid f(x) = y\}$. For example, let $A = B = [2^4]$ and let $f(x) = 7^x \mod 15$, then the collection of values that f maps to 4, $\{\cdot \xleftarrow{f} 4\}$, is the set $\{2, 6, 10, 14\}$ as shown in Fig. 12. Symbolic retrodictive execution can be seen as a method to generate boolean

formulae that describe the pre-image of the function f under study. For the example in Fig. 12, retrodictive execution might generate the formulae $x_1 = 1$ and $x_0 = 0$. The (trivial in this case) solution for the formulae is indeed the set $\{2, 6, 10, 14\}$. The critical points to note, however, are that: (i) solving the equations describing the pre-image is in general an intractable (even for quantum computers) *NP*-complete problem, and (ii) solving the equations is not needed for typical quantum algorithms. *Only some global properties of the pre-image are needed!* Indeed, we have already seen that for solving the Deutsch-Jozsa problem, the only thing needed was whether the formula contains some variables. For the Bernstein-Vazirani problem, the only thing needed was the indices of the variables occurring in the formula. For Grover's algorithm, we only need to extract the singleton element in the pre-image and for Shor's algorithm we "only" need to extract the periodicity of the elements in the pre-image.

To appreciate the difficulty of computing pre-images in general, note that finding the pre-image of a function subsumes several challenging computational problems such as pre-image attacks on hash functions [27], predicting environmental conditions that allow certain reactions to take place in computational biology [3, 23], and finding the pre-image of feature vectors in the space induced by a kernel in neural networks [25]. More to the point, the boolean satisfiability problem SAT is expressible as a boolean function over the input variables and solving a SAT problem is asking for the pre-image of true. Indeed, based on the conjectured existence of one-way functions which itself implies $P \neq NP$, all these pre-images calculations are believed to be computationally intractable in their most general setting.

The bottleneck in retrodictive execution, as we presented it, is therefore in the symbolic execution of circuits in the ANF domain. Not only does the execution scale with the number of gates in the circuit but also with the size of the intermediate ANF formulae, which could become exponential in the number of variables.

Software. The entire suite of programs including synthesis of reversible circuits, standard evaluation, retrodictive evaluation under various modes, testing, debugging, and alternative representations off ANF formulae is only 1,500 lines of Haskell. The heart of the implementation is this simple function:

```
peG :: Value v => GToffoli s v -> ST s ()
peG (GToffoli bs cs t) = do
  controls <- mapM readSTRef cs
  tv <- readSTRef t
  let funs = map (\b -> if b then id else snot) bs
  let r = sxor tv (foldr sand one (zipWith ($) funs controls))
  writeSTRef t r
```

The function performs symbolic evaluation of one generalized Toffoli gates, reading the current ANF formulae for each control and producing an appropriate ANF formula for the target.

Supplementary Information. The equations generated by retrodictive execution of the optimized circuit for 4^x mod 21 starting from observed result 1 and unknown x are:

$$1 \oplus x_0 \oplus x_1 \oplus x_2 \oplus x_0x_2 \oplus x_0x_1x_2 \oplus x_3 \oplus x_1x_3 \oplus x_0x_1x_3 \oplus x_0x_2x_3 \oplus x_1x_2x_3 \oplus x_4 \oplus x_0x_4 \oplus x_0x_1x_4 \oplus x_2x_4 \oplus x_1x_2x_4 \oplus x_0x_1x_2x_4 \oplus x_0x_3x_4 \oplus x_1x_3x_4 \oplus x_2x_3x_4 \oplus x_0x_2x_3x_4 \oplus x_0x_1x_2x_3x_4 \oplus x_5 \oplus x_1x_5 \oplus x_0x_1x_5 \oplus x_0x_2x_5 \oplus x_1x_2x_5 \oplus x_3x_5 \oplus x_0x_3x_5 \oplus x_0x_1x_3x_5 \oplus x_2x_3x_5 \oplus x_1x_2x_3x_5 \oplus x_0x_1x_2x_3x_5 \oplus x_0x_4x_5 \oplus x_1x_4x_5 \oplus x_2x_4x_5 \oplus x_0x_2x_4x_5 \oplus x_0x_1x_2x_4x_5 \oplus x_3x_4x_5 \oplus x_1x_3x_4x_5 \oplus x_0x_1x_3x_4x_5 \oplus x_0x_2x_3x_4x_5 \oplus x_1x_2x_3x_4x_5 = 1$$

$$x_1 \oplus x_0x_1 \oplus x_0x_2 \oplus x_1x_2 \oplus x_3 \oplus x_0x_3 \oplus x_0x_1x_3 \oplus x_2x_3 \oplus x_1x_2x_3 \oplus x_0x_1x_2x_3 \oplus x_0x_4 \oplus x_1x_4 \oplus x_2x_4 \oplus x_0x_2x_4 \oplus x_0x_1x_2x_4 \oplus x_3x_4 \oplus x_1x_3x_4 \oplus x_0x_1x_3x_4 \oplus x_0x_2x_3x_4 \oplus x_1x_2x_3x_4 \oplus x_5 \oplus x_0x_5 \oplus x_0x_1x_5 \oplus x_2x_5 \oplus x_1x_2x_5 \oplus x_0x_1x_2x_5 \oplus x_0x_3x_5 \oplus x_1x_3x_5 \oplus x_2x_3x_5 \oplus x_0x_2x_3x_5 \oplus x_0x_1x_2x_3x_5 \oplus x_4x_5 \oplus x_1x_4x_5 \oplus x_0x_1x_4x_5 \oplus x_0x_2x_4x_5 \oplus x_1x_2x_4x_5 \oplus x_3x_4x_5 \oplus x_0x_3x_4x_5 \oplus x_1x_3x_4x_5 \oplus x_2x_3x_4x_5 \oplus x_1x_2x_3x_4x_5 \oplus x_0x_1x_2x_3x_4x_5 = 0$$

$$x_0 \oplus x_0x_1 \oplus x_2 \oplus x_1x_2 \oplus x_0x_1x_2 \oplus x_0x_3 \oplus x_1x_3 \oplus x_2x_3 \oplus x_0x_2x_3 \oplus x_0x_1x_2x_3 \oplus x_4 \oplus x_1x_4 \oplus x_0x_1x_4 \oplus x_0x_2x_4 \oplus x_1x_2x_4 \oplus x_3x_4 \oplus x_0x_3x_4 \oplus x_0x_1x_3x_4 \oplus x_2x_3x_4 \oplus x_1x_2x_3x_4 \oplus x_0x_1x_2x_3x_4 \oplus x_0x_5 \oplus x_1x_5 \oplus x_2x_5 \oplus x_0x_2x_5 \oplus x_0x_1x_2x_5 \oplus x_3x_5 \oplus x_1x_3x_5 \oplus x_0x_1x_3x_5 \oplus x_0x_2x_3x_5 \oplus x_1x_2x_3x_5 \oplus x_4x_5 \oplus x_0x_4x_5 \oplus x_0x_1x_4x_5 \oplus x_2x_4x_5 \oplus x_1x_2x_4x_5 \oplus x_0x_1x_2x_4x_5 \oplus x_0x_3x_4x_5 \oplus x_1x_3x_4x_5 \oplus x_2x_3x_4x_5 \oplus x_0x_2x_3x_4x_5 \oplus x_0x_1x_2x_3x_4x_5 = 0$$

The equations generated by retrodictive execution of the unoptimized $4^x \mod 21$ starting from observed result 1 and unknown x. The circuit consists of 36,400 CX-gates, 38,200 CCX-gates, and 4,000 CCCX-gates. There are only three equations but each equation is exponentially large.

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