Representing, Manipulating and Optimizing Reversible Circuits

Jacques Carette Amr Sabry

McMaster University

Indiana University

June 11, 2015

Quantum Computing

Quantum physics differs from classical physics in many ways:

- Superpositions
- Entanglement
- Unitary evolution
- Composition uses tensor products
- Non-unitary measurement

Quantum Computing & Programming Languages

- It is possible to adapt all at once classical programming languages to quantum programming languages.
- Some excellent examples discussed in this workshop
- This assumes that classical programming languages (and implicitly classical physics) can be smoothly adapted to the quantum world.
- There are however what appear to be fundamental differences between the classical and quantum world that make them incompatible
- Let us *re-think* classical programming foundations before jumping to the quantum world.

Resource-Aware Classical Computing

- The biggest questionable assumption of classical programming is that it is possible to freely copy and discard information
- A classical programming language which respects no-cloning and no-discarding is the right foundation for an eventual quantum extension
- We want these properties to be inherent in the language; not an afterthought filtered by a type system
- We want to program with isomorphisms or equivalences
- The simplest instance is permutations between finite types which happens to correspond to reversible circuits.

Representing Reversible Circuits

truth table, matrix, reed muller expansion, product of cycles, decision diagram, etc.

[any easy way to reproduce Figure 4 on p.7 of Saeedi and Markov? —JC] [important remark: these are all *Boolean* circuits! —JC]

Most important part: reversible circuits are equivalent to permutations.

A (Foundational) Syntactic Theory

Ideally, want a notation that

- is easy to write by programmers
- is easy to mechanically manipulate
- can be reasoned about
- can be optimized.

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Start with a *foundational* syntactic theory on our way there:

- easy to explain
- clear operational rules
- fully justified by the semantics
- sound and complete reasoning
- sound and complete methods of optimization

Starting Point

Typed isomorphisms. First, a universe of (finite) types

```
\begin{array}{lll} \textbf{data} \ \textbf{U} : \textbf{Set where} \\ \textbf{ZERO} & : \ \textbf{U} \\ \textbf{ONE} & : \ \textbf{U} \\ \textbf{PLUS} & : \ \textbf{U} \rightarrow \textbf{U} \rightarrow \textbf{U} \\ \textbf{TIMES} : \ \textbf{U} \rightarrow \textbf{U} \rightarrow \textbf{U} \end{array}
```

and its interpretation

Equivalences and semirings

If we denote type equivalence by \simeq , then we can prove that

Theorem 1.

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We also get

Theorem 2.

If $A \simeq \text{Fin} m$, $B \simeq \text{Fin} n$ and $A \simeq B$ then $m \equiv n$.

(whose *constructive* proof is quite subtle).

Theorem 3.

If $A \simeq \text{Fin} m$ and $B \simeq \text{Fin} n$, then the type of all equivalences $A \simeq B$ is equivalent to the type of all permutations Permn.

Equivalences and semirings II

Semiring structures abound. We can define them on:

- equivalences (disjoint union and cartesian product)
- permutations (disjoint union and tensor product)

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The point, of course, is that they are related:

Theorem 4.

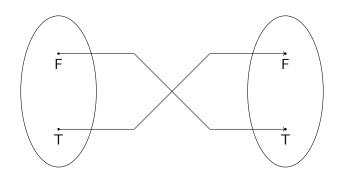
The equivalence of Theorem 3 is an isomorphism between the semirings of equivalences of finite types, and of permutations.

A Calculus of Permutations

First conclusion: it might be useful to *reify* a certain set of equivalences as combinators. We choose the fundamental "proof rules" of semirings:

```
data \longleftrightarrow : U \to U \to Set where
      u\overline{nite}_{+} : \{t: U\} \rightarrow PLUS ZERO t \longleftrightarrow t
      uniti_{+} : \{t: U\} \rightarrow t \longleftrightarrow PLUS ZERO t
      swap_{+}: \{t_1 \ t_2: U\} \rightarrow PLUS \ t_1 \ t_2 \longleftrightarrow PLUS \ t_1
      \mathsf{assocl}_+: \{t_1\ t_2\ t_3: \mathsf{U}\} \to \mathsf{PLUS}\ t_1\ (\mathsf{PLUS}\ t_2\ t_3) \longleftrightarrow \mathsf{PLUS}\ (\mathsf{PLUS}\ t_1\ t_2)\ t_3
      \mathsf{assocr}_+: \{t_1\ t_2\ t_3: \ \mathsf{U}\} \to \mathsf{PLUS}\ (\mathsf{PLUS}\ t_1\ t_2)\ t_3 \longleftrightarrow \mathsf{PLUS}\ t_1\ (\mathsf{PLUS}\ t_2\ t_3)
      unite* : \{t: U\} \rightarrow TIMES ONE t \longleftrightarrow t
      uniti\star : \{t: U\} \rightarrow t \longleftrightarrow TIMES ONE t
      swap* : \{t_1 \ t_2 : U\} \rightarrow TIMES \ t_1 \ t_2 \longleftrightarrow TIMES \ t_2 \ t_1
      assocl★: \{t_1 \ t_2 \ t_3 : U\} → TIMES t_1 (TIMES t_2 \ t_3) ←→ TIMES (TIMES t_1 \ t_2) t_3
      assocr \star : \{t_1 \ t_2 \ t_3 : U\} \rightarrow TIMES (TIMES \ t_1 \ t_2) \ t_3 \longleftrightarrow TIMES \ t_1 (TIMES \ t_2 \ t_3)
      absorbr : \{t: U\} \rightarrow \mathsf{TIMES} \; \mathsf{ZERO} \; t \longleftrightarrow \mathsf{ZERO}
      absorbl : \{t: U\} \rightarrow TIMES \ t \ ZERO \longleftrightarrow ZERO
      factorzr : \{t : U\} \rightarrow ZERO \longleftrightarrow TIMES \ t \ ZERO
      factorzl : \{t: U\} \rightarrow ZERO \longleftrightarrow TIMES ZERO t
      dist : \{t_1 \ t_2 \ t_3 : U\} \rightarrow
            TIMES (PLUS t_1 t_2) t_3 \longleftrightarrow PLUS (TIMES t_1 t_3) (TIMES t_2 t_3)
      factor : \{t_1 \ t_2 \ t_3 : U\} \rightarrow
            PLUS (TIMES t_1 t_3) (TIMES t_2 t_3) \longleftrightarrow TIMES (PLUS t_1 t_2) t_3
      id \longleftrightarrow : \{t : U\} \to t \longleftrightarrow t
      \_\oplus\_ : \{t_1 \ t_2 \ t_3 \ t_4 : U\} \rightarrow
           (t_1 \longleftrightarrow t_3) \to (t_2 \longleftrightarrow t_4) \to (PLUS \ t_1 \ t_2 \longleftrightarrow PLUS \ t_3 \ t_4)
      _⊗_ : {t<sub>1</sub> t<sub>2</sub> t<sub>3</sub> t<sub>4</sub> : U} →
            (t_1 \longleftrightarrow t_3) \to (t_2 \longleftrightarrow t_4) \to (TIMES \ t_1 \ t_2 \longleftrightarrow TIMES \ t_3 \ t_4)
```

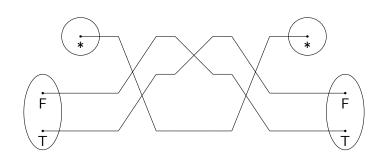
Example Circuit: Simple Negation



 $\mathsf{n}_1:\,\mathsf{BOOL}\longleftrightarrow\mathsf{BOOL}$

 $n_1 = \mathsf{swap}_+$

Example Circuit: Not So Simple Negation



```
\begin{array}{ll} n_2: \mathsf{BOOL} \longleftrightarrow \mathsf{BOOL} \\ n_2 = & \mathsf{uniti} \star \odot \\ & \mathsf{swap} \star \odot \\ & \left( \mathsf{swap}_+ \otimes \mathsf{id} \longleftrightarrow \right) \odot \\ & \mathsf{swap} \star \odot \\ & \mathsf{unite} \star \end{array}
```

Reasoning about Example Circuits

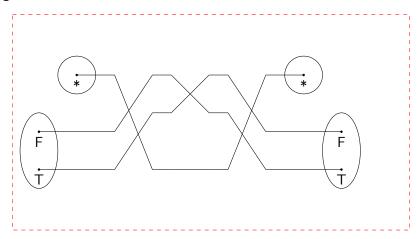
Algebraic manipulation of one circuit to the other:

```
negEx : n_2 \Leftrightarrow n_1
\mathsf{negEx} = \mathsf{uniti} \star \odot (\mathsf{swap} \star \odot ((\mathsf{swap}_+ \otimes \mathsf{id} \longleftrightarrow) \odot (\mathsf{swap} \star \odot \mathsf{unite} \star)))
             ⇔⟨ id⇔ ⊡ assoc⊙l ⟩
      uniti\star \odot ((swap \star \odot (swap_+ \otimes id \longleftrightarrow)) \odot (swap \star \odot unite \star))
            \Leftrightarrow \langle id \Leftrightarrow \boxdot (swapl \star \Leftrightarrow \boxdot id \Leftrightarrow) \rangle
      uniti \star \odot (((id \longleftrightarrow \otimes swap_+) \odot swap \star) \odot (swap \star \odot unite \star))
             \Leftrightarrow \langle id \Leftrightarrow \Box assoc \odot r \rangle
      uniti \star \odot ((id \longleftrightarrow \otimes swap_+) \odot (swap \star \odot (swap \star \odot unite \star)))
             \Leftrightarrow \langle id \Leftrightarrow \boxdot (id \Leftrightarrow \boxdot assoc \odot I) \rangle
      uniti \star \odot ((id \longleftrightarrow \otimes swap_+) \odot ((swap \star \odot swap \star) \odot unite \star))
             \Leftrightarrow \langle id \Leftrightarrow \boxdot (id \Leftrightarrow \boxdot (linv \odot l \boxdot id \Leftrightarrow)) \rangle
      uniti \star \odot ((id \longleftrightarrow \otimes swap_+) \odot (id \longleftrightarrow \odot unite \star))
             \Leftrightarrow \langle id \Leftrightarrow \boxdot (id \Leftrightarrow \boxdot idl \odot l) \rangle
      uniti \star \odot ((id \longleftrightarrow \otimes swap_+) \odot unite \star)
            ⇔⟨ assoc⊙l ⟩
      (uniti \star \odot (id \longleftrightarrow \otimes swap_+)) \odot unite \star
             \Leftrightarrow \langle \text{ unitil} \star \Leftrightarrow \boxdot \text{ id} \Leftrightarrow \rangle
                                                                                                         4 D > 4 B > 4 E > 4 E > 9 Q P
       (swap⊥ ⊙ uniti*) ⊙ unite*
```

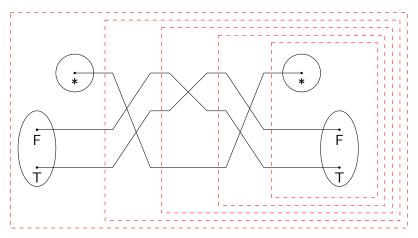
Reasoning about Example Circuits

foo

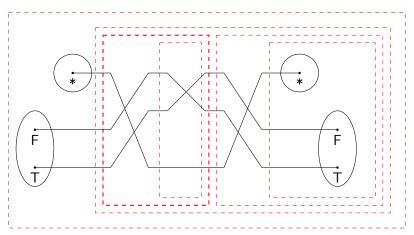
Original circuit:



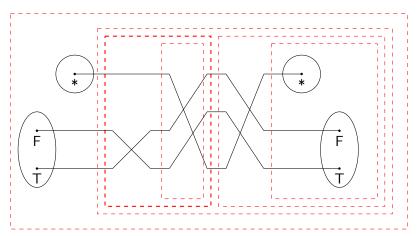
Making grouping explicit:



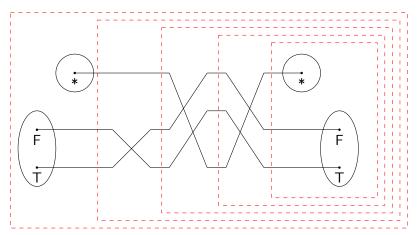
By associativity:



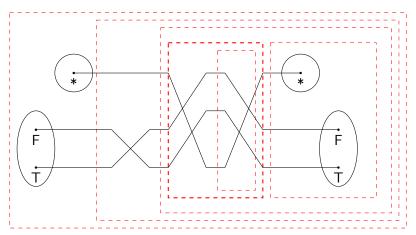
By pre-post-swap:



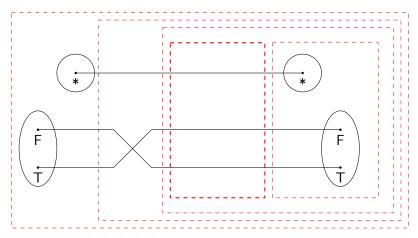
By associativity:



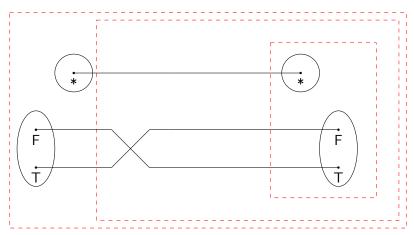
By associativity:



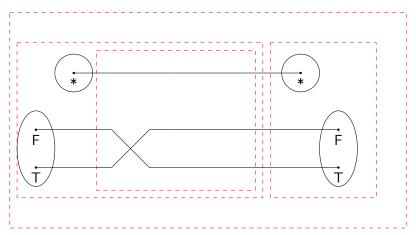
By swap-swap:



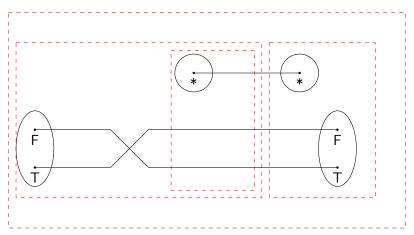
By id-compose-left:



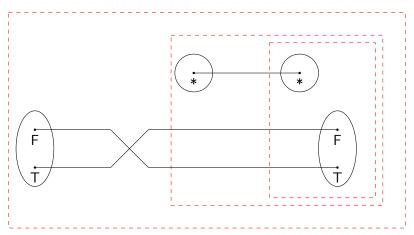
By associativity:



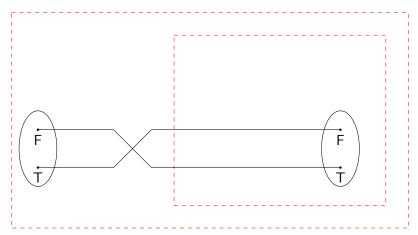
By swap-unit:



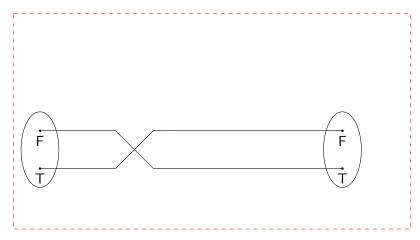
By associativity:



By unit-unit:



By id-unit-right:



Questions

- We don't want an ad hoc notation with ad hoc rewriting rules
- Notions of soundness; completeness; canonicity in some sense; what can we say?

1-paths vs. 2-paths

1-paths are between isomorphic types, e.g., A * B and B * A. List them all.

1-paths vs. 2-paths

2-paths are between 1-paths, e.g., %endcode

1-paths vs. 2-paths

