Representing, Manipulating and Optimizing Reversible Circuits

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Quantum Computing

Quantum physics differs from classical physics in many ways:

- Superpositions
- Entanglement
- Unitary evolution
- Composition uses tensor products
- Non-unitary measurement

Quantum Computing & Programming Languages

- It is possible to adapt all at once classical programming languages to quantum programming languages.
- Some excellent examples discussed in this workshop
- This assumes that classical programming languages (and implicitly classical physics) can be smoothly adapted to the quantum world.
- There are however what appear to be fundamental differences between the classical and quantum world that make them incompatible
- Let us *re-think* classical programming foundations before jumping to the quantum world.

Resource-Aware Classical Computing

- The biggest questionable assumption of classical programming is that it is possible to freely copy and discard information
- A classical programming language which respects no-cloning and no-discarding is the right foundation for an eventual quantum extension
- We want these properties to be inherent in the language; not an afterthought filtered by a type system
- We want to program with isomorphisms or equivalences
- The simplest instance is permutations between finite types which happens to correspond to reversible circuits.

A (Foundational) Syntactic Theory

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- is easy to write by programmers
- is easy to mechanically manipulate
- can be reasoned about
- can be optimized.

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Start with a *foundational* syntactic theory on our way there:

- easy to explain
- clear operational rules
- fully justified by the semantics
- sound and complete reasoning
- sound and complete methods of optimization

Starting Point

Typed isomorphisms. First, a universe of (finite) types

data U : Set where

Equivalences and semirings

If we denote type equivalence by \simeq , then we can prove that

Theorem 1.

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We also get

Theorem 2.

If $A \simeq \text{Fin} m$, $B \simeq \text{Fin} n$ and $A \simeq B$ then $m \equiv n$.

(whose *constructive* proof is quite subtle).

Theorem 3.

If $A \simeq \text{Fin} m$ and $B \simeq \text{Fin} n$, then the type of all equivalences $A \simeq B$ is equivalent to the type of all permutations Permn.

Equivalences and semirings II

Semiring structures abound. We can define them on:

- equivalences (disjoint union and cartesian product)
- permutations (disjoint union and tensor product)

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The equivalence of Theorem 3 is an isomorphism between the semirings of equivalences of finite types, and of permutations.

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A more evocative phrasing might be:

Theorem 5.

$$(A \simeq B) \simeq \operatorname{Perm}|A|$$

A Calculus of Permutations

First conclusion: it might be useful to *reify* a (sound and complete) set of equivalences as combinators, such as the fundamental "proof rules" of semirings:

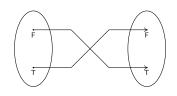
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```
data \longleftrightarrow : U \to U \to Set where
    unite_{+}: \{t: U\} \rightarrow PLUS ZERO t \longleftrightarrow t
    uniti+: \{t: U\} \rightarrow t \longleftrightarrow PLUS ZERO t
    swap_{+}: \{t_1 \ t_2: \ U\} \rightarrow PLUS \ t_1 \ t_2 \longleftrightarrow PLUS \ t_2 \ t_1
    |assocl_{+}: \{t_1, t_2, t_3: U\} \rightarrow PLUS t_1 (PLUS t_2, t_3) \longleftrightarrow PLUS (PLUS t_1, t_2) t_3
    \operatorname{assocr}_{\perp}: \{t_1 \ t_2 \ t_3: \ \mathsf{U}\} \to \mathsf{PLUS} \ (\mathsf{PLUS} \ t_1 \ t_2) \ t_3 \longleftrightarrow \mathsf{PLUS} \ t_1 \ (\mathsf{PLUS} \ t_2 \ t_3)
    unite* : \{t: U\} \rightarrow TIMES ONE t \longleftrightarrow t
    uniti* : \{t: U\} \rightarrow t \longleftrightarrow TIMES ONE t
    swap* : \{t_1 \ t_2 : U\} \rightarrow TIMES \ t_1 \ t_2 \longleftrightarrow TIMES \ t_1
    assocl⋆: \{t_1 \ t_2 \ t_3 : U\} → TIMES t_1 (TIMES t_2 \ t_3) ←→ TIMES (TIMES t_1 \ t_2) t_3
    assocr* : \{t_1 \ t_2 \ t_3 : \mathsf{U}\} \to \mathsf{TIMES} \ (\mathsf{TIMES} \ t_1 \ t_2) \ t_3 \longleftrightarrow \mathsf{TIMES} \ t_1 \ (\mathsf{TIMES} \ t_2 \ t_3)
    absorbr : \{t: U\} \rightarrow TIMES ZERO t \longleftrightarrow ZERO
    absorbl : \{t : U\} \rightarrow TIMES \ t \ ZERO \longleftrightarrow ZERO
    factorzr : \{t : U\} \rightarrow ZERO \longleftrightarrow TIMES \ t \ ZERO
    factorzl : \{t : U\} \rightarrow ZERO \longleftrightarrow TIMES ZERO t
                : \{t_1, t_2, t_3 : U\} \rightarrow TIMES (PLUS t_1, t_2) t_3 \longleftrightarrow PLUS (TIMES t_1, t_3) (TIMES t_2, t_3)
    factor : \{t_1 \ t_2 \ t_3 : U\} \rightarrow PLUS \ (TIMES \ t_1 \ t_3) \ (TIMES \ t_2 \ t_3) \longleftrightarrow TIMES \ (PLUS \ t_1 \ t_2) \ t_3
    id \longleftrightarrow : \{t : U\} \to t \longleftrightarrow t
     \_\bigcirc\_ : \{t_1 \ t_2 \ t_3 : \mathsf{U}\} \ \rightarrow (t_1 \longleftrightarrow t_2) \rightarrow (t_2 \longleftrightarrow t_3) \rightarrow (t_1 \longleftrightarrow t_3)
    : \{t_1 \ t_2 \ t_3 \ t_4 : \mathsf{U}\} \to (t_1 \longleftrightarrow t_3) \to (t_2 \longleftrightarrow t_4) \to (\mathsf{TIMES} \ t_1 \ t_2 \longleftrightarrow \mathsf{TIMES} \ t_3 \ t_4)
```

Example Circuit: Simple Negation



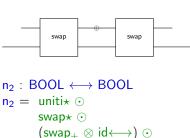


BOOL : U BOOL = PLUS ONE ONE

 $\mathsf{n}_1:\,\mathsf{BOOL}\longleftrightarrow\mathsf{BOOL}$

 $n_1 = swap_+$

Example Circuit: Not So Simple Negation



swap⋆ ⊙ unite⋆



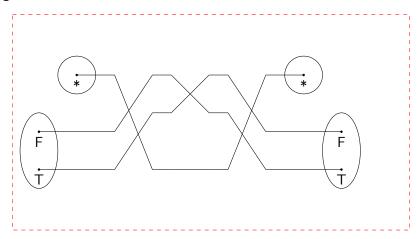
```
;→) ⊙
```

Reasoning about Example Circuits

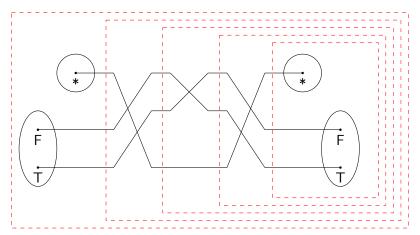
Algebraic manipulation of one circuit to the other:

```
negEx : n_2 \Leftrightarrow n_1
negEx = uniti \star \odot (swap \star \odot ((swap + \otimes id \longleftrightarrow) \odot (swap \star \odot unite \star)))
           ⇔ ( id⇔ ⊡ assoc⊙l )
     uniti* \odot ((swap* \odot (swap_+ \otimes id \longleftrightarrow)) \odot (swap* \odot unite*))
           \Leftrightarrow \langle id \Leftrightarrow \Box (swapl \star \Leftrightarrow \Box id \Leftrightarrow) \rangle
     uniti \star \odot (((id \longleftrightarrow \otimes swap_+) \odot swap_*) \odot (swap \star \odot unite \star))
            ⇔ ( id⇔ □ assoc⊙r )
     uniti* \odot ((id \longleftrightarrow \otimes swap_+) \odot (swap* \odot (swap* \odot unite*)))
           \Leftrightarrow \langle id \Leftrightarrow \Box (id \Leftrightarrow \Box assoc \odot I) \rangle
     uniti* \odot ((id \longleftrightarrow \otimes swap_+) \odot ((swap* \odot swap*) \odot unite*))
           \Leftrightarrow \langle id \Leftrightarrow \boxdot (id \Leftrightarrow \boxdot (linv \odot l \boxdot id \Leftrightarrow)) \rangle
     uniti \star \odot ((id \longleftrightarrow \otimes swap_+) \odot (id \longleftrightarrow \odot unite \star))
           \Leftrightarrow \langle id \Leftrightarrow \Box (id \Leftrightarrow \Box idl \odot I) \rangle
     uniti \star \odot ((id \longleftrightarrow \otimes swap_+) \odot unite \star)
           ⇔ ⟨ assoc⊙l ⟩
     (uniti \star \odot (id \longleftrightarrow \otimes swap_+)) \odot unite \star
           \Leftrightarrow \langle \text{ unitil} \star \Leftrightarrow \square \text{ id} \Leftrightarrow \rangle
     (swap+ ⊙ uniti*) ⊙ unite*
           ⇔ ( assoc⊙r )
     swap+ ⊙ (uniti* ⊙ unite*)
           ⇔⟨ id⇔ ⊡ linv⊙l ⟩
     swap_{+} \odot id \longleftrightarrow
           ⇔ ⟨ idr⊙l ⟩
     swap⊥ □
```

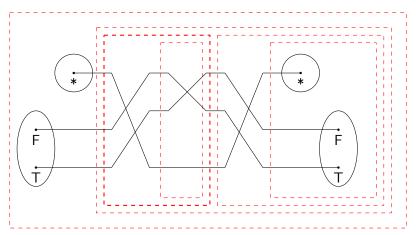
Original circuit:



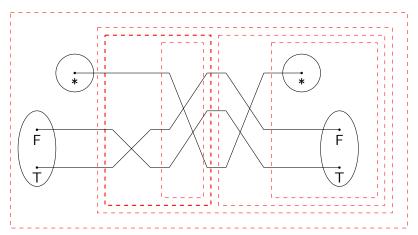
Making grouping explicit:



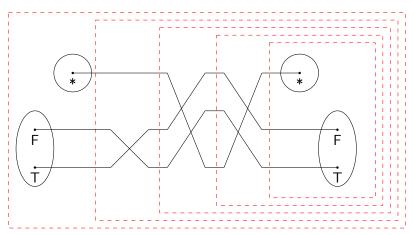
By associativity:



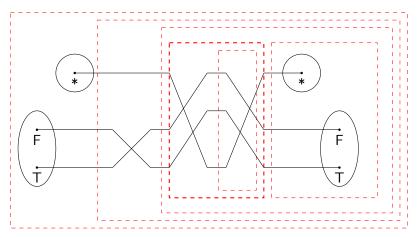
By pre-post-swap:



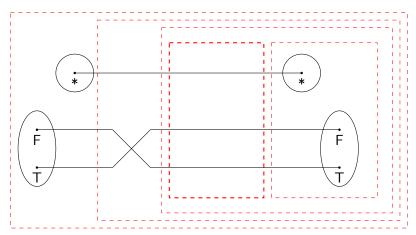
By associativity:



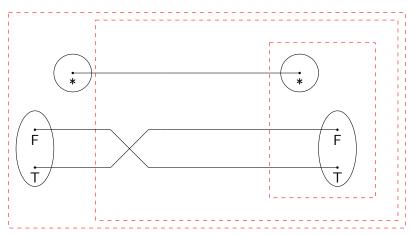
By associativity:



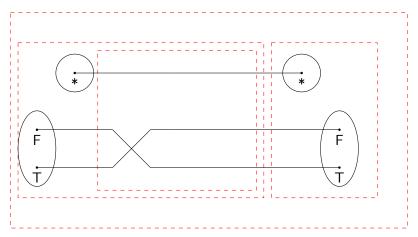
By swap-swap:



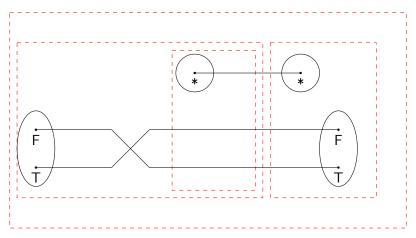
By id-compose-left:



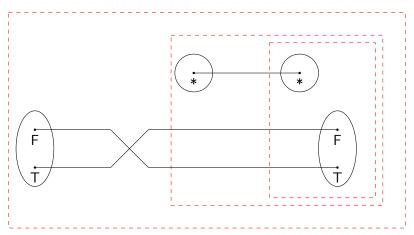
By associativity:



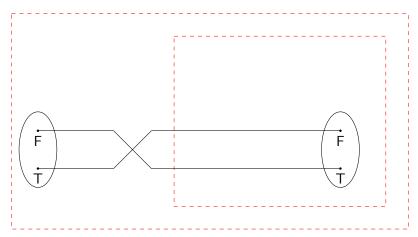
By swap-unit:



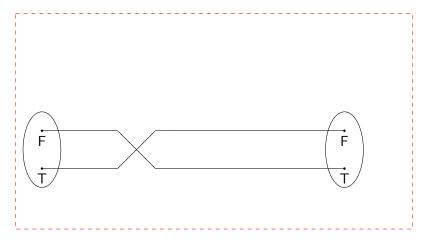
By associativity:



By unit-unit:



By id-unit-right:



But is this a programming language?

We get forward and backward evaluators

```
\begin{array}{l} \mathsf{eval} : \left\{ t_1 \ t_2 : \, \mathsf{U} \right\} \to \left( t_1 \longleftrightarrow t_2 \right) \to \left[\!\left[ \ t_1 \ \right]\!\right] \to \left[\!\left[ \ t_2 \ \right]\!\right] \\ \mathsf{evalB} : \left\{ t_1 \ t_2 : \, \mathsf{U} \right\} \to \left( t_1 \longleftrightarrow t_2 \right) \to \left[\!\left[ \ t_2 \ \right]\!\right] \to \left[\!\left[ \ t_1 \ \right]\!\right] \end{array}
```

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$$\begin{array}{l} \mathsf{eval} : \{ t_1 \ t_2 : \, \mathsf{U} \} \to (t_1 \longleftrightarrow t_2) \to \llbracket \ t_1 \ \rrbracket \to \llbracket \ t_2 \ \rrbracket \\ \mathsf{evalB} : \{ t_1 \ t_2 : \, \mathsf{U} \} \to (t_1 \longleftrightarrow t_2) \to \llbracket \ t_2 \ \rrbracket \to \llbracket \ t_1 \ \rrbracket \end{array}$$

which really do behave as expected

c2equiv :
$$\{t_1 \ t_2 : \mathsf{U}\} \to (c : t_1 \longleftrightarrow t_2) \to \llbracket \ t_1 \ \rrbracket \simeq \llbracket \ t_2 \ \rrbracket$$

Manipulating circuits

Nice framework, but:

- We don't want ad hoc rewriting rules.
 - Our current set has 76 rules!
- Notions of soundness; completeness; canonicity in some sense.
 - Are all the rules valid? (yes)
 - Are they enough? (next topic)
 - Are there canonical representations of circuits? (open)

Categorification I

Type equivalences (such as between $A \times B$ and $B \times A$) are Functors. Equivalences between Functors are Natural Isomorphisms. At the value-level, they induce 2-morphisms:

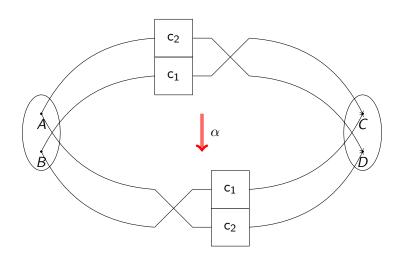
postulate

$$c_1: \{B \ C: \ U\} \rightarrow B \longleftrightarrow C$$

$$c_2: \{A \ D: \ U\} \rightarrow A \longleftrightarrow D$$

$$\begin{array}{l} \mathsf{p_1} \ \mathsf{p_2} : \{A \ B \ C \ D : \mathsf{U}\} \to \mathsf{PLUS} \ A \ B \longleftrightarrow \mathsf{PLUS} \ C \ D \\ \mathsf{p_1} = \mathsf{swap}_+ \ \odot \ (\mathsf{c_1} \oplus \mathsf{c_2}) \\ \mathsf{p_2} = (\mathsf{c_2} \oplus \mathsf{c_1}) \ \odot \ \mathsf{swap}_+ \end{array}$$

2-morphism of circuits



Categorification II

The categorification of a semiring is called a Rig Category. As with a semiring, there are two monoidal structures, which interact through some distributivity laws.

Theorem 6.

The following are Symmetric Bimonoidal Groupoids:

- The class of all types (Set)
- The set of all finite types
- The set of permutations
- The set of equivalences between finite types

The coherence rules for Symmetric Bimonoidal groupoids give us 58 rules.

Categorification III

Conjecture 1.

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and of course the punchline:

Theorem 7 (Laplaza 1972).

There is a sound and complete set of coherence rules for Symmetric Rig Categories.

Conjecture 2.

The set of coherence rules for Symmetric Rig Groupoids are a sound and complete set for circuits.