

Conversor de 7 segmentos:

Expressão Booleana simplificada : $\sim C \sim A + B + C A$

Tabela verdade:

File Edit Project Simulate Window Help									
Inputs			Outputs		Table	Expression	Minimized		
C	B	A	a	b	c	d	e	f	g
0	0	0	1	1	1	1	1	1	0
0	0	1	0	1	1	0	0	0	0
0	1	0	1	1	0	1	1	0	1
0	1	1	1	1	1	1	0	0	1
1	0	0	0	1	1	0	0	1	1
1	0	1	1	0	1	1	0	1	1
1	1	0	1	0	1	1	1	1	1
1	1	1	1	1	1	0	0	0	0

Build Circuit