This week's highlights

- Define multiple ways for representing numbers
- Compute the ranges of numbers that can be represented using a given definition
- Represent negative integers in multiple ways
- Perform arithmetic operations on integers using multiple representations
- Relate algorithms for integer operations to bitwise boolean operations
- List the truth tables and meanings for conjunction, disjunction, exclusive or.
- Correctly use XOR and bit shifts
- Relate boolean operations to applications in combinatorial circuits.

Lecture videos

Week 2 Day 1 YouTube playlist
Week 2 Day 2 YouTube playlist
Week 2 Day 3 YouTube playlist

Monday January 11

Definition (Rosen p. 246) For b an integer greater than 1 and n a positive integer, the **base b expansion of n** is

$$(a_{k-1} \cdots a_1 a_0)_b$$

where k is a positive integer, $a_0, a_1, \ldots, a_{k-1}$ are nonnegative integers less than b, $a_{k-1} \neq 0$, and

$$n = a_{k-1} b^{k-1} + \cdots + a_1 b + a_0$$

Algorithm for converting from base b_1 expansion to base b_2 expansion:

Definition For b an integer greater than 1, w a positive integer, and n a nonnegative integer ______________, the **base b fixed-width w expansion of n** is

$$(a_{w-1} \cdots a_1 a_0)_{b,w}$$

where $a_0, a_1, \ldots, a_{w-1}$ are nonnegative integers less than b and

$$n = a_{w-1} b^{w-1} + \cdots + a_1 b + a_0$$

Decimal $b = 10$	Binary $b = 2$	Binary fixed-width 10 $b = 2$, $w = 10$	Binary fixed-width 7 $b = 2$, $w = 7$	Binary fixed-width $b = 2$, $w = 4$
$(20)_{10}$				

Definition For b an integer greater than 1, w a positive integer, w' a positive integer, and x a real number the **base b fixed-width expansion of x with integer part width w and fractional part width w'** is $(a_{w-1} \cdots a_1 a_0 . c_1 \cdots c_{w'})_{b,w,w'}$ where $a_0, a_1, \ldots, a_{w-1}, c_1, \ldots, c_{w'}$ are nonnegative integers less than b and

____

and

____

3.75 in fixed-width binary, integer part width 2, fractional part width 8	
0.1 in fixed-width binary, integer part width 2, fractional part width 8	

```
welcome $jshell
|  Welcome to JShell -- Version 10.0.1
|  For an introduction type: /help intro

jshell> 0.1
$1 ==>

jshell> 0.2
$2 ==>

jshell> 0.1 + 0.2
$3 ==>

jshell> Math.sqrt(2)
$4 ==>

jshell> Math.sqrt(2)*Math.sqrt(2)
$5 ==>

jshell>
```

Note: Java uses floating point, not fixed width representation, but similar rounding errors appear in both.

Wednesday January 13

base b expansion of n	**base b fixed-width w expansion of n**
For b an integer greater than 1 and n a positive integer, the **base b expansion of n** is $(a_{k-1} \cdots a_1 a_0)_b$ where k is a positive integer, $a_0, a_1, \ldots, a_{k-1}$ are nonnegative integers less than b, $a_{k-1} \neq 0$, and $n = a_{k-1}b^{k-1} + \cdots + a_1 b + a_0$	For b an integer greater than 1, w a positiv and n a nonnegative integer with $n < b^w$, t **fixed-width w expansion of n** is $(a_{w-1} \cdot$ where $a_0, a_1, \ldots, a_{w-1}$ are nonnegative int than b and $n = a_{w-1}b^{w-1} + \cdots + a_1 b + a_0$

Representing negative integers in binary: Fix a positive integer width for the representation w, $w > 1$.

	To represent a positive integer n	To represent a negative integer $-n$
Sign-magnitude	$[0a_{w-2} \cdots a_0]_{s,w}$, where $n = (a_{w-2} \cdots a_0)_{2,w-1}$ Example $n = 17$, $w = 7$:	$[1a_{w-2} \cdots a_0]_{s,w}$, where $n = (a_{w-2} \cdot\cdot$ Example $-n = -17$, $w = 7$:
2s complement	$[0a_{w-2} \cdots a_0]_{2c,w}$, where $n = (a_{w-2} \cdots a_0)_{2,w-1}$ Example $n = 17$, $w = 7$:	$[1a_{w-2} \cdots a_0]_{2c,w}$, where $2^{w-1} - n = ($ Example $-n = -17$, $w = 7$:
Extra example: 1s complement	$[0a_{w-2} \cdots a_0]_{1c,w}$, where $n = (a_{w-2} \cdots a_0)_{2,w-1}$ Example $n = 17$, $w = 7$:	$[1\bar{a}_{w-2} \cdots \bar{a}_0]_{1c,w}$, where $n = (a_{w-2}$ we define $\bar{0} = 1$ and $\bar{1} = 0$. Example $-n = -17$, $w = 7$:

Representing 0:

Fixed-width addition: adding one bit at time, using the usual column-by-column and carry arithmetic, and dropping the carry from the leftmost column so the result is the same width as the summands. *Does this give the right value for the sum?*

$$\begin{array}{r} (1\ 1\ 0\ 1\ 0\ 0)_{2,6} \\ +\underline{(0\ 0\ 0\ 1\ 0\ 1)_{2,6}} \end{array} \qquad \begin{array}{r} [1\ 1\ 0\ 1\ 0\ 0]_{s,6} \\ +\underline{[0\ 0\ 0\ 1\ 0\ 1]_{s,6}} \end{array} \qquad \begin{array}{r} [1\ 1\ 0\ 1\ 0\ 0]_{2c,6} \\ +\underline{[0\ 0\ 0\ 1\ 0\ 1]_{2c,6}} \end{array}$$

Extra example

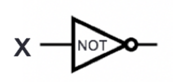
$$\begin{array}{r} (1\ 1\ 0\ 1\ 0\ 0)_{2,6} \\ \times\underline{(0\ 0\ 0\ 1\ 0\ 1)_{2,6}} \end{array} \qquad \begin{array}{r} [1\ 1\ 0\ 1\ 0\ 0]_{s,6} \\ \times\underline{[0\ 0\ 0\ 1\ 0\ 1]_{s,6}} \end{array} \qquad \begin{array}{r} [1\ 1\ 0\ 1\ 0\ 0]_{2c,6} \\ \times\underline{[0\ 0\ 0\ 1\ 0\ 1]_{2c,6}} \end{array}$$

Friday January 15

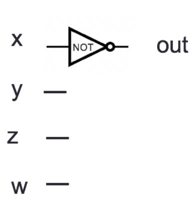
Input x	Input y	Output x AND y
1	1	1
1	0	0
0	1	0
0	0	0

Input x	Input y	Output x XOR y
1	1	0
1	0	1
0	1	1
0	0	0

Input x	Output NOT x
1	0
0	1



Example digital circuit:



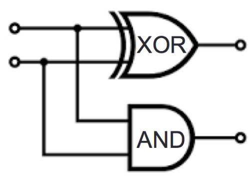
Output when $x = 1, y = 0, z = 0, w = 1$ is _____ Output when $x = 1, y = 1, z = 1, w = 1$ is _____ Output when $x = 0, y = 0, z = 0, w = 1$ is _____

Draw a logic circuit with inputs x and y whose output is always 0. *Can you use exactly 1 gate?*

Fixed-width addition: adding one bit at time, using the usual column-by-column and carry arithmetic, and dropping the carry from the leftmost column so the result is the same width as the summands. In many cases, this gives representation of the correct value for the sum when we interpret the summands in fixed-width binary or in 2s complement.

For single column:

Input		Output	
x_0	y_0	s_0	c_0
1	1		
1	0		
0	1		
0	0		



x

Draw a logic circuit that implements fixed-width 2 binary addition:

- Inputs x_0, y_0, x_1, y_1 represent $(x_1x_0)_{2,2}$ and $(y_1y_0)_{2,2}$
- Outputs z_0, z_1, z_2 represent $(z_2z_1z_0)_{2,3} = (x_1x_0)_{2,2} + (y_1y_0)_{2,2}$ (may require up to width 3)

First approach: half-adder for each column, then combine carry from right column with sum of left column

Write expressions for the circuit output values in terms of input values:
$z_0 =$ ____
$z_1 =$ ____
$z_2 =$ ____

Second approach: for middle column, first add carry from right column to x_1, then add result to y_1

Write expressions for the circuit output values in terms of input values:
$z_0 =$ ____
$z_1 =$ ____
$z_2 =$ ____

Extra example Describe how to generalize this addition circuit for larger width inputs.

Review quiz questions

1. Recall the definitions from class for number representations for **base b expansion of n**, **base b fixed-width w expansion of n**, and **base b fixed-width expansion of x with integer part width w and fractional part width w'**.

   For example, the base 2 (binary) expansion of 4 is $(100)_2$ and the base 2 (binary) fixed-width 8 expansion of 4 is $(00000100)_{2,8}$ and the base 2 (binary) fixed-width expansion of 4 with integer part width 3 and fractional part width 2 of 4 is $(100.00)_{2,3,2}$

   Compute the listed expansions. Enter your number using the notation for base expansions with parentheses but without subscripts. For example, if your answer were $(100)_{2,3}$ you would type `(100)2,3` into Gradescope.

   (a) Give the binary (base 2) expansion of the number whose octal (base 8) expansion is
   $$(371)_8$$

   (b) Give the decimal (base 10) expansion of the number whose octal (base 8) expansion is
   $$(371)_8$$

   (c) Give the octal (base 8) fixed-width 3 expansion of $(9)_{10}$?

   (d) Give the ternary (base 3) fixed-width 8 expansion of $(9)_{10}$?

   (e) Give the hexadecimal (base 16) fixed-width 6 expansion of $(16711935)_{10}$?[1]

   (f) Give the hexadecimal (base 16) fixed-width 4 expansion of
   $$(1011\ 1010\ 1001\ 0000)_2$$
   Note: the spaces between each group of 4 bits above are for your convenience only. How might they help your calculations?

   (g) Give the binary fixed width expansion of 0.125 with integer part width 2 and fractional part width 4.

[1] This matches a frequent debugging task – sometimes a program will show a number formatted as a base 10 integer that is much better understood with another representation.

(h) Give the binary fixed width expansion of 0.1 with integer part width 2 and fractional part width 3.

2. Select all and only the correct choices below.

   (a) Suppose you were told that the positive integer n_1 has the property that n_1 **div** $2 = 0$. Which of the following can you conclude?

      i. n_1 has a binary (base 2) expansion
      ii. n_1 has a ternary (base 3) expansion
      iii. n_1 has a hexadecimal (base 16) expansion
      iv. n_1 has a base 2 fixed-width 1 expansion
      v. n_1 has a base 2 fixed-width 20 expansion

   (b) Suppose you were told that the positive integer n_2 has the property that n_2 **mod** $4 = 0$. Which of the following can you conclude?

      i. the leftmost symbol in the binary (base 2) expansion of n_2 is 1
      ii. the leftmost symbol in the base 4 expansion of n_2 is 1
      iii. the rightmost symbol in the base 4 expansion of n_2 is 0
      iv. the rightmost symbol in the octal (base 8) expansion of n_2 is 0

3. Recall the definitions of signed integer representations from class: sign-magnitude and 2s complement.

   (a) Give the 2s complement width 6 representation of the number represented in binary fixed-width 6 representation as $(001011)_{2,6}$.

   (b) Give the 2s complement width 4 representation of the number represented in sign-magnitude width 4 as $[1111]_{s,4}$.

   (c) Give the sign magnitude width 4 representation of the number represented in 2s complement width 4 as $[1111]_{2c,4}$.

   (d) In binary fixed-width addition (adding one bit at time, using the usual column-by-column and carry arithmetic, and ignoring the carry from the leftmost column), we get:

$$\begin{array}{rl} 1110 & \text{first summand} \\ +0100 & \text{second summand} \\ \hline 0010 & \text{result} \end{array}$$

Select all and only the true statements below:

i. When interpreting each of the summands and the result in binary fixed-width 4, the result represents the actual value of the sum of the summands.
ii. When interpreting each of the summands and the sum in sign-magnitude width 4, the result represents the actual value of the sum of the summands.
iii. When interpreting each of the summands and the sum in 2s complement width 4, the result represents the actual value of the sum of the summands.

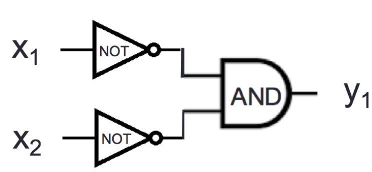
(e) In binary fixed-width addition (adding one bit at time, using the usual column-by-column and carry arithmetic, and ignoring the carry from the leftmost column), we get:

0110	first summand
+0111	second summand
1101	result

Select all and only the true statements below:

i. When interpreting each of the summands and the result in binary fixed-width 4, the result represents the actual value of the sum of the summands.
ii. When interpreting each of the summands and the sum in sign-magnitude width 4, the result represents the actual value of the sum of the summands.
iii. When interpreting each of the summands and the sum in 2s complement width 4, the result represents the actual value of the sum of the summands.

4. (a) Consider the logic circuit



Calculate the value of the output of this circuit (y_1) for each of the following settings(s) of input values.

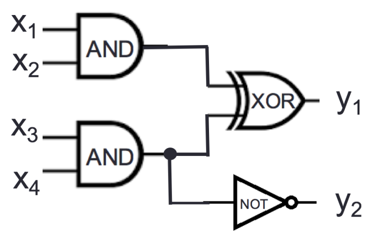
i. $x_1 = 1$, $x_2 = 1$

ii. $x_1 = 1$, $x_2 = 0$

iii. $x_1 = 0$, $x_2 = 1$

iv. $x_1 = 0$, $x_2 = 0$

(b) Consider the logic circuit



For which of the following settings(s) of input values is the output $y_1 = 0$, $y_2 = 1$. (Select all and only those that apply.)

i. $x_1 = 0$, $x_2 = 0$, $x_3 = 0$, and $x_4 = 0$

ii. $x_1 = 1$, $x_2 = 1$, $x_3 = 1$, and $x_4 = 1$

iii. $x_1 = 1$, $x_2 = 0$, $x_3 = 0$, and $x_4 = 1$

iv. $x_1 = 0$, $x_2 = 0$, $x_3 = 1$, and $x_4 = 1$