

Rep_Mid_B0829002

In this task, we are going to a count down counter with setting mode, blink, stop, and speed-up mode.

1. When the stop button was pressed, the counter will be stopped.
2. There are 2 button for setting clock, one is for change resume counting, setting second, setting minute. The other one is for increment the setting number, once pressed will add 1.
3. We can use speed-up button to make speed become 10x faster and pressed it again will become back to normal speed.
4. When setting sec, setting minute, and count to 00:00, this clock will blink.

Code:

```
module Rep_Mid(ClkIn, outsec0, outsec1, outmin0, outmin1, stop, LED, but1, but2, mode, but3);
input ClkIn, but1, but2, but3;
output reg stop, LED;
reg [3:0] min1 = 5;
reg [3:0] min0 = 0;
reg [3:0] sec1 = 5;
reg [3:0] sec0 = 0;
output [0:0] outsec0, outsec1, outmin0, outmin1;
wire newbut1, newbut2, newbut3;
reg clk, blink_s, blink_m, speed;
reg [25:0] counter;
output reg [1:0] mode;
sib(sec0, outsec0, blink_s, clk);
sib(sec1, outsec1, blink_s, clk);
sib(min0, outmin0, blink_m, clk);
sib(min1, outmin1, blink_m, clk);
debounce(ClkIn, but1, newbut1);
debounce(ClkIn, but2, newbut2);
debounce(ClkIn, but3, newbut3);
parameter hz = 3000000;
parameter hz_10 = 5000000;

always @(posedge ClkIn) begin
    //1hz counter
    counter <= counter+1;
    //setting sec
    case(mode)
        2'b00: begin
            blink_s <= 1;
            blink_m <= 0;
            if(newbut2) begin
                sec0 <= sec0 + 1;
                if(sec0==9) begin
                    sec1 <= 0;
                    sec0 <= 0;
                end else if(sec0==9) begin
                    sec0 <= 0;
                    sec1 <= sec1+1;
                end
            end
        end
        //setting min
        2'b01: begin
            blink_m <= 1;
            blink_s <= 0;
            if(newbut3) begin
                min0 <= min0 + 1;
                if(min0==9) begin
                    min1 <= 0;
                    min0 <= 0;
                end else if(min0==9) begin
                    min0 <= 0;
                    min1 <= min1+1;
                end
            end
        end
    endcase
    //speed normal and counting down
    if(!speed) begin
        if(counter > hz) begin
            clk <= ~clk;
            if(mode==2'b00) begin
                blink_s <= 0;
                blink_m <= 0;
            end
            if(!stop && !mode) begin
                sec0 <= sec0 - 1;
                if(min1==0 && min0==0 && sec1==0 && sec0==0) begin
                    sec0 <= 0;
                    sec1 <= 0;
                    min0 <= 0;
                    min1 <= 0;
                end else if(min0 <= 0 && sec1 <= 0 && sec0 <= 0) begin
                    sec0 <= 9;
                    sec1 <= 9;
                    min0 <= min0-1;
                end else if(sec1 <= 0 && sec0 <= 0) begin
                    sec0 <= 9;
                    sec1 <= 9;
                end
            end
        end
    end
    //10x speed
    if(counter > hz_10) begin
        clk <= ~clk;
        if(mode==2'b01) begin
            blink_s <= 0;
            blink_m <= 0;
        end
        if(!stop && !mode) begin
            sec0 <= sec0 - 1;
            if(min1==0 && min0==0 && sec1==0 && sec0==0) begin
                sec0 <= 0;
                sec1 <= 0;
                min0 <= 0;
                min1 <= 0;
            end else if(min0 <= 0 && sec1 <= 0 && sec0 <= 0) begin
                sec0 <= 9;
                sec1 <= 9;
                min0 <= 0;
                min1 <= 0;
            end else if(sec1 <= 0 && sec0 <= 0) begin
                sec0 <= 9;
                sec1 <= 9;
            end
        end
    end
    counter <= 0;
end

endmodule
```

```

1 module debounce(clk_in, button_in, button_out);
2   input clk_in, button_in;
3   output reg button_out;
4   reg [25:0] counter;
5   parameter divn_num = 5000000;
6   always @(posedge clk_in) begin
7     if(button_in)
8       counter <= 0;
9     else if(counter != divn_num)
10      counter <= counter + 1;
11   end
12   always @(*) begin
13     if(counter == divn_num-1)
14       button_out = 1'b1;
15     else
16       button_out = 1'b0;
17   end
18 endmodule
19
1 module sib(in, out, blink, clk);
2   input [3:0] in;
3   input blink, clk;
4   output reg [6:0] out;
5
6   always @(*) begin
7     if(clk && blink) begin
8       out = 7'b1111111;
9     end else begin
10      case(in)
11        4'b0000: out = 7'b1000000;
12        4'b0001: out = 7'b1111001;
13        4'b0010: out = 7'b0100100;
14        4'b0011: out = 7'b0110000;
15        4'b0100: out = 7'b0011001;
16        4'b0101: out = 7'b0010010;
17        4'b0110: out = 7'b0000010;
18        4'b0111: out = 7'b1011000;
19        4'b1000: out = 7'b0000000;
20        4'b1001: out = 7'b0010000;
21        default: out = 7'b1111111;
22      endcase
23    end
24  end
25 endmodule

```

Difference:

In my opinion, the most difficult part is trying to combine the 2 different speed count-down counter with blinking function. Because we need to use the same clock to make sure debounce button can work successfully, thus we must to write a mux to select the parameter for different speed counter. The second hard module is blink mode. We can easily use the counter complement to make the seven segment blinking. And when the seven segment blinking, the signal will also be sometime all shine, sometimes shine the number.

Discussion:

After this task, I learned that we cannot modify value in different module and how we can make seven segment blinking. And because this count-down counter has a lot of different mode, we need to make the coding style clean and module working logic.