VISVESVARAYA TECHNOLOGICAL UNIVERSITY JNANA SANGAMA, BELAGAVI – 590018



A Self Study Report on

"A Comparison of CMOS circuit techniques: Differential Cascode Voltage Switch logic versus conventional logic"

Submitted in partial fulfillment for the award of degree of

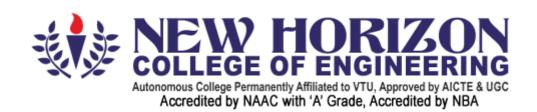
Bachelor of Engineering In Electronics and Communication Engineering

Submitted by

K Girivardhan (1NH16EC036)

Under the guidance of

Mrs. Neethu Johny, Senior Assistant Professor. Department of ECE, NHCE, Bengaluru



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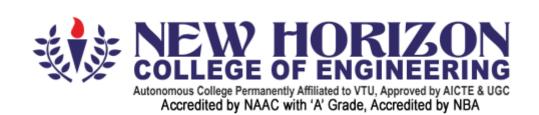
Bachelor of Engineering In Electronics and Communication Engineering

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