



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

## CONSOLIDATED MARKS MEMO / CREDIT SHEET



CMM. No.: **K 00066630**

Serial No.: 201003013448

Name: **JAGANI JAGADEESH**

Hall Ticket No. **07K11A0418**

Bachelor of Technology **Electronics & Communication Engineering**

Name of the College: PYDAM COLLEGE OF ENGG & TECH

Name & Year of Final Exam: **B.Tech 2011**

Class Awarded: **First Class**

Year of Admission: **2007 - 2008**

S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INT. MARKS	EXT. MARKS	TOTAL	CREDITS
<b>I YEAR</b>											
1	APPLIED PHYSICS	17	47	64	4	2	C PRG. & DATA STRUCTURES	18	33	51	6
3	ELECTRONIC DEVICES & CKTS.	15	45	61	5	4	ENGINEERING DRAWING	15	53	69	4
5	ENGLISH	16	48	64	4	5	MATHEMATICAL METHODS	17	35	52	6
7	MATHEMATICS - I	19	62	81	6	6	NETWORK ANALYSIS	15	37	52	4
9	COMPUTER PROGRAMMING LAB	24	49	73	4	10	ELECTRONIC DEV.&CKTS.LAB	24	45	69	4
11	ENGLISH LANG.COMM.SKILLS LAB	20	43	63	4	12	IT WORKSHOP	24	45	73	4
<b>II YEAR</b>											
1	ELECTRICAL TECHNOLOGY	13	65	78	4	1	ANALOG COMMUNICATIONS	14	37	51	4
2	ELECTRONICS CIRCUIT ANALYSIS	15	28	43	4*	2	CONTROL SYSTEMS	16	62	78	4
3	ENVIRONMENTAL STUDIES	14	33	47	4	3	ELECTRO MAGNETIC WAVES & TRANSMISSION	14	28	42	4*
4	MATHEMATICS - III	15	39	54	4	4	OBJECT ORIENTED PROGRAMMING	16	41	57	4
5	PROBABILITY THEORY & STO. PROCESSES	14	39	53	4	5	PULSE & DIGITAL CIRCUITS	15	32	47	4
6	SIGNALS & SYSTEMS	14	34	48	4	6	SWITCHING THEORY & LOGIC DESIGN	17	54	71	4
7	ELECTRICAL TECHNOLOGY (LAB)	18	33	51	2	7	ANALOG COMMUNICATIONS (LAB)	22	47	69	2
8	ELECTRONIC CIRCUITS (LAB)	21	38	59	2	8	PULSE & DIGITAL CIRCUITS (LAB)	21	46	67	2
<b>III YEAR</b>											
1	ANTENNAS AND WAVE PROPAGATION	16	48	64	4	1	DIGITAL SIGNAL PROCESSING	16	44	60	4
2	COMPUTER ORGANIZATION	17	34	51	4	2	MANAGEMENT SCIENCE	16	37	53	4
3	DIGITAL COMMUNICATIONS	15	59	74	4	3	MICRO PROCESSORS AND INTERFACING	15	40	55	4
4	DIGITAL IC APPLICATIONS	18	42	60	4	4	MICROWAVE ENGINEERING	16	31	47	4
5	LINEAR IC APPLICATIONS	18	47	65	4	5	TELECOMMUNICATION SWITCHING SYSTEM	17	44	61	4
6	MANAGERIAL ECONOMICS AND FINANCIAL	18	38	56	4	6	VLSI DESIGN	17	48	65	4
7	IC APPLICATIONS AND ECAD LAB.	18	40	58	2	7	ADVANCED ENGLISH COMMUNICATION SK.	19	38	57	2
8	DIGITAL COMMUNICATIONS LAB.	20	41	61	2	8	MICRO PROCESSORS AND INTERFACING LAB	22	47	69	2
<b>IV YEAR</b>											
1	CELLULAR AND MOBILE COMMUNICATIONS	18	44	62	4	1	COMPREHENSIVE VIVA	0	90	90	2
2	COMPUTER NETWORKS	18	42	60	4	2	BIO-MEDICAL INSTRUMENTATION	14	55	69	4
3	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	19	58	77	4	3	OPTICAL COMMUNICATIONS	18	43	61	4
4	RADAR SYSTEMS	19	47	66	4	4	WIRELESS COMMUNICATIONS AND NETWORKS	13	50	63	4
5	SATELLITE COMMUNICATIONS	17	34	51	4	5	SEMINAR	45	—	45	2
6	TELEVISION ENGINEERING	19	44	63	4	6	INDUSTRY ORIENTED MINI PROJECT	0	46	46	2
7	DIGITAL SIGNAL PROCESSING LAB	22	45	67	2	7	PROJECT WORK	37	149	186	10
8	MICROWAVE AND OPTICAL COMMUNICATIONS LAB	21	45	66	2						

Number of Credits registered for: **224**

Aggregate Marks Secured for best: **216 Credits 3631 out of 5350 (67.87 %)**

Date of Declaration of Result:

**May 2011**

(See overleaf for Instructions)

(\*Courses registered but not countered for calculation of aggregate)

*A. M. Prasad*

11/04/2012 **CONTROLLER OF EXAMINATIONS**