

**RV University**  
**School of Computer Science and Engineering**

**B.Tech (Hons) Degree Examinations- January 2025**

**Semester : I**

**Course Code : CS1101**

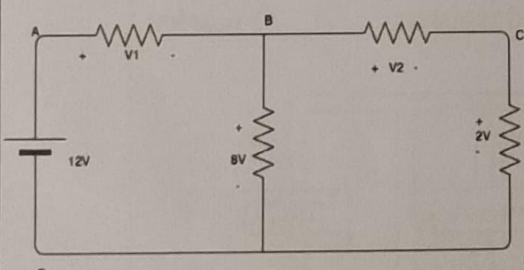
**Course Title : Digital Systems and Computer Architecture**

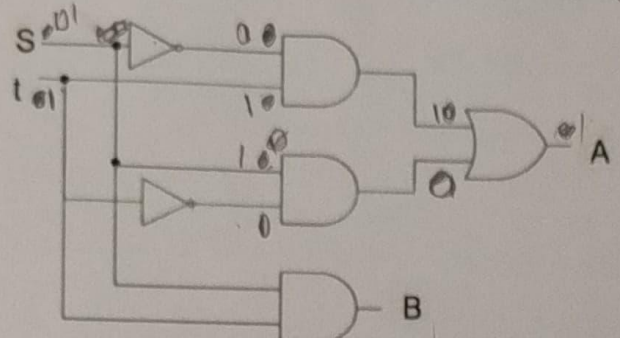
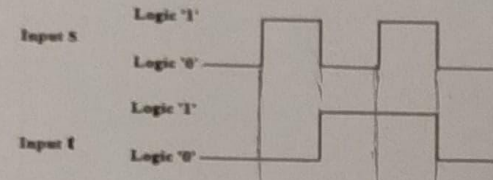
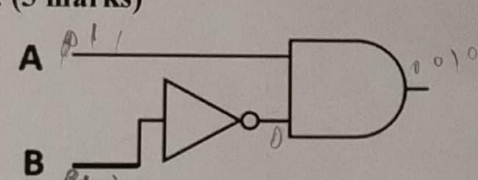
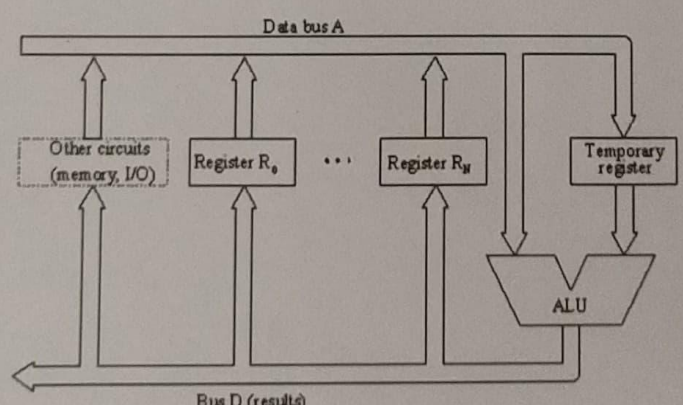
**Duration : 2 Hours**

**Max. Marks: 30**

**Instructions to students:**

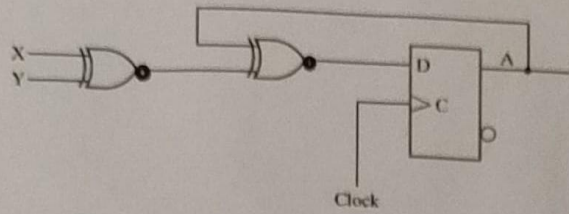
- Answer all questions
- No calculators are allowed

Sl. No.	PART A – Max Marks(10)	Marks	L1-L6	CO
1.	<p>a. Design a Half Subtractor with Truth Table and Implement it using appropriate Logic gates.(2 marks)</p> <p>b. Convert the following numbers into expected format. (2 marks)</p> <p>(i) 0x7A to Octal</p> <p>(ii) (56.27)<sub>10</sub> to 5421 BCD code</p> <p>c. Determine the Complement of Following Boolean expression (A+B)C+D. (1 Mark)</p>	5	L3	CO2
2.	<p>a. Analyze the given circuit to identify the value of V1 and V2. (2 marks)</p>  <p>b. Analyze the importance of Q-Point in functioning of a Diode with a neat diagram. (2 mark)</p> <p>c. Analyze the behaviour of a capacitor in circuits with DC source. (1 marks)</p>	5	L4	CO1

Sl. No.	PART B – Max Marks(20)	Marks	L1-L6	CO
1.	<p>a. Simplify the Boolean expression  <math>F = AB + AB' + BC</math>            using both Karnaugh Map and Boolean algebra. Also draw the minimized circuit. (3 marks)</p> <p>b. Generate the output waveform for output A of the given circuit by applying the given input waveforms for S and t. (2 marks)</p>   <p>c. Differentiate between flip-flops and latches. (2 marks)</p> <p>d. Construct the below circuit with NAND gates only and write the truth table. (3 marks)</p> 	10	L3	CO3
2.	<p>a. Describe the sequence of operation for the instruction <math>R_0 = R_N + R_N</math> using the ALU and registers (3 marks)</p>  <p>b. Describe how memory latency affects CPU performance and how cache memory helps reduce this impact. (2 marks)</p>	10	L2	CO3



c. Explain the process of deriving the state equation, state table, and state transition diagram for the given block diagram, and describe the relationships among these components. (2 marks)



d. Explain the operations that takes place after we turn on a computer. (3 marks)