

File Overall Summary

Report Info

Report: cctest_20200504_113121.pdf

Type: LINE

Generation Date: 04-May-2020 11:31:26

Filter String:

Overall Summary

Total Number of Files: 10

Total Number of Functions: 41

Total Number of Lines: 320

Total Number of Statements: 320

Code Coverage Summary

File Coverage: 100%

Function Coverage: 0%

Line Coverage: 71%

Statement Coverage: 71%

Define Properties

Default properties are being used (No user define properties are set).

Import File

Type

Analysis Date

C:\Users\JosephTaffe-Atkins\IBM\rational\sd\zUNIT\metadata\

Merged result

04-May-2020 11:29:26

plugins\com.ibm.debug.pdt.codecoverage.

core\AZUTSTRN_2020_05_04_112923_0619.cczip(Current Result)

Table of Contents

1 - File Overall Summary	1
2 - File Summary	3
3 - ADDTESTS.expanded.cbl (100%)	4
4 - CHKCODE.expanded.cbl (100%)	5
5 - NEGCHECK.expanded.cbl (55%)	6
6 - SETUP.expanded.cbl (100%)	7
7 - TEARDOWN.expanded.cbl (100%)	8
8 - TOURFILE.expanded.cbl (100%)	9
9 - TOURFIT1.expanded.cbl (100%)	10
10 - VALCHECK.expanded.cbl (55%)	11
11 - ZUTFIN01.expanded.cbl (85%)	12
12 - ZUTFOT01.expanded.cbl (39%)	13




File Summary

Name	Coverage	Lines Covered	Uncovered Lines	Total Lines
ADDTESTS.expanded.cbl	100% <div><div></div></div>	8	0	8
CHKCODE.expanded.cbl	100% <div><div></div></div>	5	0	5
NEGCHECK.expanded.cbl	55% <div><div></div></div>	11	9	20
SETUP.expanded.cbl	100% <div><div></div></div>	28	0	28
TEARDOWN.expanded.cbl	100% <div><div></div></div>	8	0	8
TOURFILE.expanded.cbl	100% <div><div></div></div>	48	0	48
TOURFIT1.expanded.cbl	100% <div><div></div></div>	6	0	6
VALCHECK.expanded.cbl	55% <div><div></div></div>	11	9	20
ZUTFIN01.expanded.cbl	85% <div><div></div></div>	62	11	73
ZUTFOT01.expanded.cbl	39% <div><div></div></div>	41	63	104
Summary	71% <div><div></div></div>	228	92	320





File and Message

No Message Found




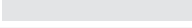
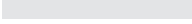

ADDTESTS.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
ADDTESTS.expanded.cbl	100%		8	0	8
'ADDTESTS'	100%		8	0	8






CHKCODE.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
CHKCODE.expanded.cbl	100%		5	0	5
'CHKCODE'	100%		5	0	5
MAINLINE	100%		5	0	5




NEGCHECK.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
NEGCHECK.expanded.cbl	55%		11	9	20
'NEGCHECK'	55%		11	9	20
MAIN	55%		11	9	20
CONVERT	0%		0	7	7
THROW-ASSERTION	0%		0	2	2









SETUP.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
SETUP.expanded.cbl	100%		28	0	28
'SETUP'	100%		28	0	28
ALLOCATE-PARM	100%		9	0	9
INITIALIZE-PARM	100%		2	0	2




TEARDOWN.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
TEARDOWN.expanded.cbl	100%		8	0	8
'TEARDOWN'	100%		8	0	8





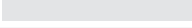
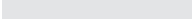
TOURFILE.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
TOURFILE.expanded.cbl	100%		48	0	48
TOURFILE	100%		48	0	48
MAINLINE	100%		48	0	48
INIT	100%		10	0	10
PROCESS-ALL-RECORDS	100%		12	0	12
COMBINE-AND-WRITE	100%		17	0	17
END-PROCESS	100%		3	0	3

TOURFIT1.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
TOURFIT1.expanded.cbl	100%		6	0	6
'TOURFIT1'	100%		6	0	6

VALCHECK.expanded.cbl

Name	Coverage		Lines	Uncovered	Total Lines
			Covered	Lines	
VALCHECK.expanded.cbl	55%		11	9	20
'VALCHECK'	55%		11	9	20
MAIN	55%		11	9	20
CONVERT	0%		0	7	7
THROW-ASSERTION	0%		0	2	2

ZUTFIN01.expanded.cbl

Name	Coverage	Lines Covered	Uncovered Lines	Total Lines
ZUTFIN01.expanded.cbl	85% <div><div></div></div>	62	11	73
ZUTFIN01	85% <div><div></div></div>	62	11	73
MAIN	75% <div><div></div></div>	3	1	4
INIT	100% <div><div></div></div>	4	0	4
FILE-PROC	70% <div><div></div></div>	7	3	10
TERM	87% <div><div></div></div>	48	7	55
THROW-ASSERTION	0% <div><div></div></div>	0	3	3
P-INPUT-VALCHECK	100% <div><div></div></div>	22	0	22
P-INPUT-NEGCHECK	100% <div><div></div></div>	22	0	22
CHECK-FILE-STATUS	20% <div><div></div></div>	1	4	5

ZUTFOT01.expanded.cbl

Name	Coverage	Lines	Uncovered	Total Lines
		Covered	Lines	
ZUTFOT01.expanded.cbl	39% <div><div></div></div>	41	63	104
ZUTFOT01	39% <div><div></div></div>	41	63	104
MAIN	85% <div><div></div></div>	11	2	13
INIT	100% <div><div></div></div>	9	0	9
FILE-PROC	60% <div><div></div></div>	6	4	10
TERM	21% <div><div></div></div>	15	57	72
CONVERT	0% <div><div></div></div>	0	7	7
THROW-ASSERTION	0% <div><div></div></div>	0	3	3
P-OUTPUT-VALCHECK	0% <div><div></div></div>	0	18	18
P-OUTPUT-NEGCHECK	36% <div><div></div></div>	12	21	33
CHECK-FILE-STATUS	0% <div><div></div></div>	0	8	8