Register

imme(instr[15:0])

shamt(unSigExt)

rd1

rd2

instr[20:16]

instr[15:11]

instr[10:6]

writedata

wd

ALU

result

MUX

MUX

instr[5:0]

rt

MUX

wa

ra1

ra2

rs

instr[25:21]

PC

IR

adr

Exmemory