PROJECT REPORT

VLSI IMPLEMENTATION IN HARDWARE SECURITYMODULE BASED ON AES ENCRYPTION METHOD

20ECPJ801- **PROJECT PHASE - II**

Submitted by

AKASH A (412520106007)

JAIGANESH P (412520106053)

MAHIZHAN M (412520106085)

in partial fulfillment for award of the degreeof

BACHELOR OF ENGINEERING

IN

ELECTRONICS AND COMMUNICATION ENGINEERING

SRI SAI RAM ENGINEERING COLLEGE

(An Autonomous Institution; Affiliated to Anna University, Chennai - 600 025)

ANNA UNIVERSITY :: CHENNAI 600 025

APRIL 2024

SRI SAIRAM ENGINEERING COLLEGE

(An Autonomous Institution; Affiliated to Anna University, Chennai -600 025)

BONAFIDE CERTIFICATE

Certified that this project report on **"VLSI IMPLEMENTATION IN HARDWARE SECURITY MODULE BASED ON AES ENCRYPTION METHOD"** is the bonafide work of **"AKASH A (412520106007), JAIGANESH P (412520106053) and MAHIZHAN M (412520106085)"** who carried out the **20ECPJ801- PROJECT PHASE - II** Work under my supervision.

SIGNATURE	**SIGNATURE**
Dr J Raja	**Mr. K Srinivasan**
HEAD OF THE DEPARTMENT	**SUPERVISOR**
Department of Electronics and Communication Engineering, **Sri Sairam Engineering College, (Autonomous) Chennai - 600 044**	Associate Professor, Department of Electronics and Communication Engineering, **Sri Sairam Engineering College, (Autonomous) Chennai - 600 044**

Submitted for VIVA-VOCE EXAMINATION held on:

INTERNAL EXAMINER   **EXTERNAL EXAMINER**