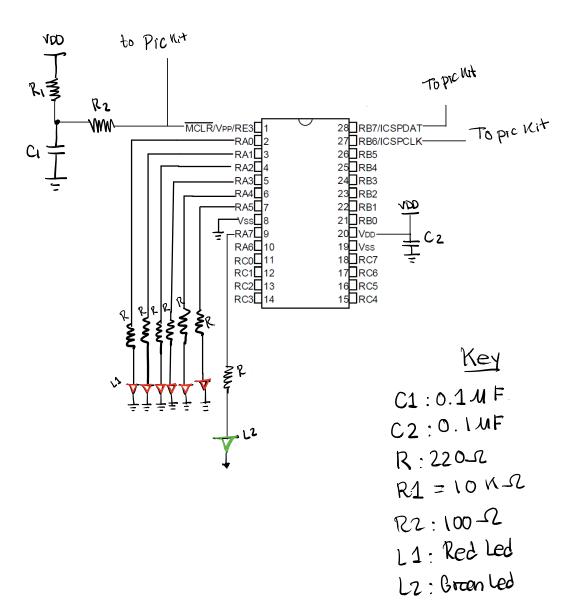
Pic Activity 3 Circuit Schematic

Jailim Lugo 30587167



Pseudo Code for Pic Activity 3

In talization

- configure TRISA to all outputs

- configure ANSELA to digital

- configure LATA to all off (except RAT)

While (1)

{ - a static counter to starting postion of all zeros (except bit PAT)

- check if counter is all one's (all bitson)

- if canter is all one's, set counter back to storting postion

- increment counter by one

- configure LATA to counter

- de lay about 250 ms

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