

## Education

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### Northeastern University

Boston, MA

Bachelor of Science, **Computer Engineering**

May 2019

- **Minor:** Computer Science
- **Honors:** Eta Kappa Nu(IEEE honor's society), Dean's List, University Honor's Program

#### Relevant Coursework

- Adv Algorithm (**C++**), Database Design, Networks, Electronics, Fund. of CS (**Lisp,Java**), Digital Logic Design (**Verilog, FPGA, MIPS**), Embedded Design (**C/C++**, **FPGA**)

#### Class Projects

- Implemented a greedy algorithm to solve knapsack and graph coloring problems (**C++**)
- Developed an FPGA design in Simulink for pulse width modulation on a robotic arm

## Technical Skills

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**Programming Languages:** Python (Selenium, Matplotlib), C/C++, Bash, Verilog, LaTeX, Java

**Software Development:** Agile Development Methodology, Behavior Driven Development, Jenkins

**Operating Systems:** QNX(RTOS), Unix

## Work Experience

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### Abiomed

Danvers, MA

Software Engineering Co-op

July 2017 – January 2018

- Led the design of a project for monitoring software development process (**Python(Matplotlib), Unix, Git, Jenkins**)
- Designed an operating system installation process on firmware with a usb device that reduce's installation time by 90 percent (**Python, C++**, **Unix, QNX, Bash**)
- Utilized selenium to automate database backup from server(**Python**)
- Performed time critical verification test on a remote link device

## Research Experience

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### NU Computer Architecture Research Group

Boston, MA

Undergraduate Researcher

May 2016 – Present

- Study GPGPU applications vulnerability to soft errors
- Utilized **Python** and SQLite3 to analyze fault injection results
- Implemented fault injection algorithm on memory in multi2sim GPU simulator (**C++**)
- Wrote scripts to automate fault injection (**Bash**)

#### Publication

- Combining Architectural Fault-injection and Neutron Beam Testing Approaches Toward Better Understanding of GPU Soft-error Resilience, Midwest Symposium on Circuits and Systems, August 2017.