



Student Name: \_\_\_\_\_ Section #: \_\_\_\_\_

### Introduction:

You are to elaborate on your pwm module by adding the below requirements:

- shall have controllable period, enable, and duty cycle from the processor
- shall have a graphical way of controlling the above signals as well as status indicators such as "motors enabled"
- shall demonstrate functionality via oscilloscope waveforms

### Module Report:

Create a tech memo for the GUI/hardware/software modules describing how the processor and FPGA interact. Block diagrams are highly recommended.

### Lab Submission:

1. Print out this lab description with your name on the front and attach all code [processor and FPGA] and the tech memo. Hand this packet in at the start of the following lab session **two weeks** from now.
2. Submit a single zip file with all code and the tech memo by midnight Feb 24th or Feb 26th depending on your lab session to <https://www.dropbox.com/request/ZlqCuRaxhITsAHaHclhp>. This will give you exactly two weeks minus 10 hours or a total of 326 hours to complete the assignment. You can also take a video of the demonstration with your phone and include it in your single zip file along with your code and tech memo.

### Grading:

	Score	Pts
<b>Code [Processor and FPGA]</b> No tabs please. Proper spacing and formatting. Adequate variable names. Consistency.		/5
<b>Comments</b> Proper header and comments throughout		/2
<b>Demonstration</b> To be performed within lab week. Can show after due date with time stamped code.		/3
<b>GUI</b> Organization, functionality, appearance.		/4
<b>Tech Memo</b> To be performed within lab week. Can show after due date with time stamped code.		/6
<b>Final Grade</b>		/20