

# W5100S Errata Sheet

## Document History

Ver 1.0.0 (MAY. 22, 2023)	First release (erratum 1)
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<sup>1</sup> The SPI provided by W5100S can access arbitrary length from 1 byte to N bytes depending on the memory size and register specified by the user.

<sup>2</sup> The W5100S defaults to SYS\_CLK operation at 100 MHz, but clock switching allows for 25 MHz operation.