

ELC 2137 Lab 4: Subtractor

Jake Simmons and Haonan Jin

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Summary

Type the summary of your experiment and results here.

Q&A

Answer questions posed in the lab assignment here.

Results

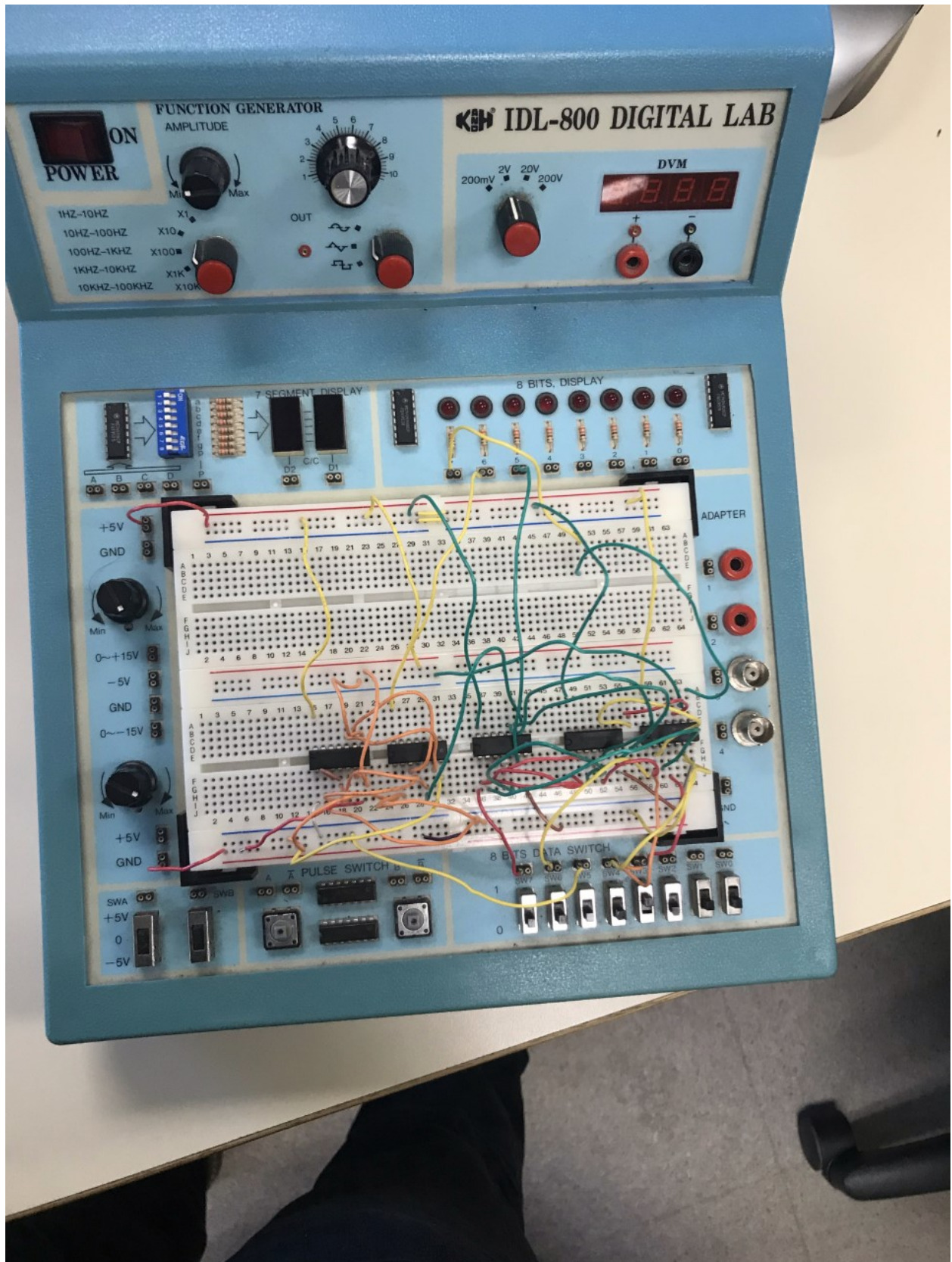


Figure 1: Picture of Assembled Circuit

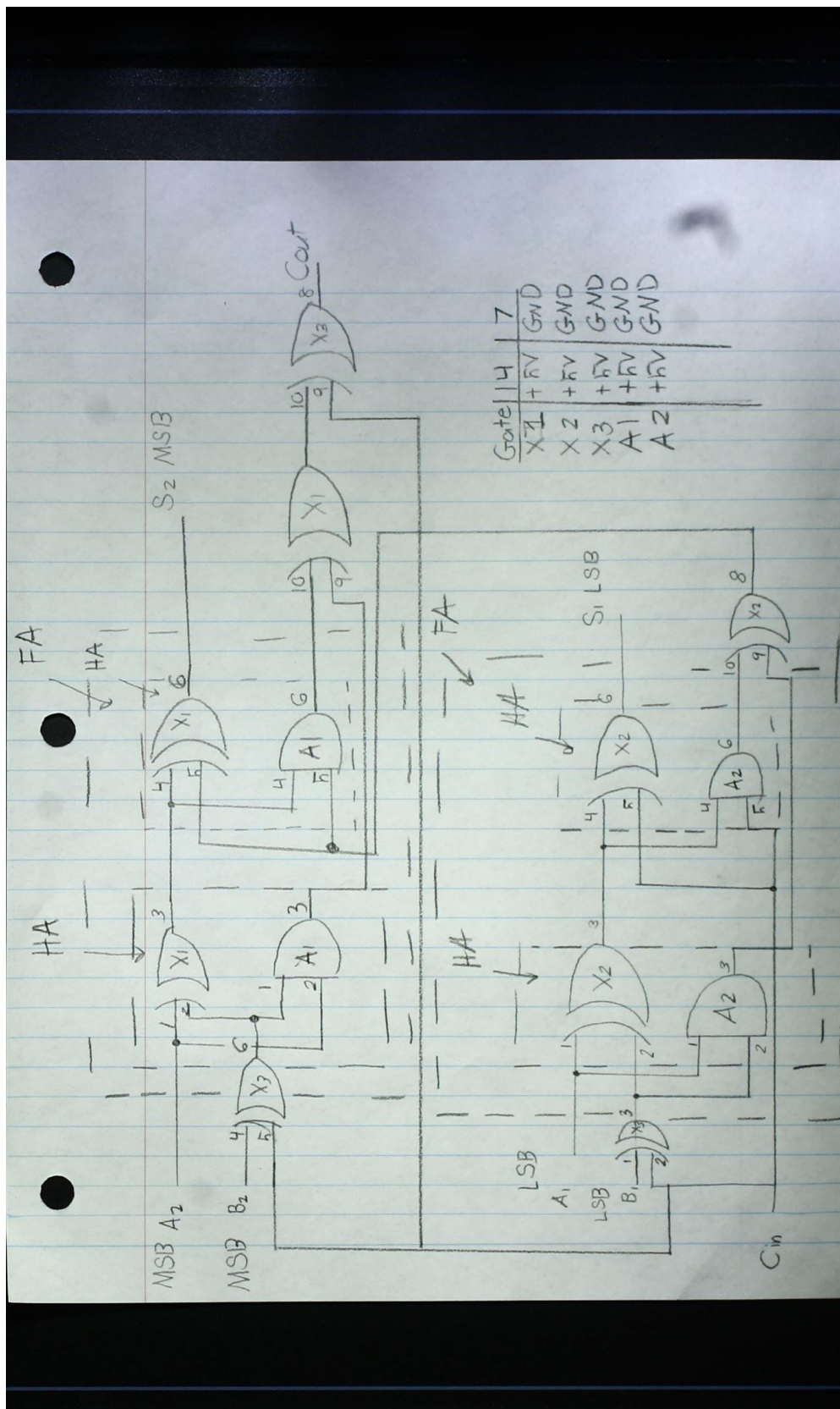


Figure 2: Two-Bit Adder/Subtractor Schematic

Circuit Demonstration Page

Student names: Jake Simmons
Haonan Jin

Instructor Signatures

Separate Full Adders Del Walton

Two-Bit Adder Del Walton

Adder/Subtractor Del Walton

Inputs		Expected Results			Actual Results
A	B	B 2's comp	Sub	Dec	Sub
00	01	11	011	3	111
00	10	10	010	2	110
00	11	01	001	1	101
01	01	11	100	-4	000
10	01	11	101	-3	001
10	00	00	110	-2	010

Figure 3: Circuit Demonstration Page

Code

Include all of the code you wrote or modified here.