

ELC 2137 Lab 2: Transistor Logic Gates

Jake Simmons and Haonan Jin

January 23, 2020

Summary

The purpose of this lab was to build and explore the behaviors of logic gates. We first built an OR Gate, second a Not Gate and third a Nor Gate. The last logic gate was a combination of two Not Gates and a single Nor Gate.

Q&A

1. What logic operation does the Final gate implement?
 - (a) The logic operation that the Final gate implements is an And Gate.

Results

A	B	Led
0	0	0
0	1	0
1	0	0
1	1	1

Table 1: Truth Table of the Final gate.

Code