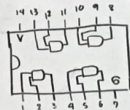


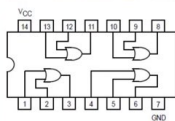
# Data sheet

or 2

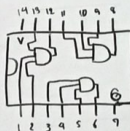
And 2: 7408



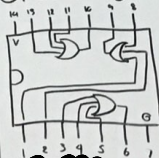
**74LS32**



And 3: 7411

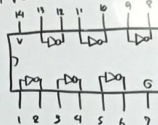


Or 3: 4075

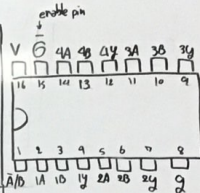


Circuit Board

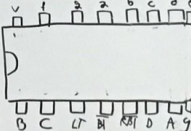
Not: 7404



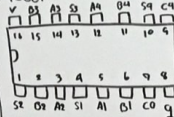
Mux: 74159



Decoder: 74159N



Adder: 74283



7-Seq



ก้อง 6210554717  
 Patty 6210554707  
 ชื่น 6210554725

		m		3 2 1 0				out put			
		$m_1$	$m_2$	$k_3$	$k_2$	$k_1$	$k_0$	A	B	C	D
0	4 ตัว	1	0	0	0	0	0	0	1	0	0
1	4 ตัว	1	0	0	0	0	1	0	1	0	0
2	4 ตัว	1	0	0	0	1	0	0	1	0	0
3	0 ตัว	1	0	0	0	1	1	0	0	0	0
4	3 ตัว	1	0	0	1	0	0	0	0	1	1
5	7 ตัว	1	0	0	1	0	1	0	1	1	1
6	3 ตัว	1	0	0	1	1	0	0	0	1	1
7	4 ตัว	1	0	0	1	1	1	0	1	0	0
8	0 ตัว	1	0	1	0	0	0	0	0	0	0
9	1 ตัว	1	0	1	0	0	1	0	0	0	1
				1	0	1	0	d	d	d	d
				1	0	1	1	d	d	d	d
				1	1	0	0	d	d	d	d
				1	1	0	1	d	d	d	d
				1	1	1	0	d	d	d	d
				1	1	1	1	d	d	d	d

1	0	1	0	d	d	d	d
1	0	1	1	d	d	d	d
1	1	0	0	d	d	d	d
1	1	0	1	d	d	d	d
1	1	1	0	d	d	d	d
1	1	1	1	d	d	d	d

A

$k_3, k_2$ \ $k_1, k_0$	00	01	11	10
00	0	0	0	0
01	0	0	0	0
11	d	d	d	d
10	0	0	d	d

B

$k_3, k_2$ \ $k_1, k_0$	00	01	11	10
00	1	0	0	1
01	0	1	1	0
11	d	d	d	d
10	0	0	d	d

C

$k_3, k_2$ \ $k_1, k_0$	00	01	11	10
00	0	0	0	0
01	1	1	0	1
11	d	d	d	d
10	0	0	d	d

D

$k_3, k_2$ \ $k_1, k_0$	00	01	11	10
00	0	0	0	0
01	1	1	0	1
11	d	d	d	d
10	0	1	d	d

$$\bar{k}_3 \bar{k}_2 \bar{k}_0 + \bar{k}_3 \bar{k}_1 k_0 + k_3 k_0$$

$$k_2 \bar{k}_1 + k_2 k_0$$

$$k_2 \bar{k}_1 + k_2 k_0 + k_3 k_0$$

$$\bar{k}_3 \cdot (\bar{k}_2 \bar{k}_0 + \bar{k}_1 k_0) + k_3 k_0$$

