1 Code File list

1.1 Embedded C

 $\mathbf{Embedded} \ \mathbf{C} \ \mathrm{main} \ \text{-} \ \mathrm{main.c}$

Embedded C Digiswitch

Digiswitch Digiswitch source - Digiswitch.c

Digiswitch Digiswitch header - Digiswitch.h

Embedded C PID

PID PID source - PID.c

 ${f PID}$ PID header - PID.h

Embedded C Keypad

 ${f Keypad}$ Keypad source - Keypad.c

Keypad Keypad header - Keypad.h

Embedded C UI

UI UI source - UI.c

UI UI header - UI.h

Embedded C Blinking

 ${\bf Blinking}\;$ Blinking source - Blinking.c

 ${\bf Blinking}\;$ Blinking header - Blinking.h

Embedded C SPI

SPI SPI source - SPI.c

 ${f SPI}$ SPI header - SPI.h

Embedded C UART

UART uart source - uart.c

 \mathbf{UART} uart header - uart.h

 ${\bf Embedded} \,\, {\bf C} \,\, {\rm systick_frt}$

 ${\bf systick_frt} \ \ {\bf systick_frt} \ \ {\bf source - systick_frt.c}$

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Code File list

 ${\bf systick_frt} \ \ {\bf systick_frt} \ \ {\bf header-systick_frt.h}$

Embedded C Config - Config.h

 ${\bf Embedded} \,\, {\bf C} \,\, {\bf emp_type \, \text{-} \, emp_type.h}$

Embedded C tm4c123gh6pm - tm4c123gh6pm.h

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1.2 VHDL

```
VHDL Top level - Info test.sch
```

Top level Communication - Communication.sch

Communication RecMode - RecMode.vhdl

Communication SPISlave - SPI.vhdl

Communication Toggler - Toggler.vhdl

Communication TraMode - TraMode.vhdl

Top level Control - Control.sch

Control ModPWM - ModPWM.vhdl

Control Mux8x2 - Mux8x2.vhdl

Control PanCalibration - PanCal.vhdl

Control TiltCalibration - TiltCal.vhdl

Top level Encoders - Encoders.sch

Encoders EncoderFilter - EncoderFilter.vhdl

Encoders Dir V3 - Dir V3.vhdl

Encoders SCount - SCount.vhdl

 ${\bf Encoders}\;\; {\bf Error Memory.vhdl}\;\;$

Top level InfoDis - InfoDis.sch

 $\mathbf{InfoDis}$ BinaryTBcd - BinaryTBcd.sch

BinaryTBcd AutoLoadSignal - AutoLoadSignal.vhdl

BinaryTBcd BcdDigit - BcdDigit.vhdl

 ${\bf BinaryTBcd} \ \, {\rm BinaryToBcd} \cdot {\rm BinaryToBcd}. vhdl$

InfoDis Multiplexed Display - Multiplexed Display.sch

 ${\bf Multiplexed_Display} \ \ {\bf clkDiv1kHz} \ - \ {\bf clkDiv1kHz}. {\bf vhdl}$

 ${\bf Multiplexed_Display}\ \ {\bf Count_2_bit}\ -\ {\bf Count_2_bit}. vhdl$

Multiplexed Display Encoder_2x4 - Encoder_2x4.vhdl

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 ${\bf Multiplexed_Display}\ {\bf Mux_4x1_bit-Mux_4x1_bit.vhdl}$

 ${\bf Multiplexed_Display}\ {\bf Mux_4x8_bit-Mux_4x8_bit.vhdl}$

 ${\bf Multiplexed} \quad {\bf Display} \ \, {\bf SegmentDecoder} \, \, {\bf -} \, \, {\bf SegmentDecoder.vhdl}$

 $\mathbf{InfoDis}$ BlinkDp - Blink.vhdl

InfoDis clkDiv1Hz - clkDiv10Hz.vhdl

 ${f InfoDis}\ {f clkDiv2Hz}$ - ${f clkDivNHz.vhdl}$

InfoDis Count1Bit - Count1Bit.vhdl

 $\mathbf{InfoDis}\ \mathrm{Mux}4\mathrm{x}2\text{ - }\mathrm{Mux}_4\mathrm{x}2.\mathrm{vhdl}$

 $\mathbf{InfoDis}$ $\mathbf{Mux}1\mathbf{x}2$ - $\mathbf{Mux}1\mathbf{x}2.\mathbf{vhdl}$

InfoDis Mux8x2 - Mux8x2.vhdl

InfoDis Mux16x2 - Mux16x2.vhdl

InfoDis PWMToLED - PWMToLED.vhdl

InfoDis SCount16Bit - SCount16Bit.vhdl

InfoDis SignedToUsigned - SigendToUsigen.vhdl

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1.3 MATLAB

 \mathbf{MATLAB}

 \mathbf{MATLAB}

 \mathbf{MATLAB}

 $\mathbf{MATLAB} \ \mathrm{LQR}$

 \mathbf{LQR} Main LQR Project - LQR
projekt.m

 \mathbf{LQR} Nbar calculation script - rscale.m

 \mathbf{LQR} Simulink file for LQR - LQR
sim.slx