

INTRODUCTION (GGE302)

Project Name:

SP10-GGE302 DC DC Synchronous Auto Grade

Objective- To Development Electronics part (DC Supply) for with auto grade components

Scope- DC DC Converter is converter which converter High DC voltage in to Low voltage with below Spec.

Input -35-90V ,Output Rating 12V-10A and 5V 1A DC ,Enclosure- IP 66.

Hardware side - Designing part of his Converter using Synchronous buck converter

Software side- No any scope of work.

Mechanical Side- To develop IP 67 Enclosure in Aluminum casing

Measurement Goals - SV: $\pm 20\%$, PDD: 0.10 ± 0.02 , PPDD : 0.20 ± 0.02

Link to Project Data : http://192.168.100.9:8080/svn/DC_DC_Converter/SP10_GGE302

Team Size: 10 Nos. Effort Size: 201.04 hrs. Time Line : 07-09-2022 to 31-10-2022

Actual Scheduled Start to planed Finish Date: 19-7-2022 to 01-11-2022

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Estimating and Planning

High Level Estimation in start meeting with Sr. Management.

- Complexity -Medium Type
- Cost approx 400-450 Rs (assumption with 2 nos Mosfet -160Rs , 2 Inductor 50Rs , 6 nos capacitor -60 ,enclosure-30rs and harness 30rs ,PWM controller-80rs and converter-25Rs)
- Duration-35-40 day (design-7 days, design implementation - 15 days,Testing 10days,validation 5days , other activity-5 days)
- effort- high level reference of GGE295 approx 180-190persone hours


S. No.	Reference Project Name	Why Selected as Reference	Actual Efforts in RD Phase & Planning Phase	Actual Efforts in Design and Implementation	Actual Efforts in Testing and integration	Actual Efforts in Validation Phase , Closure	Actual Efforts in End
1	GGE295 DC-DC Converter 12V-10A & 5V-1A	Capacity Rating is approx equal and input na d output spec. also approx equal.	27	45	27	53	22
Average Efforts in person hours			27	45	27	53	22

	Estimated Efforts in RD Phase & Planning Phase	Estimated Efforts in Design and Implementation	Estimated Efforts in Testing and integration	Estimated Efforts in Validation Phase , Closure	Estimated Efforts in End	
Past Projects	27	45	27	53	22	
Differences from reference projects	4	7	4	9	4	Total
Final estimates	31	52	31	62	26	202

	Difference from reference in detail	Impact in person hours due to the difference	Complexity	Remarks (Optional)
1	Enclosure Design	-8	H	First time development in Reference Project
2	Auto grade components selection and arrangement	8	H	First time development
3	Synchronous Buck converter Design	10	H	First time development
4	PCB layout and new component footprint making	8	H	First time development
5	5V design	4	M	First time development
6	5V PCB layout	4	M	First time development
7	Testing	10	H	First time testing
8	Readability of Power device and Inductor	-4	M	Reuse components
9	Testing Ids	-4	M	Reuse components
Total Impact		28		

Size	5H+4M+0L
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Project Planning in Einframe - Project Plan Link-<https://gil.einframe.com/rptprojectoverview.aspx>

Project Team

Project Manager-	SP
Hardware Designer-	SP,AK
Mechanical Designer-	BK
PCB Designer-	RP
Validation Manager-	JM
Integrator-	RJ
Validator-	RS
Material Management-	SJ
Reviewer-	SJ,SRS,SP,JM
Auditor-	SW
Senior Management-	TG
Assembler-	PK,RK

- Task list making
- Team assign in task with tools (asset and work environment)
- Task approval (Time sheet approval)
- Deployment of plan with team
- Operation and support transition plan
- Team Meeting Planning (6 Times)
- Sr. Management review (8 times)
- Critical dependency (two)
- Audit planning (3 times)
- Review planning at significant task
- CM planning

Skill Category	- ACTUAL -					- REQUIREMENT -					- GAPS -					ACTIONS
	1	2	3	4	5	1	2	3	4	5	1	2	3	4	5	
Leadership Set																
Project Management	0	0.64	0.1	0	0	0	0.2	0	0	0	0	0.44	0.1	0	0	
General Management	0.1	0.4	0.3	0	0	0	0	0.05	0	0	0.1	0.4	0.25	0	0	
Operational Set																
Embedded (ST/Expressive Platform)	0.15	0	0	0	0	0	0	0	0	0	0.15	0	0	0	0	
Hardware Design like SMPS, H-Bridge etc	0.9	0.87	0.4	0	0	0	0.1	0.02	0	0	0.9	0.47	0.38	0	0	
Hardware PCB Design	0.48	0	0.1	0	0	0	0	0	0	0	0.48	0	0.1	0	0	
Schematic and PCB Design	0.15	0.3	0.9	0.1	0	0	0	0.11	0	0	0.15	0.3	0.79	0.1	0	
2D & 3D Modeling	0	0	0.9	0.1	0	0	0	0.08	0	0	0	0	0.83	0.1	0	
Tool / Application development on VLSI, Android, iOS	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Testing & Validation (Product & Components)	0.17	0.5	0.57	0	0	0	0	0.35	0	0	0.17	0.5	0.22	0	0	
SAP - MM	0.02	0.1	0	0.5	0	0	0	0.06	0	0	0.02	0.1	0.06	0.5	0	
Process Management	0	0.05	1.4	0	0	0	0	0.01	0	0	0	0.05	1.39	0	0	

Validate Plan
Add Team Member
Add New Task
Load Tasks from Template

Add new task

Task Name:

Task Category:

Lead/Lag: days

Duration: days


Select Team Members:

Member Name	Role	Involved
<input type="checkbox"/> Adishank Gupta	designer	0 %
<input type="checkbox"/> Bharat Kakra	designer	0 %
<input type="checkbox"/> Bharat Sharma	designer	0 %
<input type="checkbox"/> CRO 4 CHANNELS TEKTRONIX TPS2024	designer	0 %
<input type="checkbox"/> CRO FLUXE 190-204	designer	0 %
<input type="checkbox"/> Deepesh Jain	designer	0 %

Select Predecessors:

☐ 165 - Start
☐ 166 - Test Gate 0
☐ 167 - Test Gate 1
☐ 168 - Test Gate 2
☐ 173 - making instant sensor software development
☐ 170 - wiring Diagram

Name of Asset	Source	Status
CAD for mechanical design	In-House	Available
MS Office	In-House	Available
Multimeter	In-House	Available
Clamp Meters	In-House	Available
Einframe	In-House	Available
SVN Server	In-House	Available
Altium	In-House	Available
Heat Chamber	In-House	Available
CRO TPS 2024	In-House	Available
Power Analyzer WT 230	In-House	Available
Thermal Imager	In-House	Available
Multi-Sim Hardware Design Software	In-House	Available
CRO Yokogawa DLM 3024	In-House	Available
Vibration tester	Network of Excellence (Vibration Test Setup)	Relationship Exists



EXECUTION

Scheduled Start	2022-9-7
Target Finish	2022-10-31
Scheduled Finish	2022-11-1
Priority	Medium
Add to Roadmap	Yes

PROJECT TEAM

Team Member	Role
Sobhag Prajapat	Project Manager
Adarsh Kumar Bairwa	Hardware designer
Bharat Kakra	Mechanical designer

Efforts estimation (Project Size), Meeting for Planning

Sobhag Prajapat [40%]

Adarsh Kumar Bairwa [20%]

Bharat Kakra [20%]

Jalaj Mathur [20%]

Rakesh Pandey [20%]

Ravi Jindal [20%]

Rohit Kumar Sachan [20%]

Sandeep Jain [20%]

Shweta Aggarwal [20%]

Syngiram Sharma [20%]

ASSET REQUIREMENT

Name of Asset	Source	Status
CAD for mechanical design	In-House	Available
MS Office	In-House	Available
Multimeter	In-House	Available
Clamp Meters	In-House	Available
Einframe	In-House	Available

Plan Review with checklist: defects resolve & Close

Sobhag Prajapat [24%]

Jalaj Mathur [12%]

Meeting to deploy plan , Plan Approval & Publish

Sobhag Prajapat [20%]

Adarsh Kumar Bairwa [5%]

Bharat Kakra [5%]

Jalaj Mathur [5%]

Rakesh Pandey [5%]

Ravi Jindal [5%]

Rohit Kumar Sachan [5%]

Sandeep Jain [5%]

Shweta Aggarwal [5%]

Syngiram Sharma [5%]

Tenu Gupta [5%]

Senior Management Review with Metrics Report , Audit in RD Phase ,Audit NC defect and close

Sobhag Prajapat [70%]

Shweta Aggarwal [15%]

Tenu Gupta [5%]

Module testing and Support documentation ; defects resolve & Close

Clamp/Meter Meter [30%]

Multimeter Meter [30%]

Adarsh Kumar Bairwa [25%]

Thermal Imager [15%]

CRO 4 CHANNELS TEKTRONIX TPS2024 [10%]

Heat Chamber Setup [10%]

Sobhag Prajapat [5%]

Pawan Kumar Sharma [5%]

POWER ANALYZER WT 230 [2%]

LCR METER [1%]

MICRO OHM METER [1%]

Team Meeting on integration phase

Adarsh Kumar Bairwa [4%]

Bharat Kakra [4%]

Jalaj Mathur [4%]

Rakesh Pandey [4%]

Ravi Jindal [4%]

Rohit Kumar Sachan [4%]

Sandeep Jain [4%]

Shweta Aggarwal [4%]

Sobhag Prajapat [4%]

ID	Task Name	Project Management	Start	End	Duration	Predecessors	Relationship
1461	Project Creation and Preliminary Planning up to TG1	Project Management	1460	2022-9-8	0 (days)	1 (days)	2022-9-9
1462	Kick Off Meeting , MOM	Project Management	1476, 1461	2022-9-9	0 (days)	1 (days)	2022-9-10
1463	Requirement Capture & Elicitation	Project Management	1460, 1461	2022-9-9	0 (days)	1 (days)	2022-9-10
1464	Functional Specifications And RFT	Project Management	1462, 1463	2022-9-10	0 (days)	1 (days)	2022-9-12

Record task completion

Task ID & Description	Planned Start/End	Actual Start/End	Sch. Variance	Planned Efforts	Actual Efforts	Effort Variance	Planned Cost	Actual Cost	Cost Variance
[1482] Kick Off Meeting , MOM ✔ Approved Latest Comments: Task completed and MOM shared with team	Sep 09, 2022 Sep 10, 2022	Sep 10, 2022 Sep 10, 2022	-100%	2.4	3.25	35%	2.4	3.25	35%
<div><div>Jadav Mathur (50%)</div><div>Sandeep Jain (50%)</div><div>Shweta Aggarwal (50%)</div><div>Subhash Prasad (50%)</div><div>Tarun Gupta (50%)</div></div>									

TASK APPROVAL

[HOME](#) / [DATA CAPTURE](#) / [INNOVATIVE DEVELOPMENT](#) / [TASK APPROVAL](#)

APPROVE TIME SHEET ENTRIES AGAINST PROJECT TASKS

Select Project *	SP10-GGEG302 DC DC Synchronous Auto Gra	View All Recs
Select Task *	1551 - Sr. M Review in design phase with M	View Timesheet
First Timesheet Entry	Oct 15, 2022	
Last Timesheet Entry	Oct 15, 2022	
Person Hours	0.25 Hrs.	
Marked Finished by All	No	
Actual Start *	Oct 15, 2022	
Actual End *	Oct 15, 2022	
Comments *		

[View Timesheet](#)

Task Name: [1482] Kick Off Meeting , MOM

Team Member	Activity Type	Description	Date & Time Range	Status
Tarun Gupta	Project Assigned	Take care of Schedule variance	Sep 10, 2022 10:45 to 11:30	Finished
Jalaj Mathur	Project Assigned	As validation manager kick off meeting attended.	Sep 10, 2022 10:45 to 11:30	Finished
Sobhag Prajapat	Project Assigned	Team meeting done	Sep 10, 2022 10:45 to 11:30	Finished
Sobhag Prajapat	Project Assigned	MOM Making	Sep 10, 2022 14:00 to 14:15	Finished
Sandeep Jain	Project Assigned	Attend Meeting and understand the project	Sep 10, 2022 10:45 to 11:30	Finished

Close

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- Identify issues and resolution
- Tracking review defect and status.

- Managing upcoming task due to delay in last task

INCIDENT MANAGEMENT

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Search Incidents

Enter search criteria and click 'Search' below

Status:	All	Additional Search Criteria (for closed incidents only)	
Business Unit:	Genus Innovation	Product (IP Affected)	Select
Type:	Project Related	Defect Source	Select
Project:	SPH-602532 OC OC Synch	Fixed in Team Category	Select
Stage:	All	Resolution Type	Select
Report Type:	All	Resolution Date Range	<input type="text"/> <input type="text"/>
Assignee:	Select User	Contains Learning	<input type="checkbox"/>
Logged Date Range:	<input type="text"/> <input type="text"/>		

[15647] Module used for development Initial Commitment: 10/28/2022 11/01/2022 11/02/2022 11/03/2022 11/04/2022 11/05/2022 11/06/2022 11/07/2022 11/08/2022 11/09/2022 11/10/2022 11/11/2022 11/12/2022 11/13/2022 11/14/2022 11/15/2022 11/16/2022 11/17/2022 11/18/2022 11/19/2022 11/20/2022 11/21/2022 11/22/2022 11/23/2022 11/24/2022 11/25/2022 11/26/2022 11/27/2022 11/28/2022 11/29/2022 11/30/2022 12/01/2022 12/02/2022 12/03/2022 12/04/2022 12/05/2022 12/06/2022 12/07/2022 12/08/2022 12/09/2022 12/10/2022 12/11/2022 12/12/2022 12/13/2022 12/14/2022 12/15/2022 12/16/2022 12/17/2022 12/18/2022 12/19/2022 12/20/2022 12/21/2022 12/22/2022 12/23/2022 12/24/2022 12/25/2022 12/26/2022 12/27/2022 12/28/2022 12/29/2022 12/30/2022 12/31/2022 1/01/2023 1/02/2023 1/03/2023 1/04/2023 1/05/2023 1/06/2023 1/07/2023 1/08/2023 1/09/2023 1/10/2023 1/11/2023 1/12/2023 1/13/2023 1/14/2023 1/15/2023 1/16/2023 1/17/2023 1/18/2023 1/19/2023 1/20/2023 1/21/2023 1/22/2023 1/23/2023 1/24/2023 1/25/2023 1/26/2023 1/27/2023 1/28/2023 1/29/2023 1/30/2023 1/31/2023 2/01/2023 2/02/2023 2/03/2023 2/04/2023 2/05/2023 2/06/2023 2/07/2023 2/08/2023 2/09/2023 2/10/2023 2/11/2023 2/12/2023 2/13/2023 2/14/2023 2/15/2023 2/16/2023 2/17/2023 2/18/2023 2/19/2023 2/20/2023 2/21/2023 2/22/2023 2/23/2023 2/24/2023 2/25/2023 2/26/2023 2/27/2023 2/28/2023 2/29/2023 3/01/2023 3/02/2023 3/03/2023 3/04/2023 3/05/2023 3/06/2023 3/07/2023 3/08/2023 3/09/2023 3/10/2023 3/11/2023 3/12/2023 3/13/2023 3/14/2023 3/15/2023 3/16/2023 3/17/2023 3/18/2023 3/19/2023 3/20/2023 3/21/2023 3/22/2023 3/23/2023 3/24/2023 3/25/2023 3/26/2023 3/27/2023 3/28/2023 3/29/2023 3/30/2023 3/31/2023 4/01/2023 4/02/2023 4/03/2023 4/04/2023 4/05/2023 4/06/2023 4/07/2023 4/08/2023 4/09/2023 4/10/2023 4/11/2023 4/12/2023 4/13/2023 4/14/2023 4/15/2023 4/16/2023 4/17/2023 4/18/2023 4/19/2023 4/20/2023 4/21/2023 4/22/2023 4/23/2023 4/24/2023 4/25/2023 4/26/2023 4/27/2023 4/28/2023 4/29/2023 4/30/2023 5/01/2023 5/02/2023 5/03/2023 5/04/2023 5/05/2023 5/06/2023 5/07/2023 5/08/2023 5/09/2023 5/10/2023 5/11/2023 5/12/2023 5/13/2023 5/14/2023 5/15/2023 5/16/2023 5/17/2023 5/18/2023 5/19/2023 5/20/2023 5/21/2023 5/22/2023 5/23/2023 5/24/2023 5/25/2023 5/26/2023 5/27/2023 5/28/2023 5/29/2023 5/30/2023 5/31/2023 6/01/2023 6/02/2023 6/03/2023 6/04/2023 6/05/2023 6/06/2023 6/07/2023 6/08/2023 6/09/2023 6/10/2023 6/11/2023 6/12/2023 6/13/2023 6/14/2023 6/15/2023 6/16/2023 6/17/2023 6/18/2023 6/19/2023 6/20/2023 6/21/2023 6/22/2023 6/23/2023 6/24/2023 6/25/2023 6/26/2023 6/27/2023 6/28/2023 6/29/2023 6/30/2023 7/01/2023 7/02/2023 7/03/2023 7/04/2023 7/05/2023 7/06/2023 7/07/2023 7/08/2023 7/09/2023 7/10/2023 7/11/2023 7/12/2023 7/13/2023 7/14/2023 7/15/2023 7/16/2023 7/17/2023 7/18/2023 7/19/2023 7/20/2023 7/21/2023 7/22/2023 7/23/2023 7/24/2023 7/25/2023 7/26/2023 7/27/2023 7/28/2023 7/29/2023 7/30/2023 7/31/2023 8/01/2023 8/02/2023 8/03/2023 8/04/2023 8/05/2023 8/06/2023 8/07/2023 8/08/2023 8/09/2023 8/10/2023 8/11
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6. Minutes of Meeting Details

Name of Meeting	Team Meeting on integration phase
Date and Time	Oct 18, 2022 17:00 to 17:20 (20 min.)
Project's Phase	Integration phase
Participants	Sobhag Prajapat, Adarsh Kumar, Ravi Jindal, Rakesh Panday, Sundeeep Jain, Rohit Sachan, Jalaj Mathur, Bharat Kakra
Absent	Shweta Aggarwal

S.No	Agenda Item	Discussion	Decision/Action Item/Issue
1	Pending action items	Not available	
2	Discussion on Delay Task	Design implementation and review task delay by 150Day	
3	Corrective action for managing project cycle and delay schedule.	<p>Task-[15551] Module testing and Support documentation ; defects resolve & Close - 5 day with 30% per day of Rs. we have to manage this work by putting 70-80% effort per day and close the task within 2-3 day.</p> <p>Task-[15552] Integration & Testing and Supporting Documentation ; defects resolve & close - 4 day with 50% per day of Rs. we have to manage this work by putting 70-80% effort per day and close the task within 1-2 day.</p> <p>Task-[15557] System testing (Validation) and Support Documentation - 5 day with 50% per day of Rs. we have to manage this work by putting 70-80% effort per day and close the task within 3-4 day.</p>	<p>We have to take action on PCB and prototype assembly approx 8 nos so below action item is generated.</p> <p>1. Adarsh identify the required material and request with google form.</p> <p>2. Sundeeep sir will take necessary action for requested material .</p>

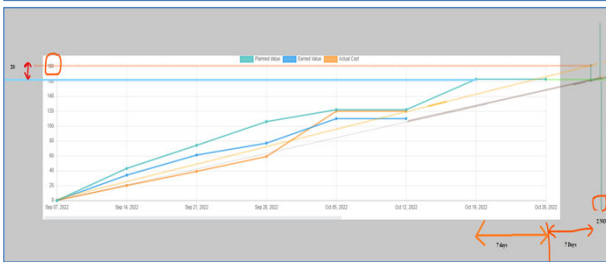
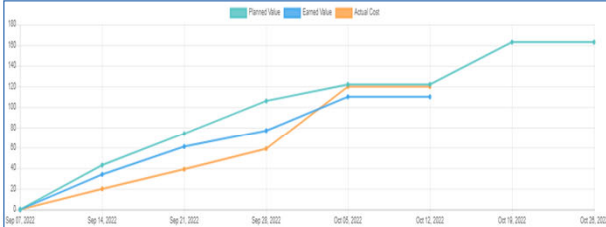
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EVMS CHART

Causal Analysis, Corrective action, Preventive action



Dates	Sep 07, 2022	Sep 14, 2022	Sep 21, 2022	Sep 28, 2022	Oct 05, 2022	Oct 12, 2022	Oct 19, 2022	Oct 26, 2022
Planned Value	0	43	74	106	122	122	163	163
Earned Value	0	34	61	77	110	110	-	-
Actual Cost	0	20	39	59	120	120	-	-



Weeks	No of Week	Name of Phase	Planned Value	Earned Value	Actual Cost	Schedule variance (EV-PV/PV)*100	Cost Variance (AC-EV/EV)*100
1st	07-09-22 to 14-09-22	RD Phase, Planning Phase	43	34	20	-20.93%	-41.17%
2nd	14-09-22 to 21-09-22	Design and Implementation	74	61	39	-17.56%	-36.06%
3rd	21-09-22 to 28-09-22	Design and Implementation	106	77	59	-27.35%	-23.37%
4th	28-09-22 to 05-10-22	Testing and integration	122	110	120	-9.83%	-9.09%
5th	05-10-22 to 12-10-22	Testing and integration	122	110	120	-9.83%	-9.09%
6th	13-10-22 to 19-10-22	Validation Phase, Closure	163				
7th	20-10-22 to 26-10-22	Validation Phase, Closure	163				
8th	27-10-22 to 01-11-22	Validation Phase, Closure	201.2				

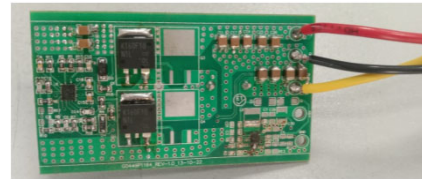
<GGE302> METRICS REPORT

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Project is Running in Module testing
GGE302_HWTACS

Test Case ID	Test Case Description	Sub-Module Name	Tools Required	Testing Steps	Expected Result	Actual Result	PCB Number	Pass/Fail
HWD_31_A	UNLID and Start Up Test	NA	Multimeter, Power Supply	1. Power supply DC supply connected to modules 2. Increase voltage from 0 to 40 Volt 3. Check output voltage, measure low cut recovery and low cut voltage at no load and at 100% resistive load (1.2 COEHL) 3. Step change from 0% load to 100% load at 40V input capture the QSP voltage waveform and measure undershoot. 4. Step change from 100% load to 0% load at 40V input, capture the QSP voltage waveform and measure overshoot.	Low cut recovery voltage -30V +/- 5V Low cut voltage -20 mV +/- 5V			
HWD_31_B	Voltage Regulation Test	NA	Multimeter, Power Supply, CRO	1. Step change from 100% load to 0% load at 40V input, capture the QSP voltage waveform and measure undershoot. 2. Step change from 0% load to 100% load at 40V input, capture the QSP voltage waveform and measure overshoot. 3. Step change from 40 V to 50V at 100% load, capture the QSP voltage waveform and measure overshoot. 4. Step change from 50V to 40V at 100% load, capture the QSP voltage waveform and measure overshoot.	voltage regulation = +/- 5%			



GGE302_MCTCAS

Test Case ID	Test Case Description	Part Name	Tools Required	Testing Steps	Expected Result	Actual Result	Drawing Number	Pass/Fail	Remarks
MCHT_1	Measure Outer size and all bendings of aluminium part	BODY	Varnier caliper, Tape, Scale	Measurement as per drawing.	85X50X33MM				
MCHT_2	Measure position and hole dia. for mosfet support clip	Body	Varnier caliper, scale	Match the dimensions c to c Check fitment	As per drawing				

GGE302_INTCAS

Test Case ID	Test Case Description	Inputs	Tools Required	Testing Steps	Expected Result	Actual Result	Pass/Fail	Remarks
INT_1	Integrated side cover and grove-met	Side Plate HW_2896 Rubber grove-met	By Hand	Rubber grove-met push into the side cover hole by fingers	Rubbergrovemmet must properly fixed			
INT_2	Integrated cover, grove met, Harness HW_2857 and Harness HW_2858	Step 1 outcome Harness HW_2857 Harness HW_2858	By Hand	Insert the each wire into the rubber grove met up to sleeve	Wire insertion should properly.			