

Bachelor of Science in Computer Science and Engineering, AUST

Semester Wise Class Schedule, Spring, 2020

Year: 2, Semester: 1 (Section B)

Time/Day	08:00 AM-08:50 AM	08:50 AM-09:40 AM	09:40 AM-10:30 AM	10:30 AM-11:20 AM	11:20 AM-12:10 PM	12:10 PM-01:00 PM	01:00 PM-01:50 PM	01:50 PM-02:40 PM	02:40 PM-03:30 PM	03:30 PM-04:20 PM	04:20 PM-05:10 PM	05:10 PM-06:00 PM
SUN				HUM 2109 7A05 (Zerin)	CSE 2105 7A05 (QNE)	CSE 2103 7A05 (EMAM HOSSAIN)	CSE 2100(B1/B2) 7B05 (Choudhury, Rab)					
MON	HUM 2109 7A05 (Zerin)	EEE 2141 7A05 (TBA)	MATH 2101 7A05 (Jahan)	EEE 2142(B1) 3B06 (SAM, Pathan)								
				CSE 2106(B2) 9A06 (Khondaker, QNE)								
TUE				CSE 2104(B2) 7B06 (Rahman, Jubair)			CSE 2105 7A03 (QNE)	EEE 2141 7A03 (TBA)	MATH 2101 7A03 (Jahan)			
WED							HUM 2109 7A03 (Zerin)	CSE 2103 7A03 (EMAM HOSSAIN)	CSE 2105 7A03 (QNE)	CSE 2104(B1) 7B08 (EMAM HOSSAIN)		
THU	CSE 2106(B1) 9A06 (QNE, Khondaker)			CSE 2103 7C07 (EMAM HOSSAIN)	MATH 2101 7C07 (Jahan)	EEE 2141 7C07 (TBA)	EEE 2142(B2) 3B06 (Ahmed, Ahmad)					

Course No	Course Title	Credit Hour	Section	Course Teacher
CSE 2100	Software Development-II	0.75	B1/B2	Ms. Moumita Choudhury, Ms. Raqeebir Rab
CSE 2103	Data Structures	3.0	B	Mr. Emam Hossain
CSE 2104	Data Structures Lab	1.5	B2	Mr. Mohammad Marufur Rahman, Mr. Mohammad Imrul Jubair
			B1	Mr. Emam Hossain
CSE 2105	Digital Logic Design	3.0	B	Ms. Qamrun Nahar Eity
CSE 2106	Digital Logic Design Lab	1.5	B2	Mr. Md. Tawkat Islam Khondaker, Ms. Qamrun Nahar Eity
			B1	Ms. Qamrun Nahar Eity, Mr. Md. Tawkat Islam Khondaker
EEE 2141	Electronic Devices & Circuits	3.0	B	
EEE 2142	Electronic Devices & Circuits Lab	1.5	B2	Mr. Mushfiqul Ahmed, Mr. Tanvir Ahmad
			B1	Mr. Sameer Ahmed Khan Mojish, Mr. Tyafur Rahman Pathan
HUM 2109	Society, Ethics and Technology	3.0	B	Ms. Sanjida Sultana Zerin
MATH 2101	Mathematics-III	3.0	B	Ms. Samsad Jahan