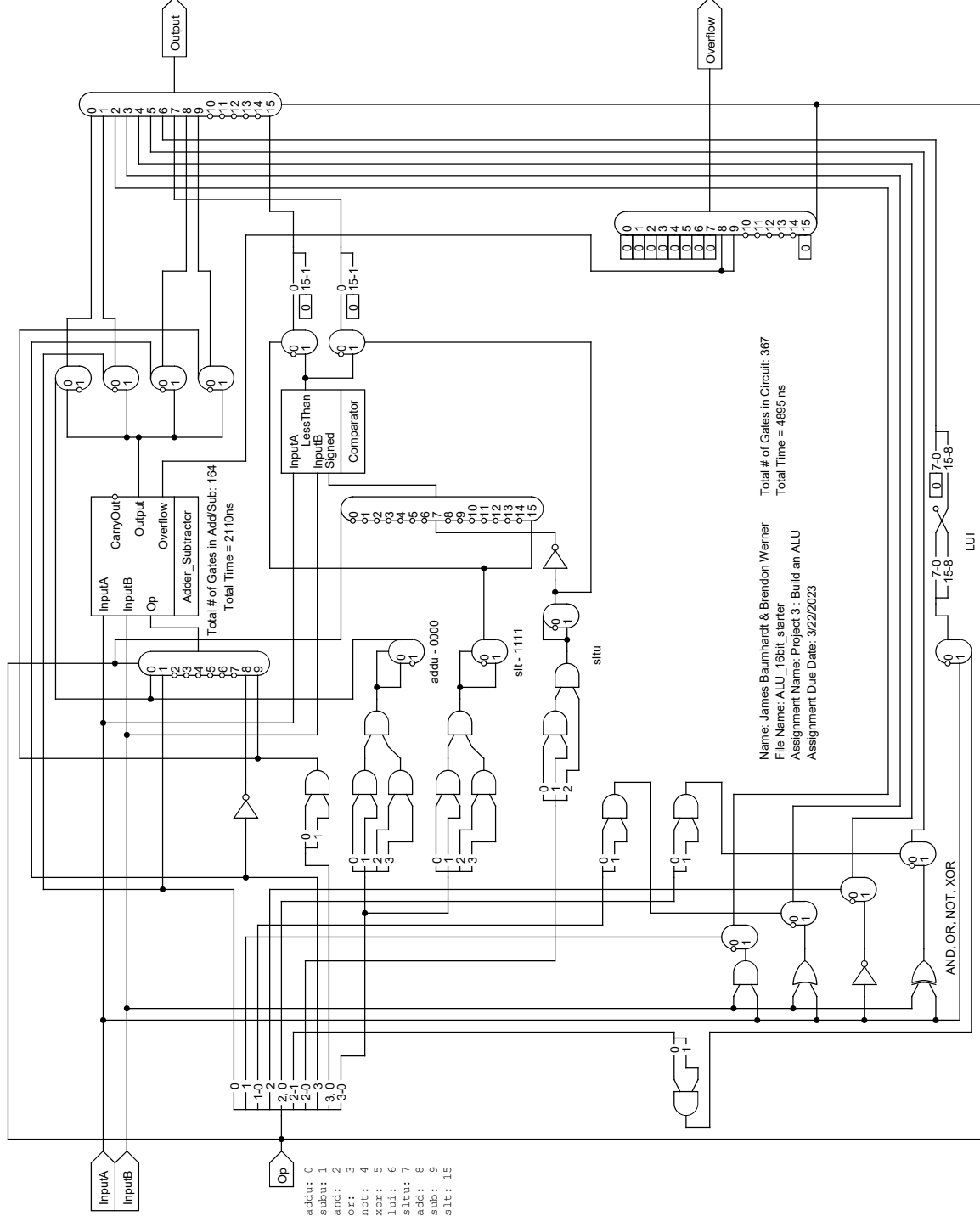


ALU_16bit_starter



Adder_Subtractor in ALU_16bit_starter

Name: James Baumhardt & Brendon Werner

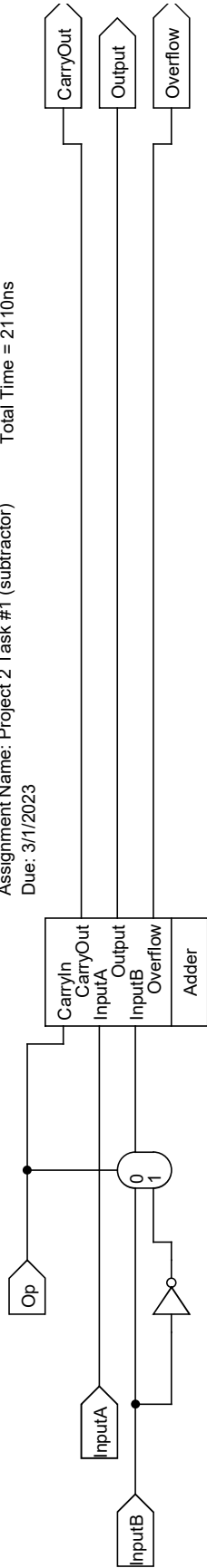
File Name: projec2.jls

Total # of Gates in Circuit: 164

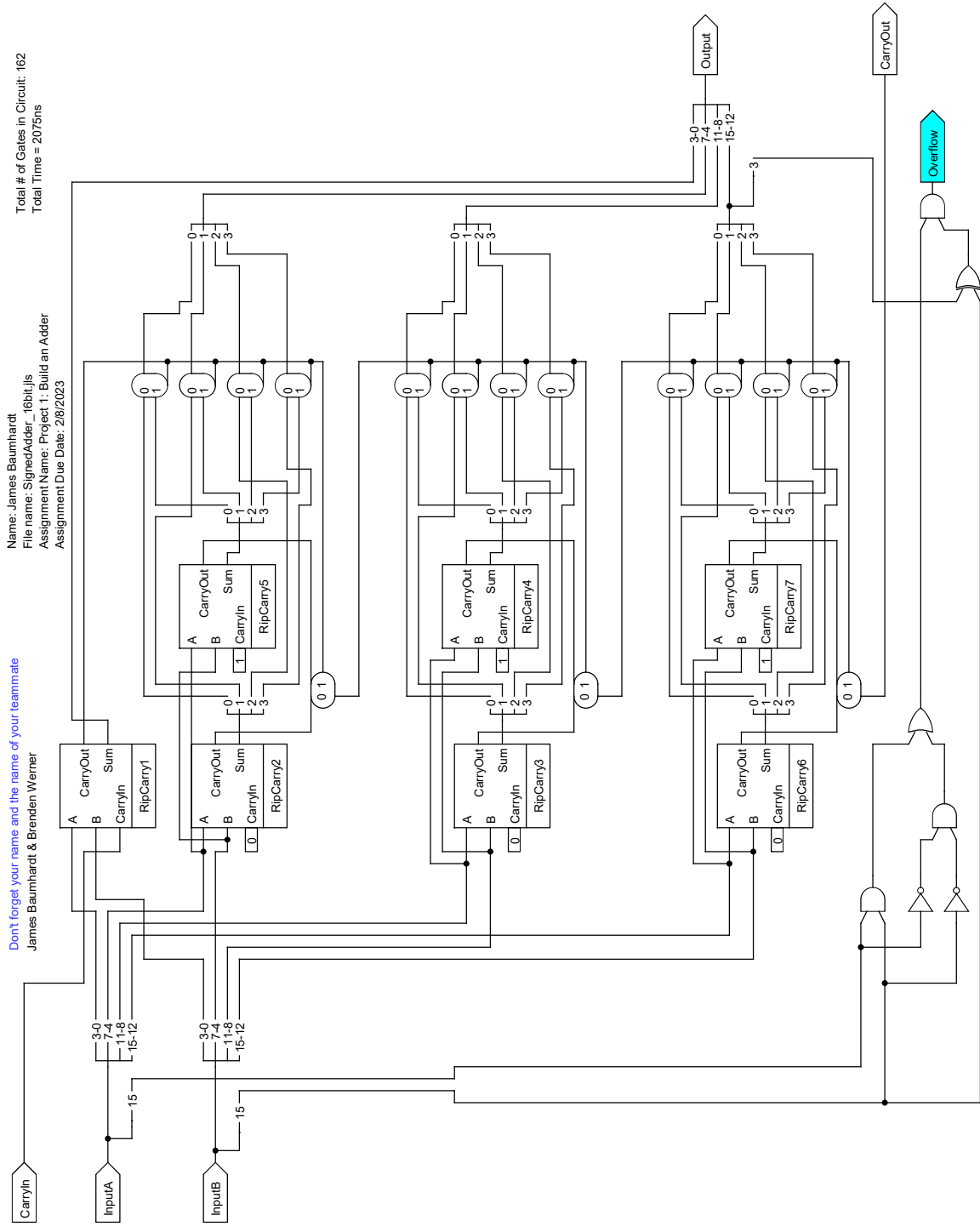
Assignment Name: Project 2 Task #1 (subtractor)

Total Time = 2110ns

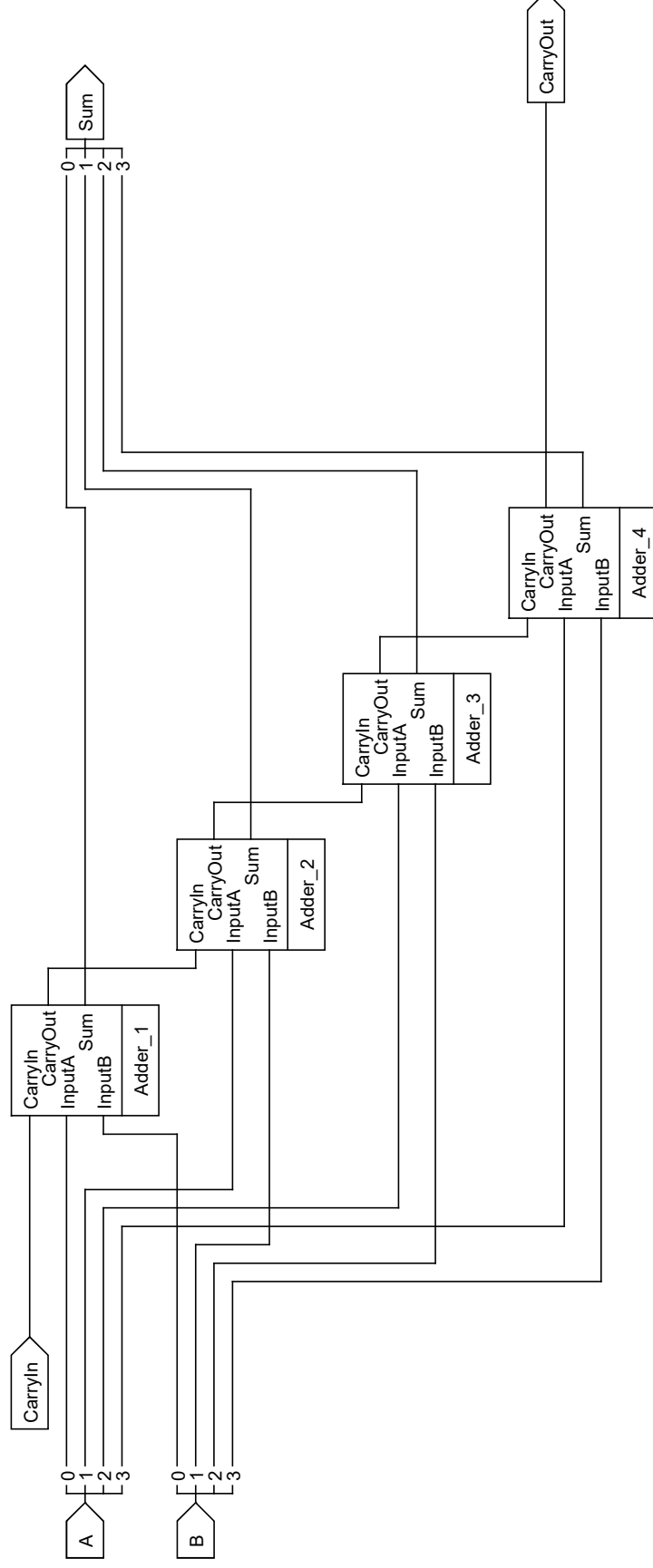
Due: 3/1/2023



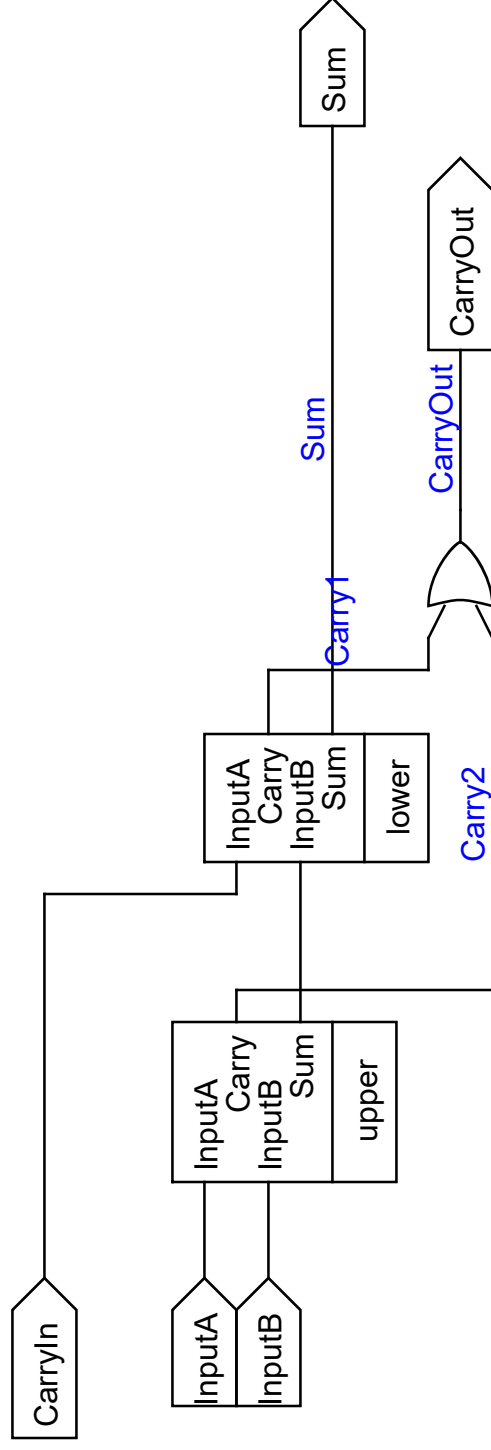
Adder in Adder_Subtractor in ALU_16bit_starter



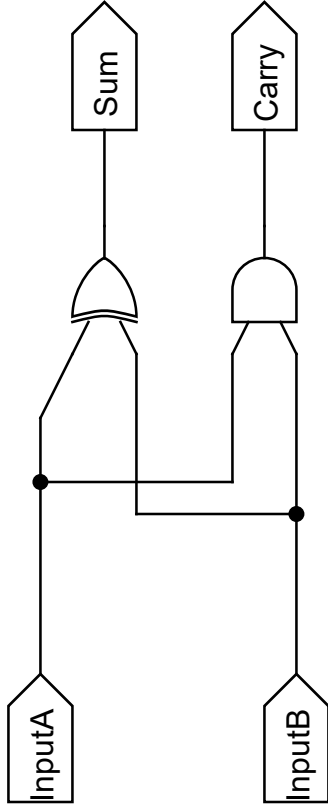
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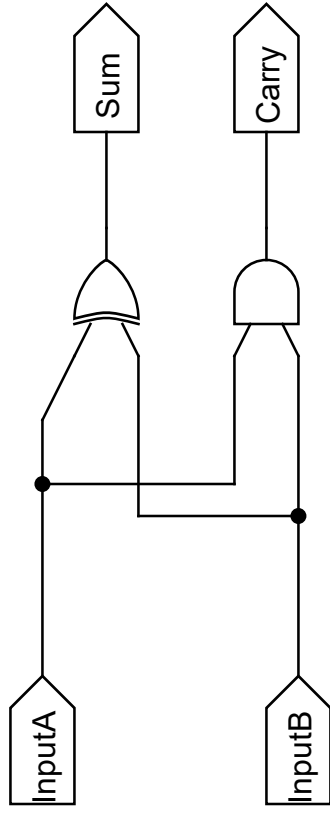
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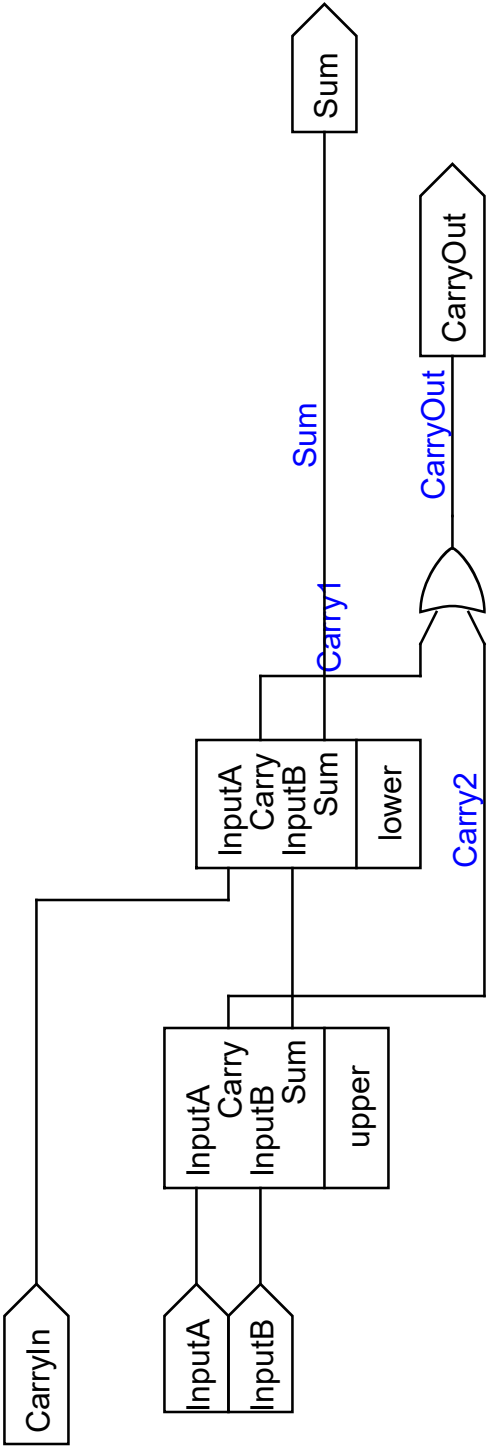
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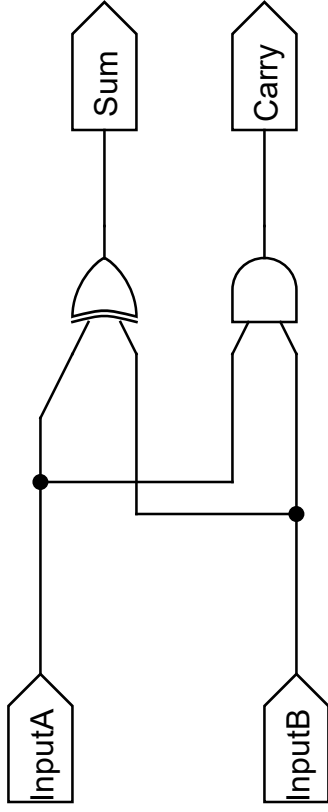
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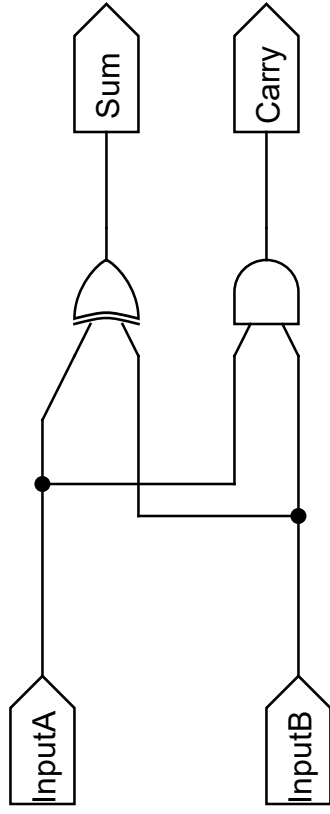
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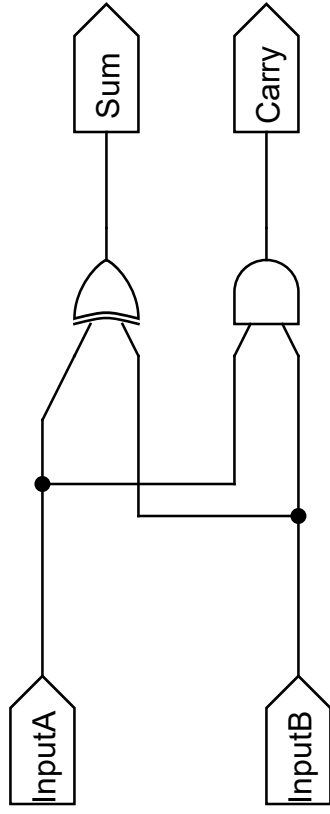
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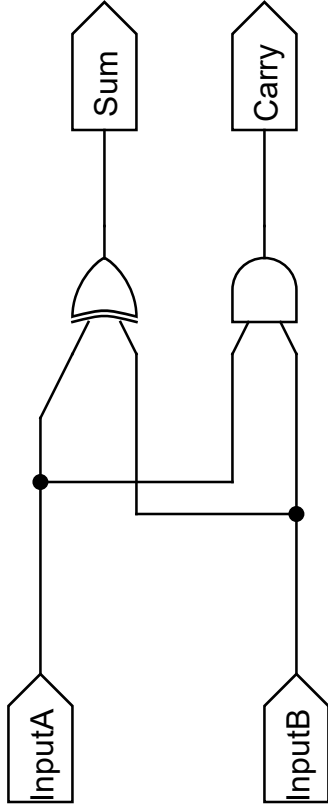
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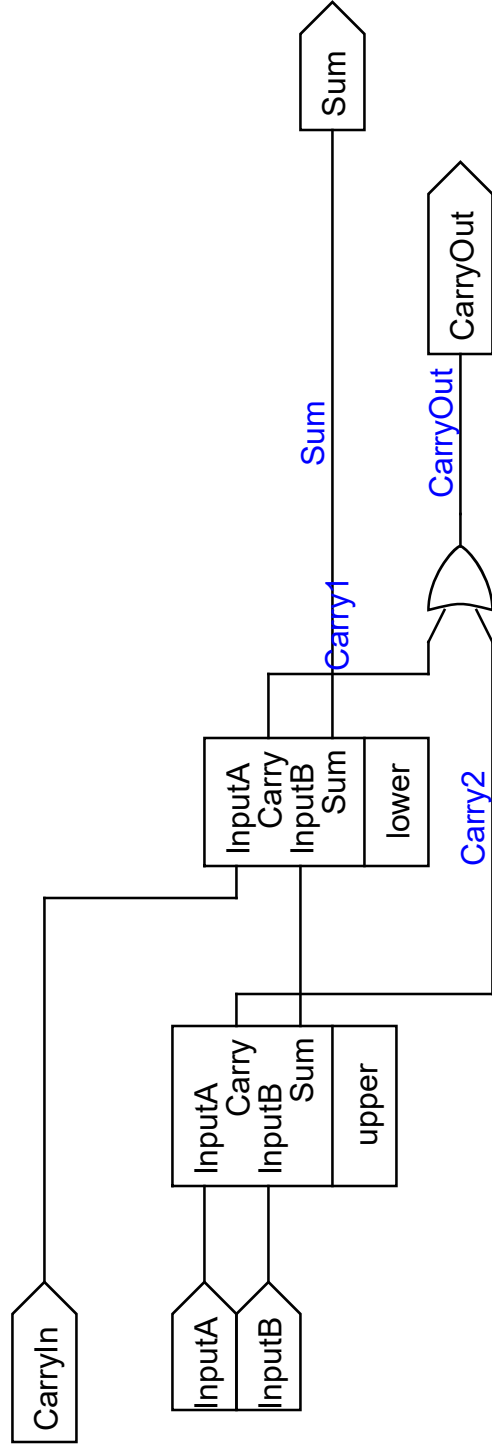
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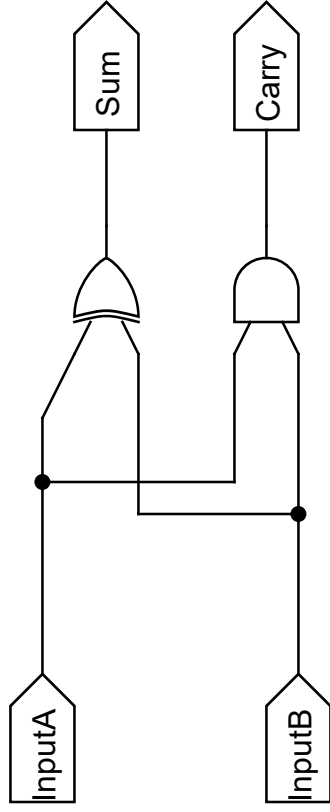
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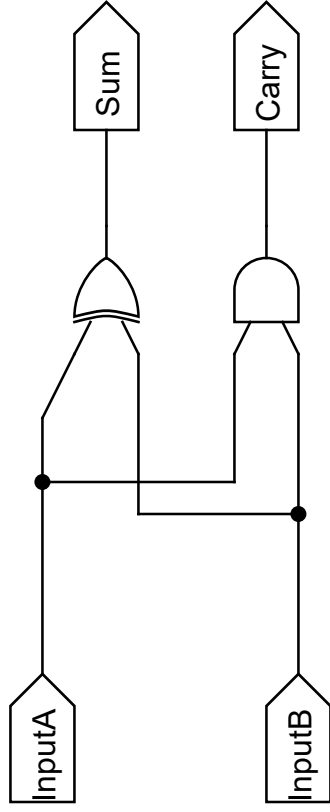
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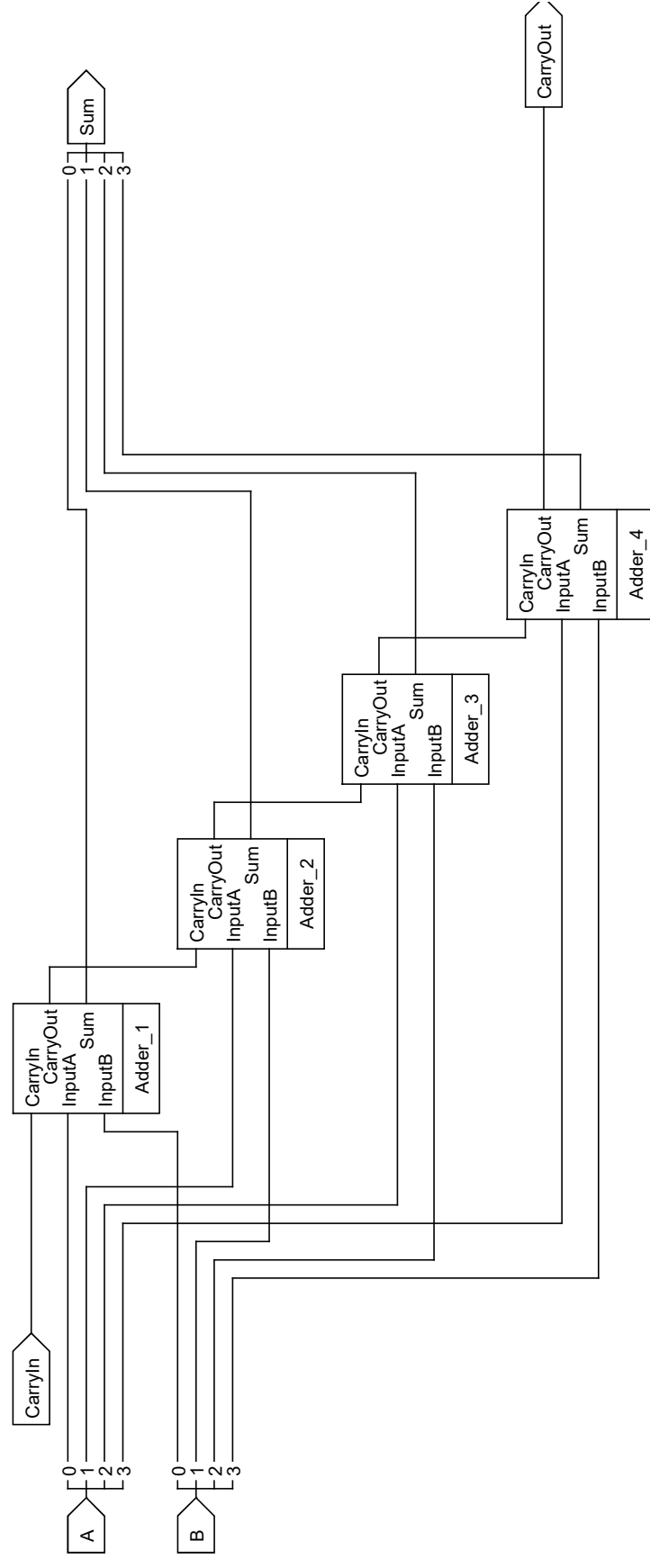
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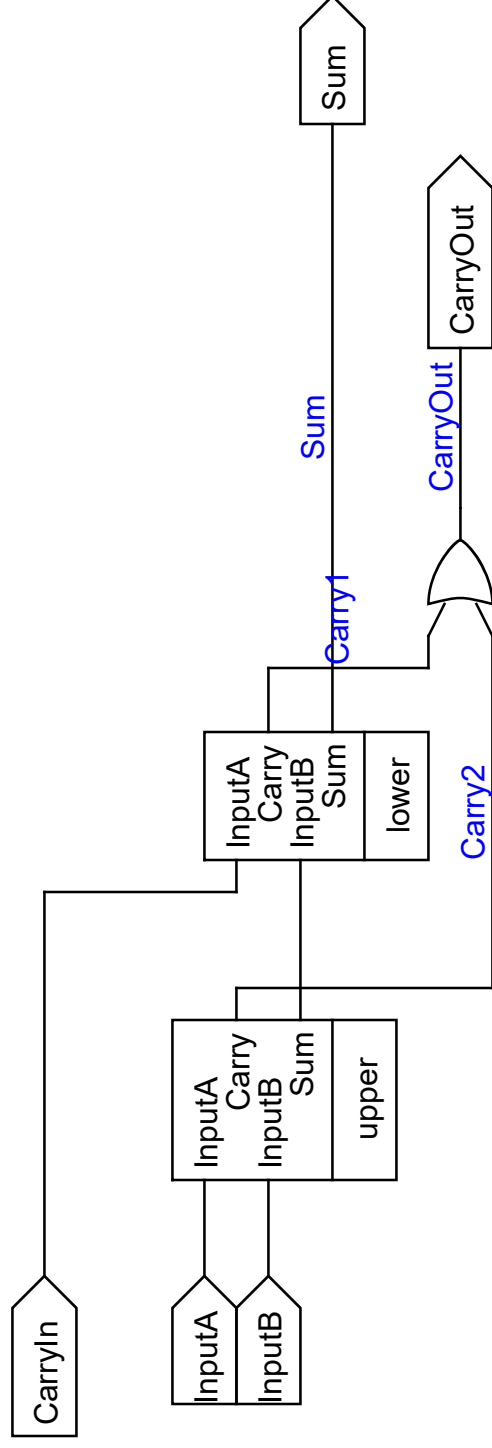
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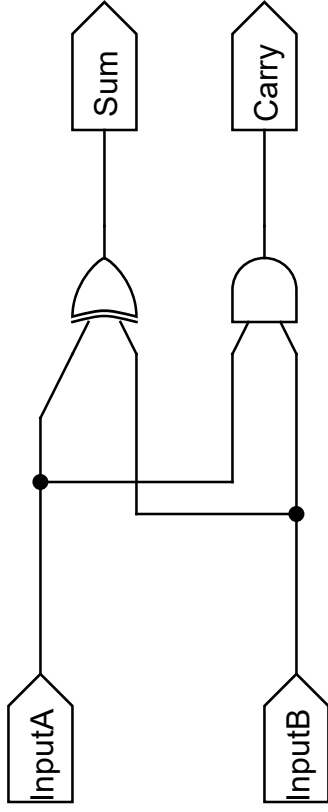
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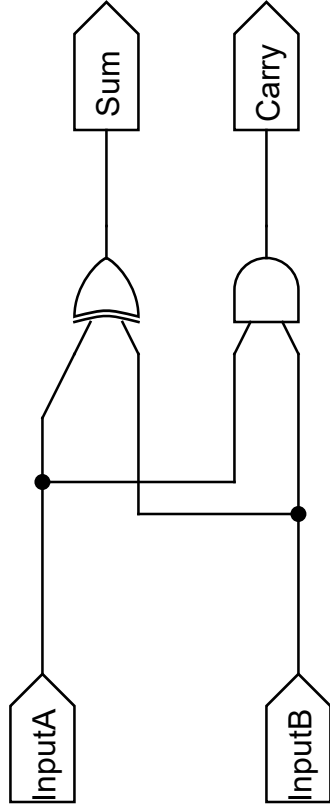
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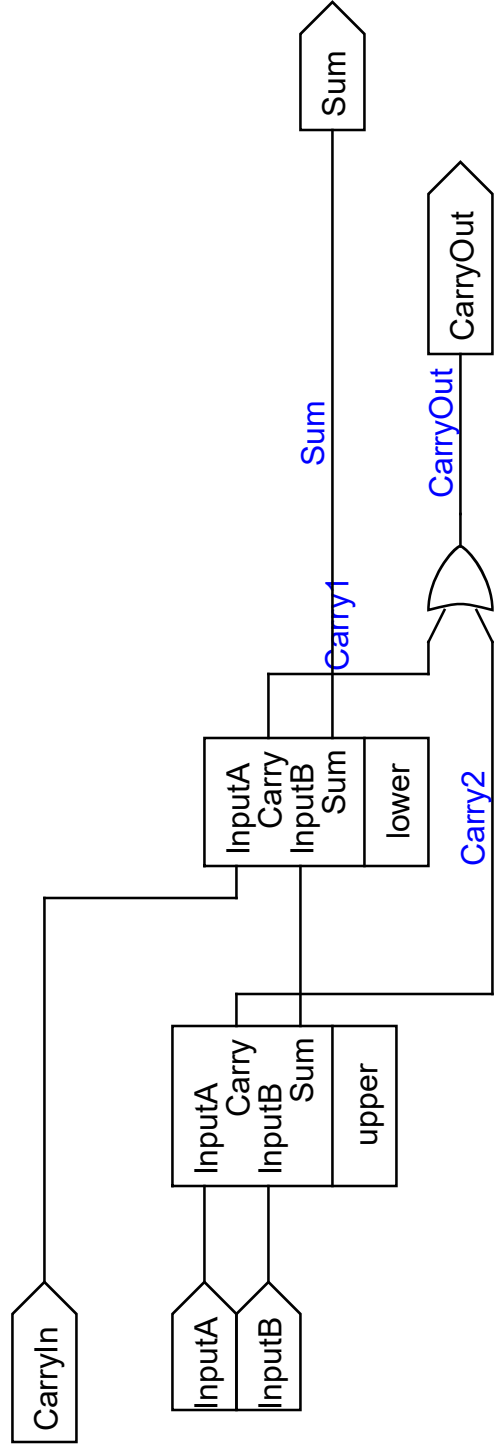
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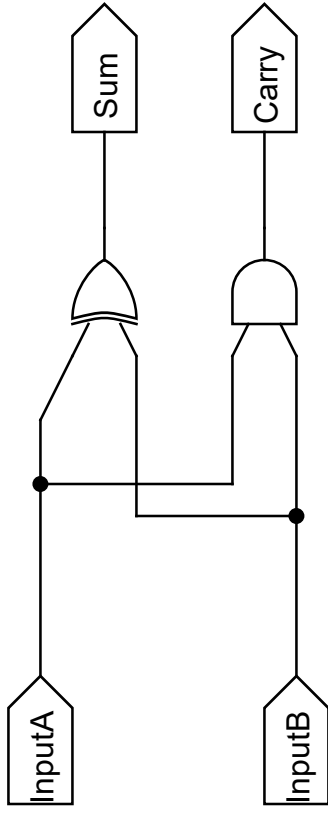
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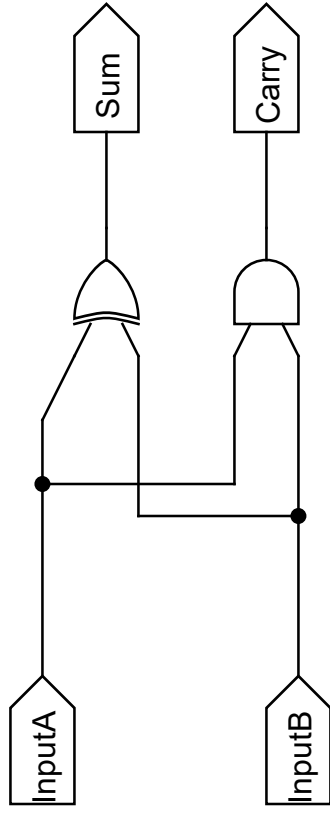
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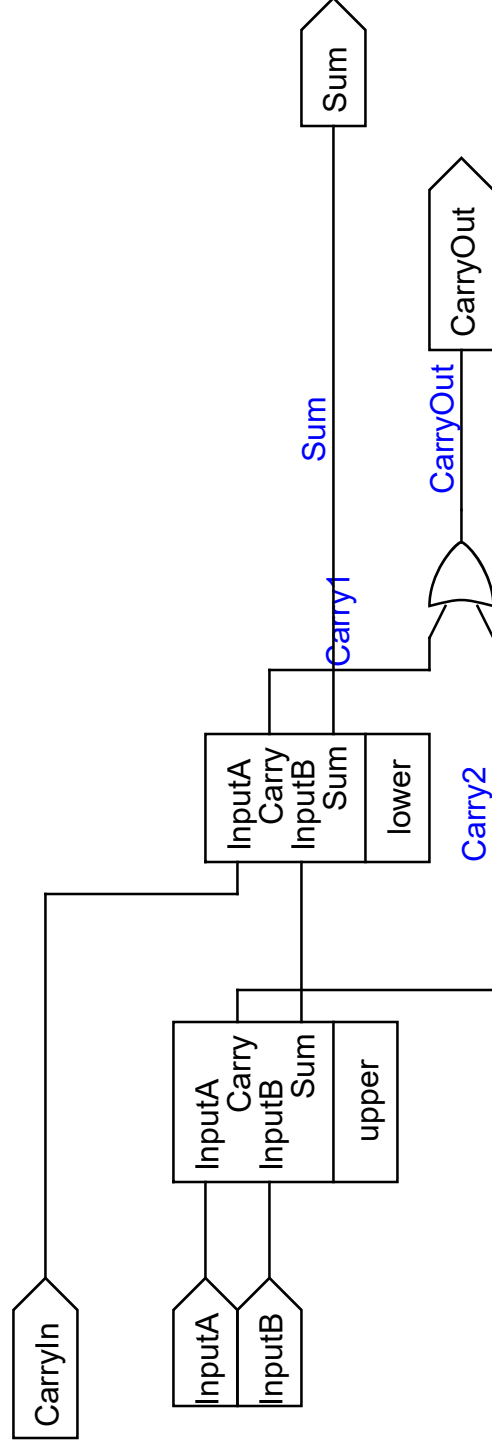
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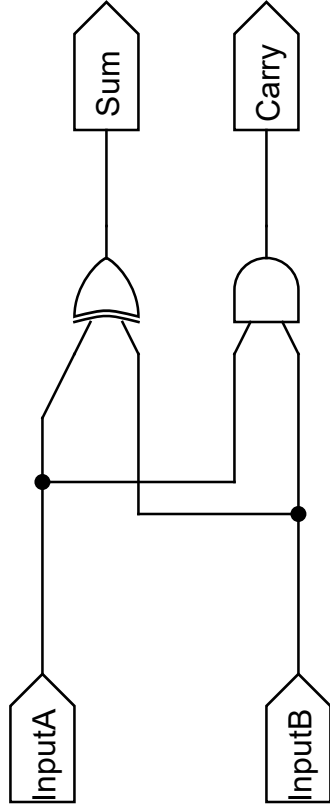
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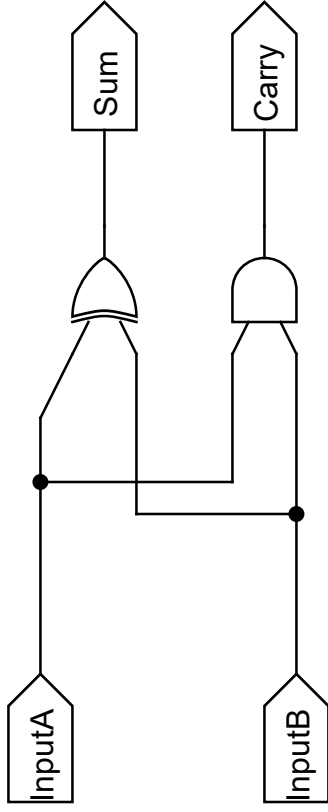
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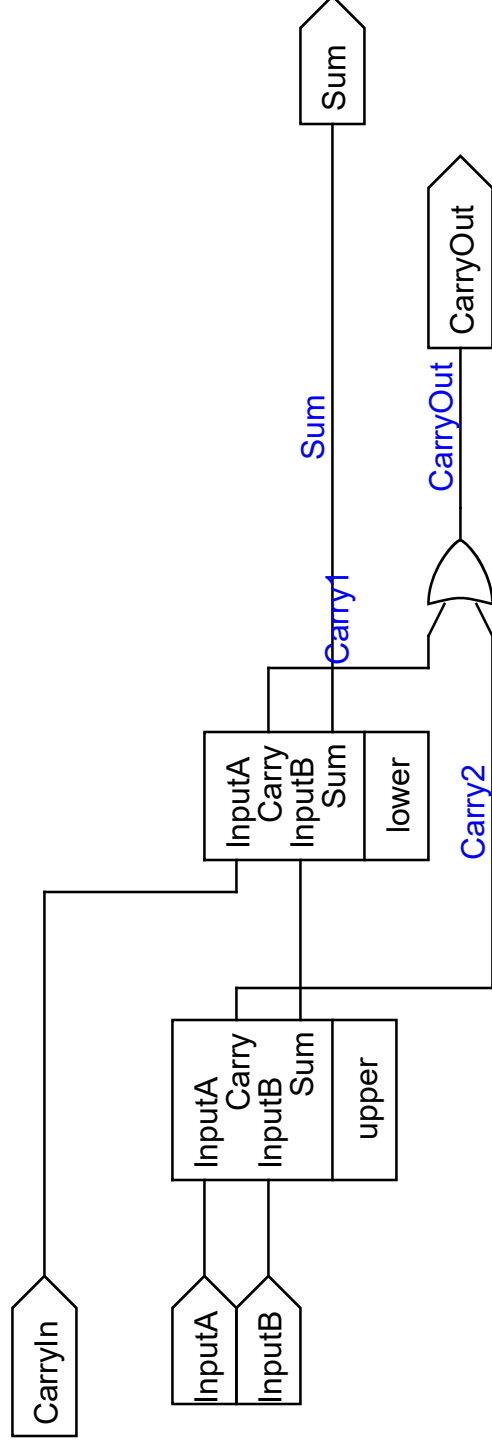
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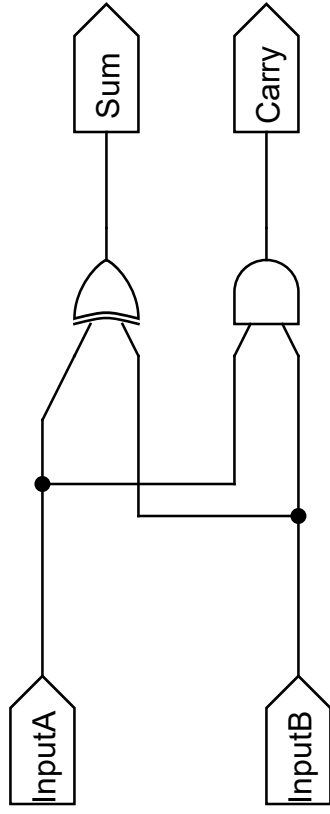
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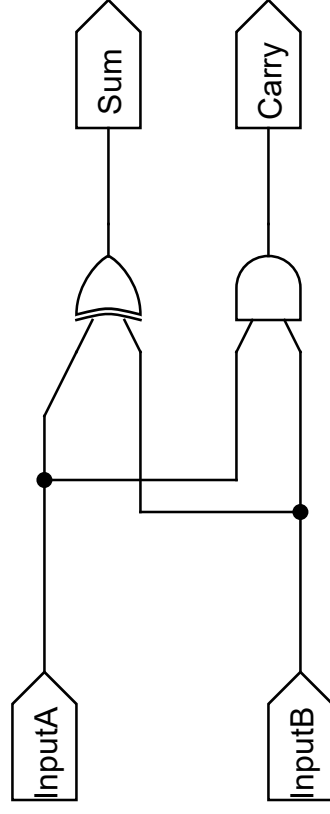
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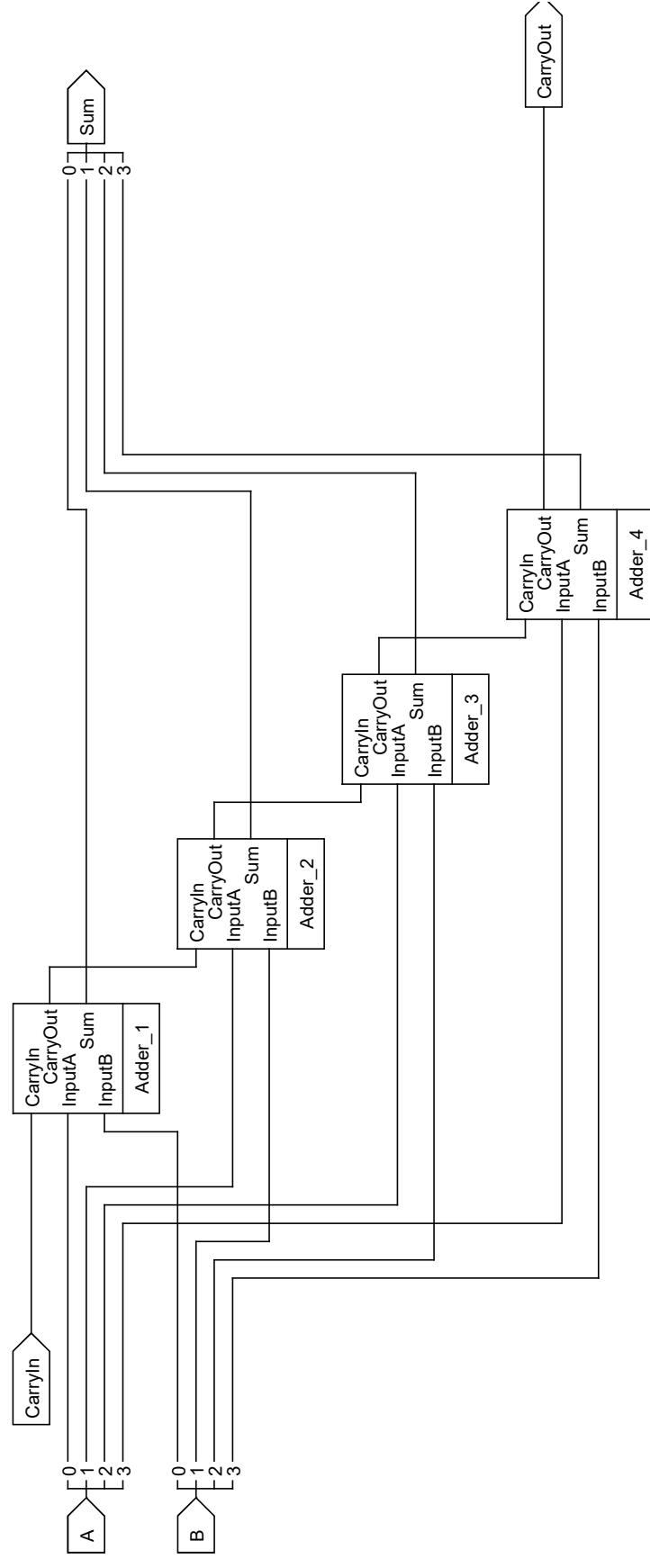
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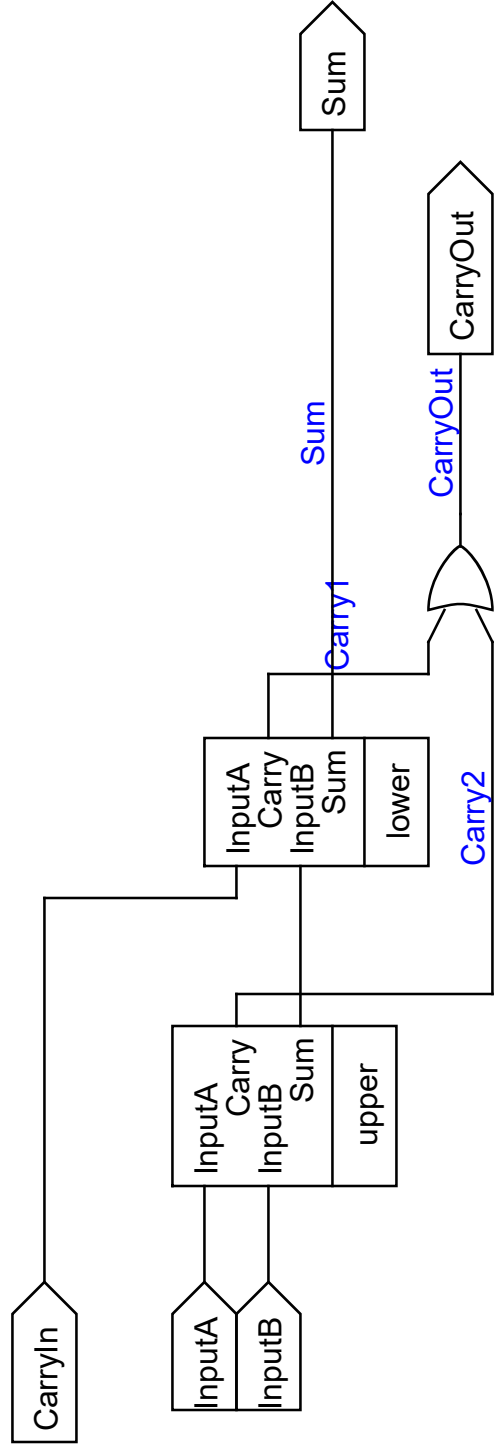
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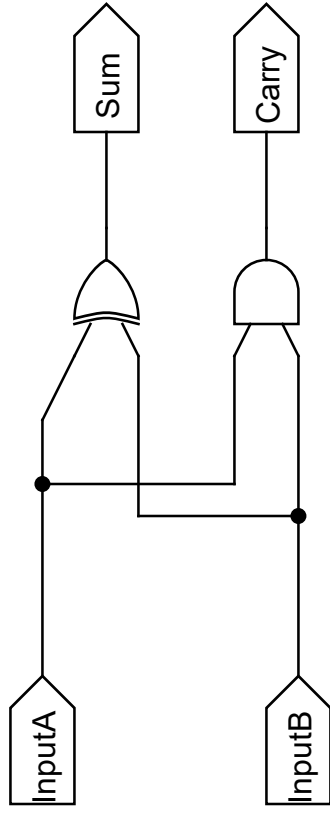
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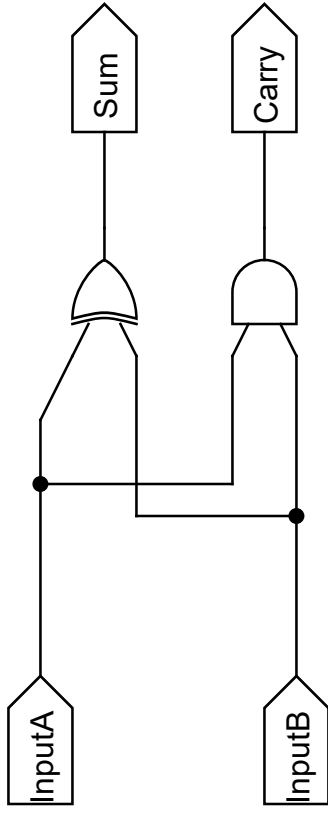
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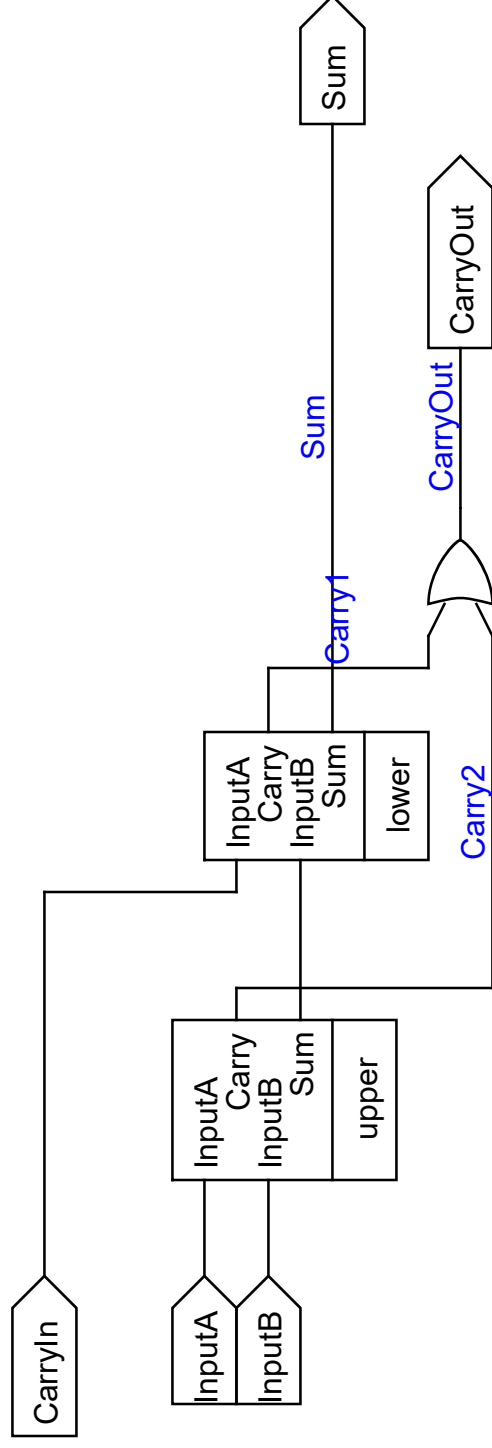
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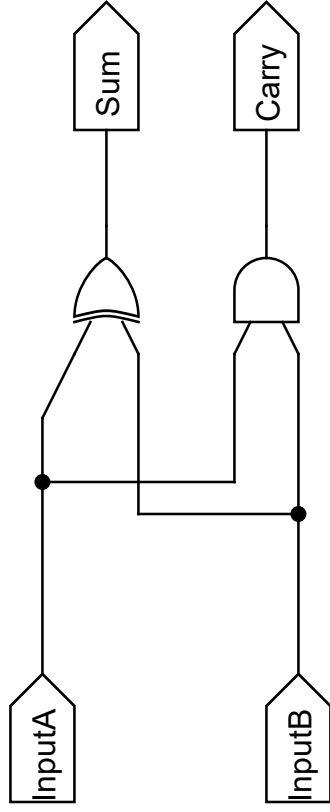
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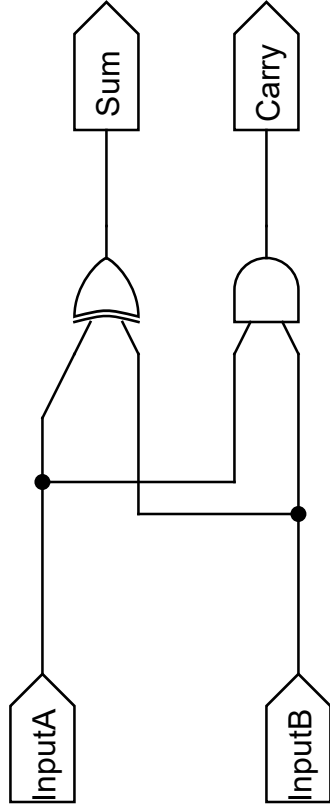
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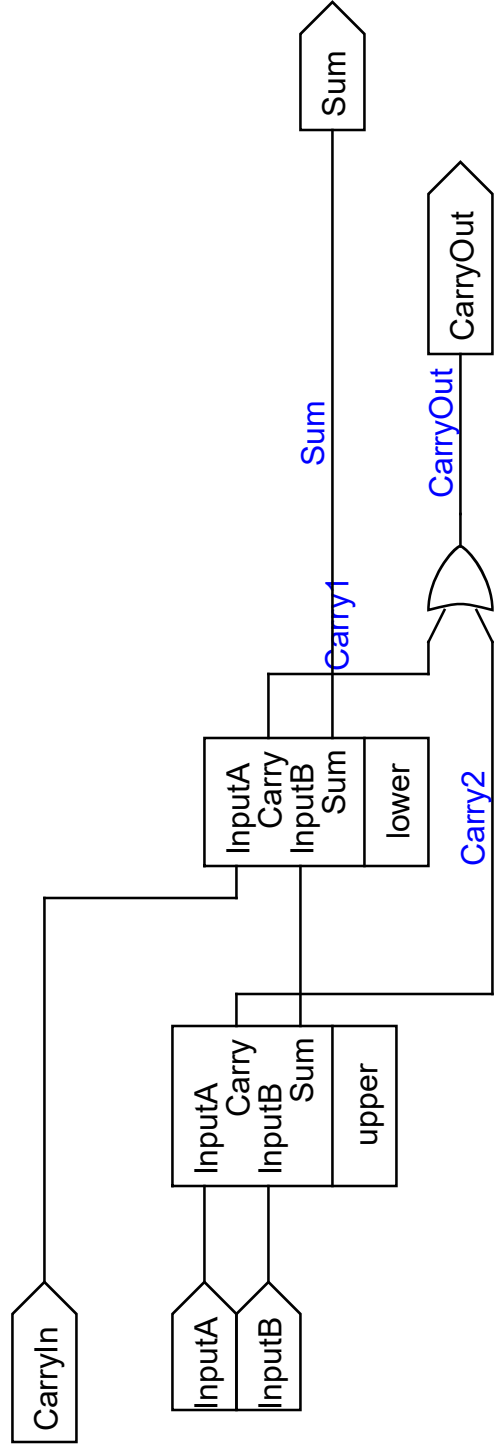
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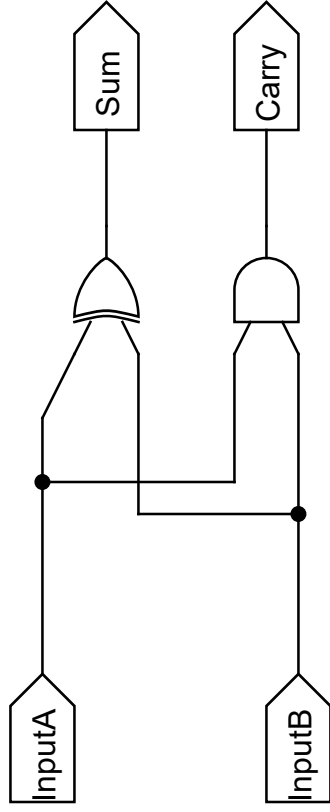
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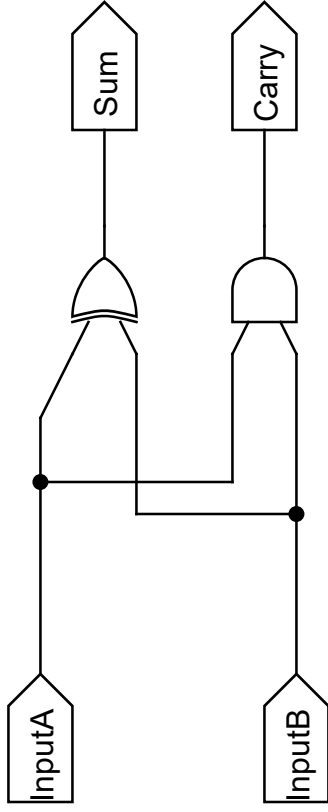
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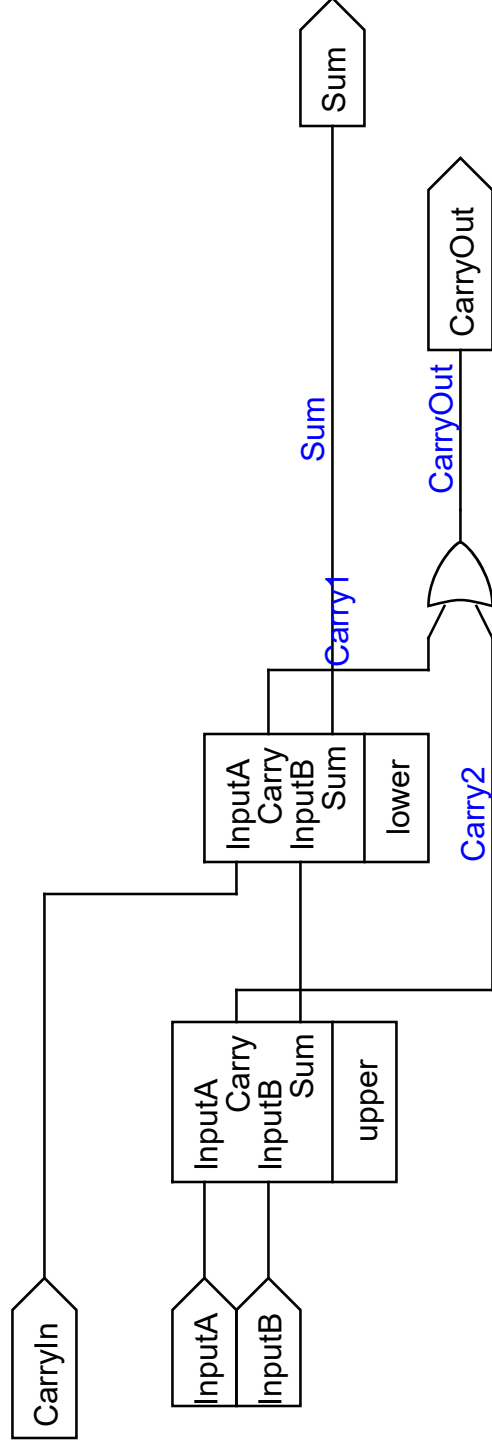
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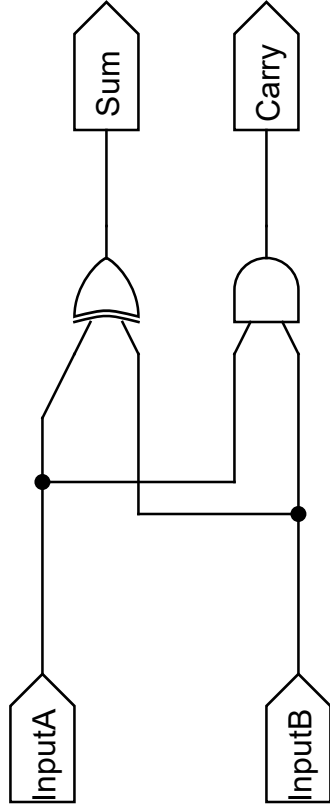
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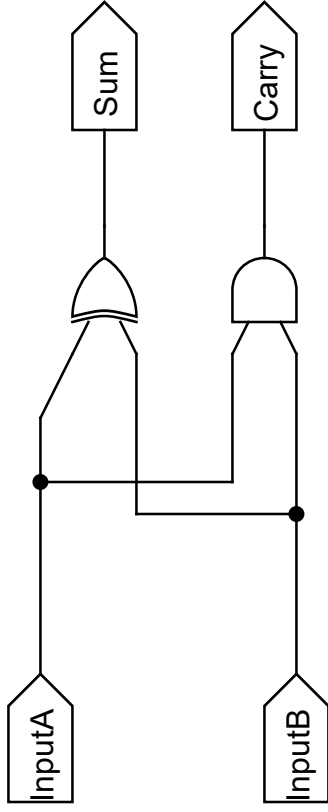
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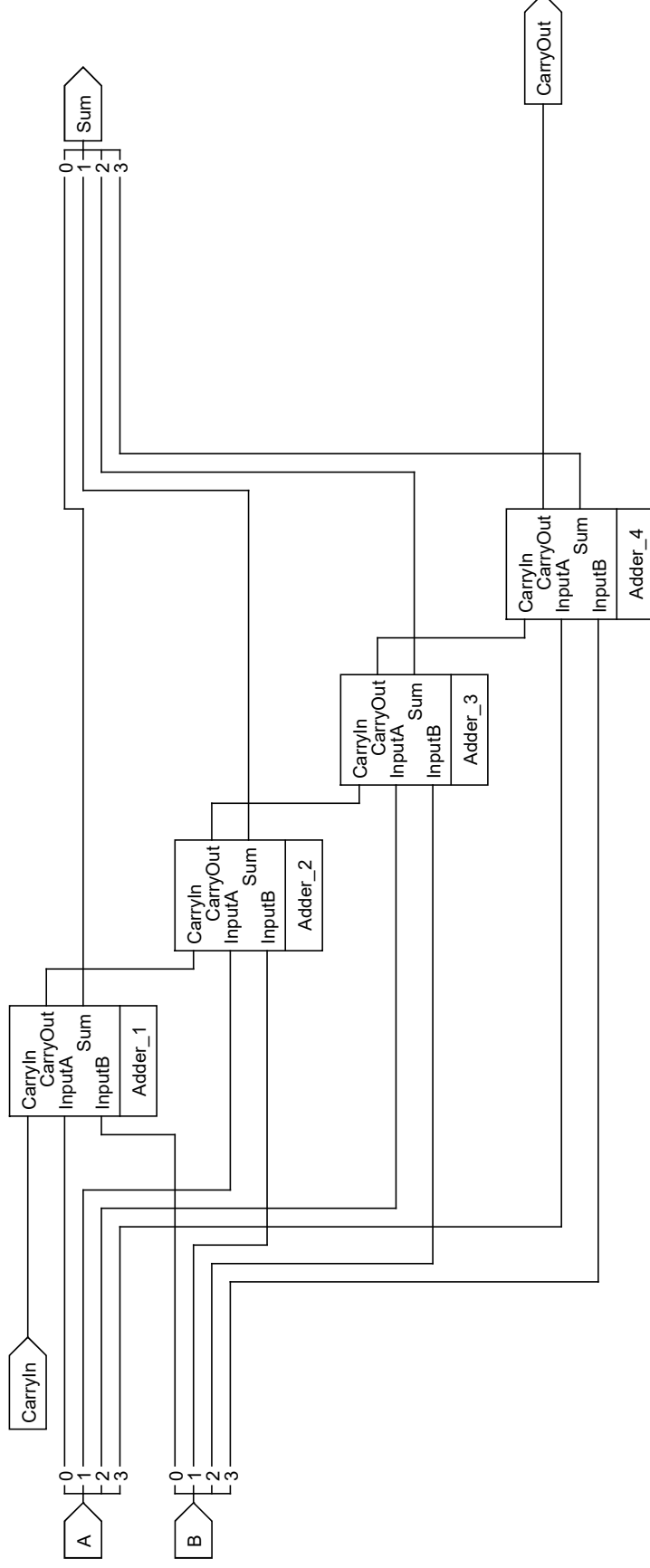
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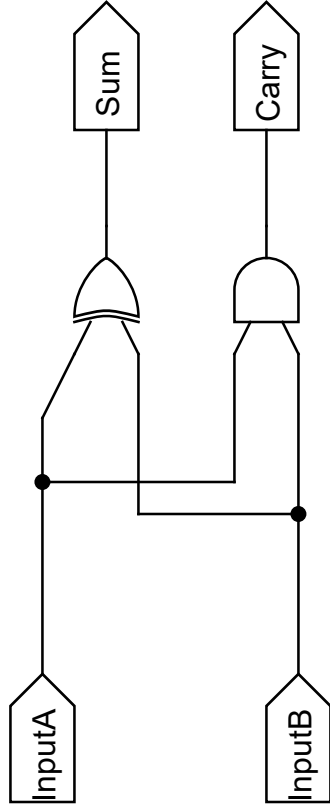
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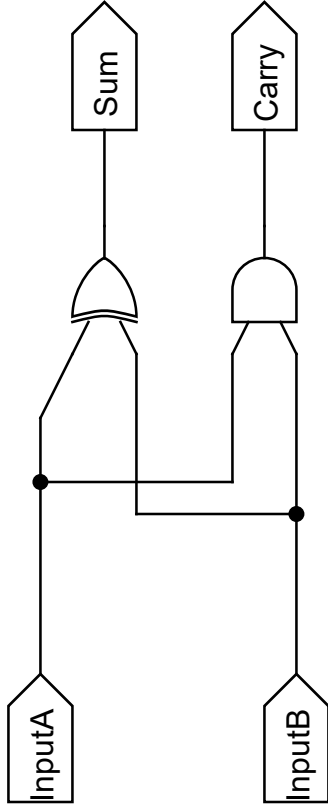
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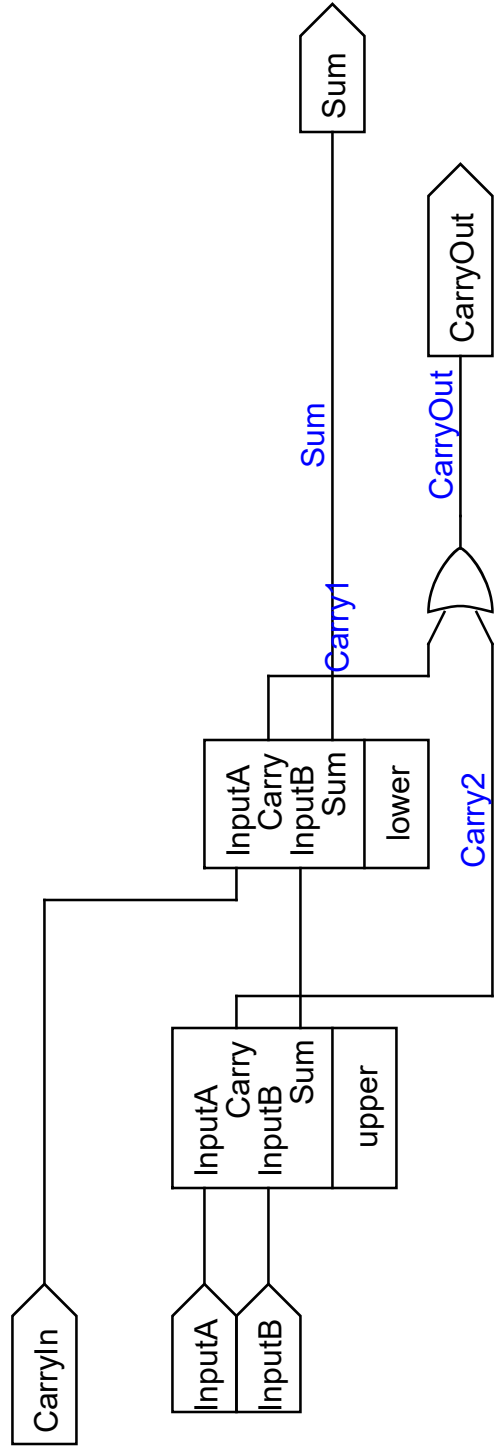
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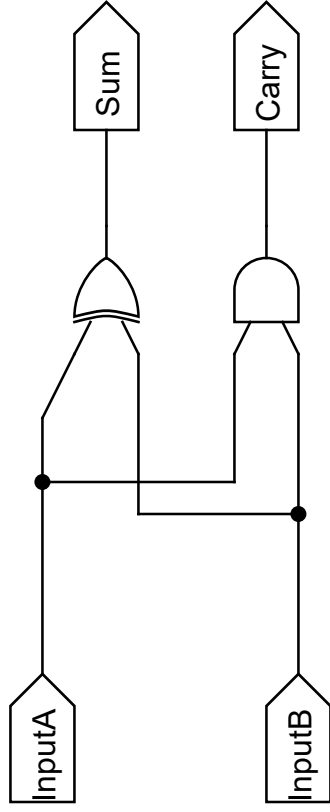
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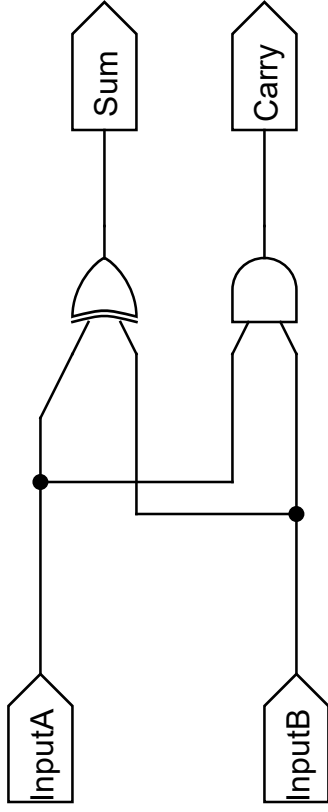
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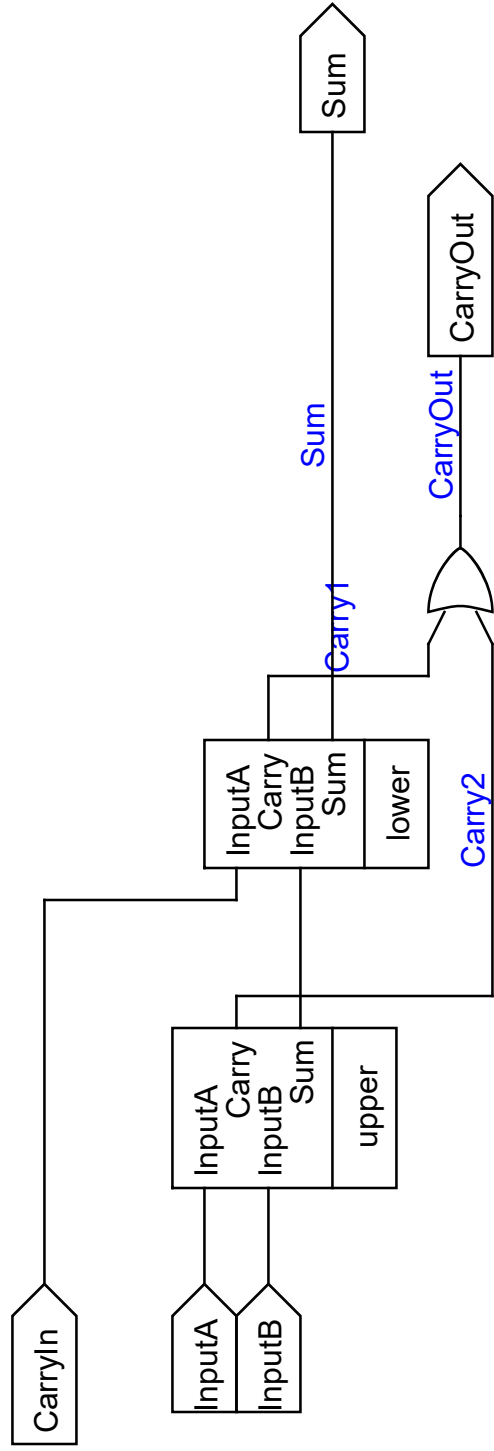
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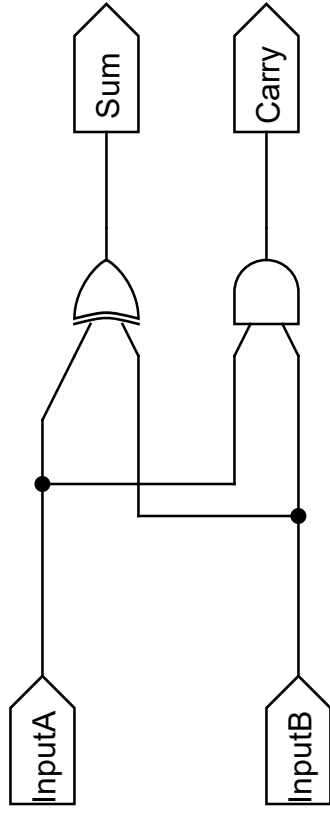
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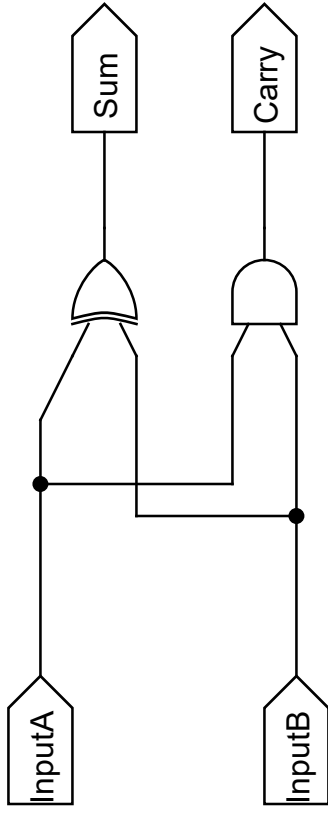
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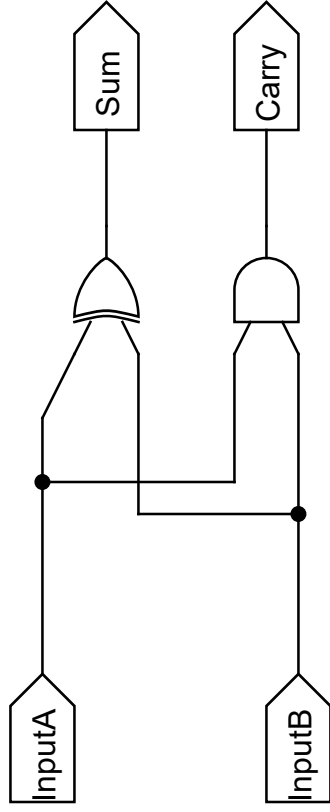
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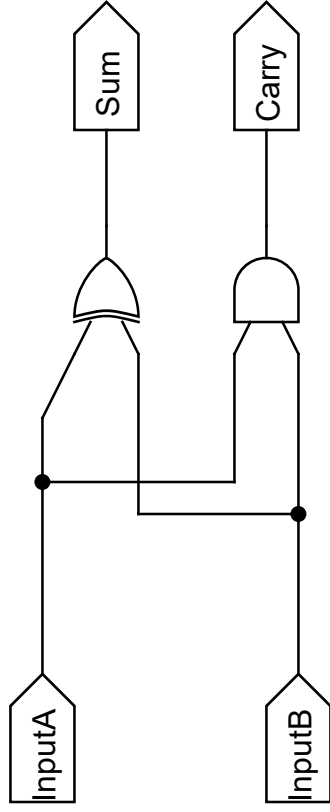
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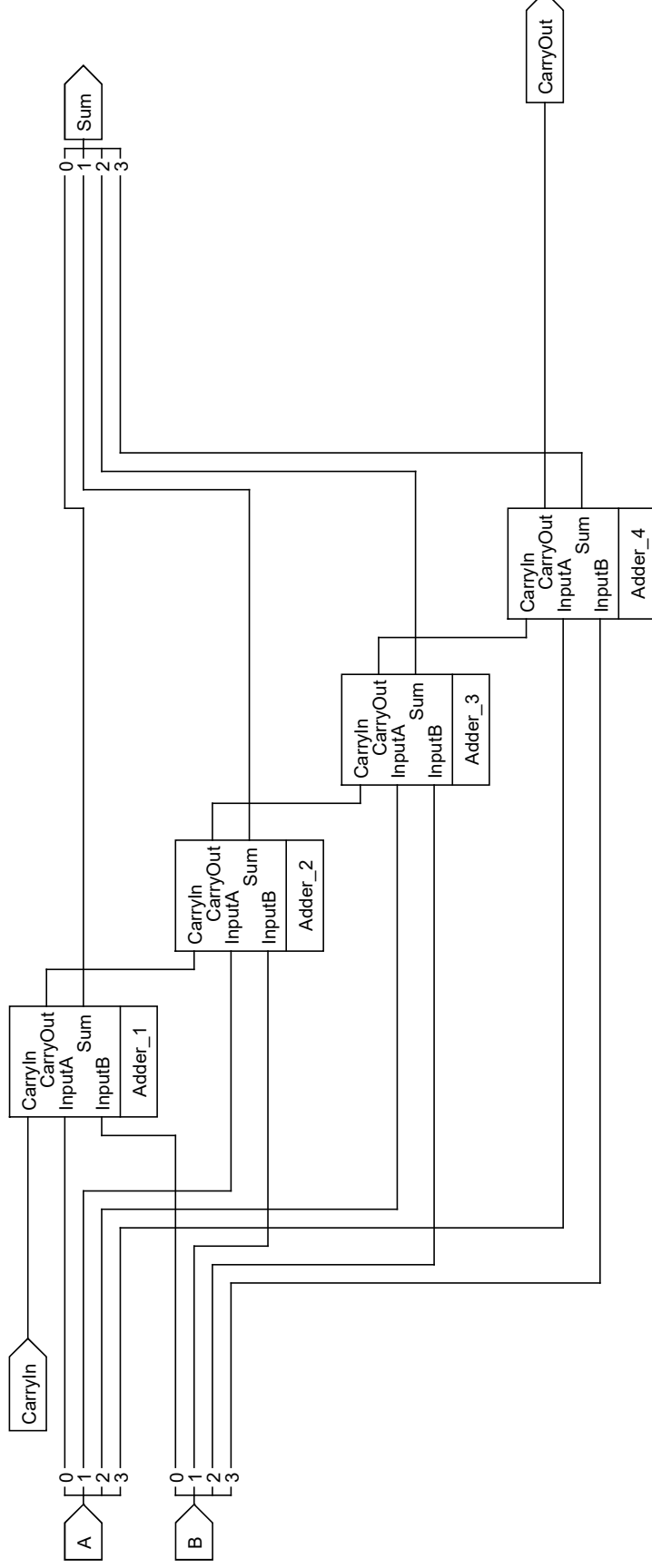
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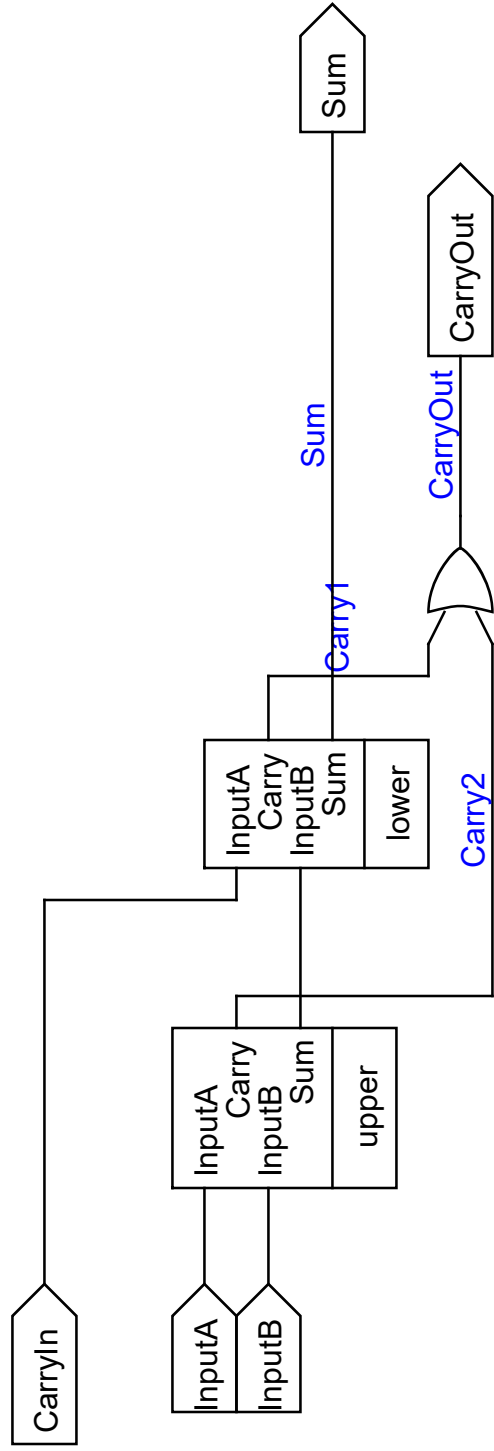
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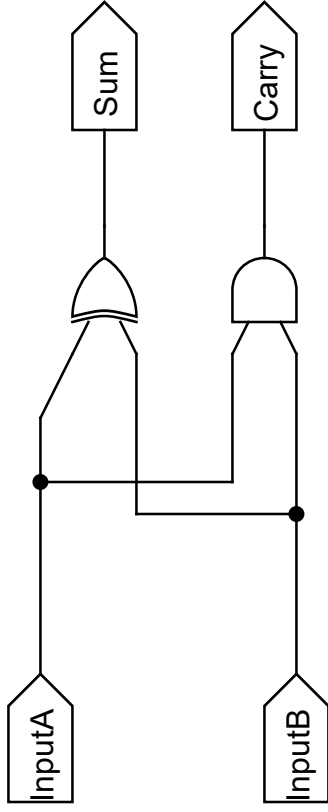
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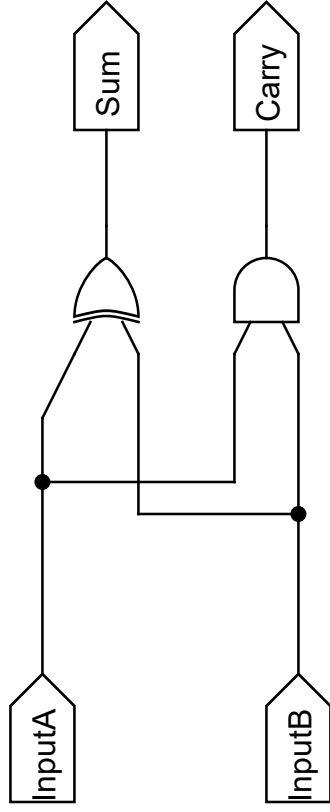
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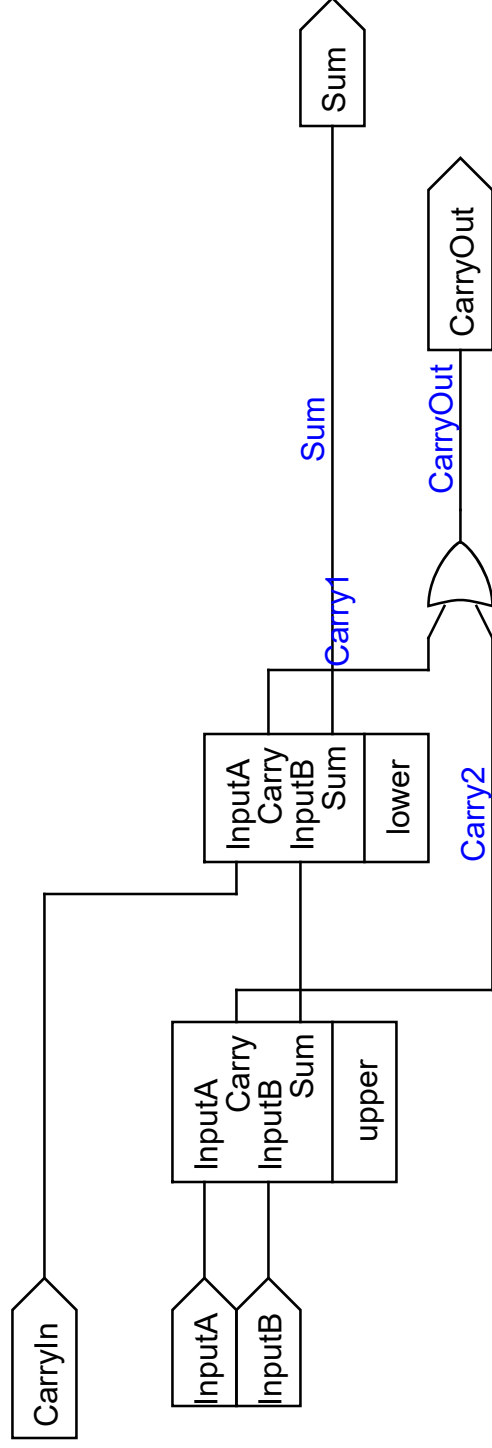
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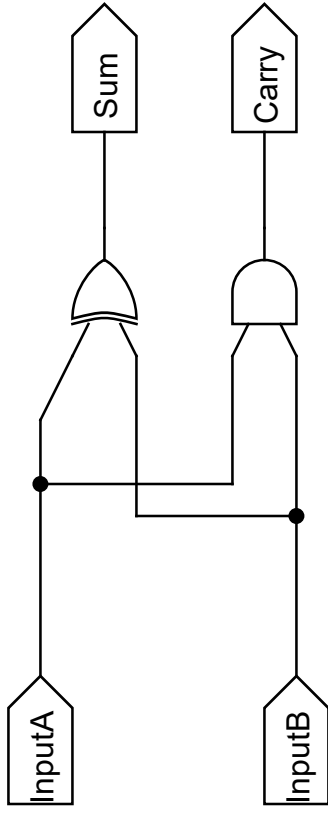
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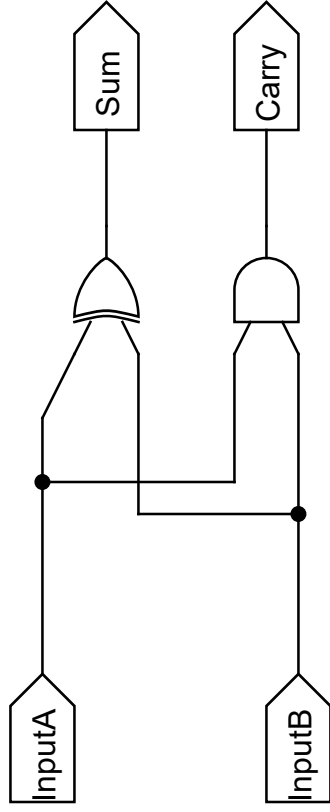
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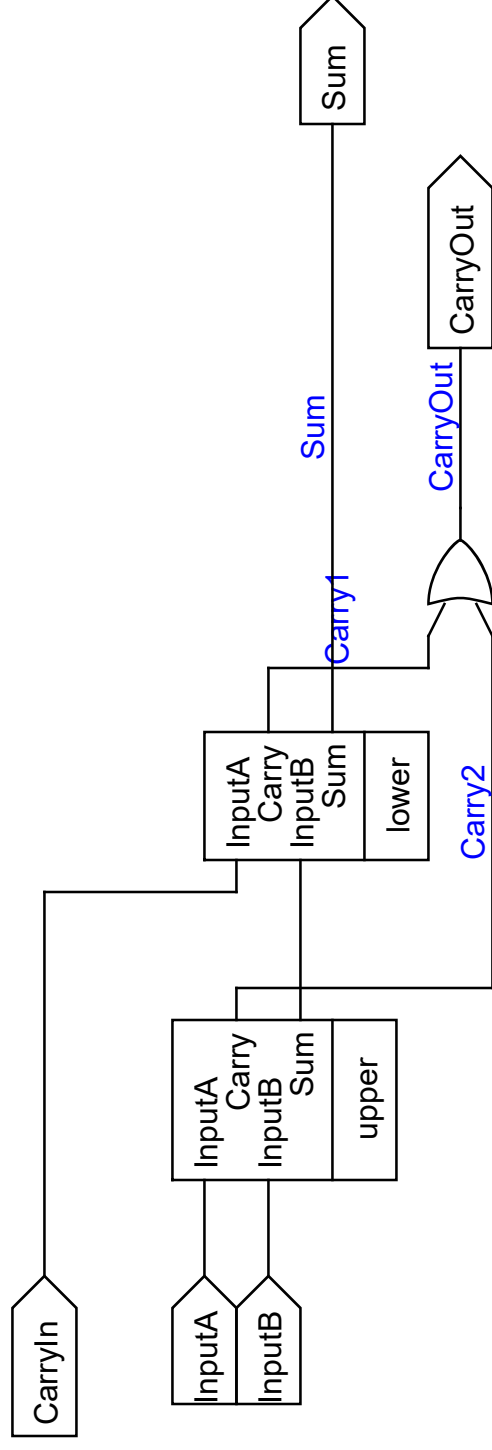
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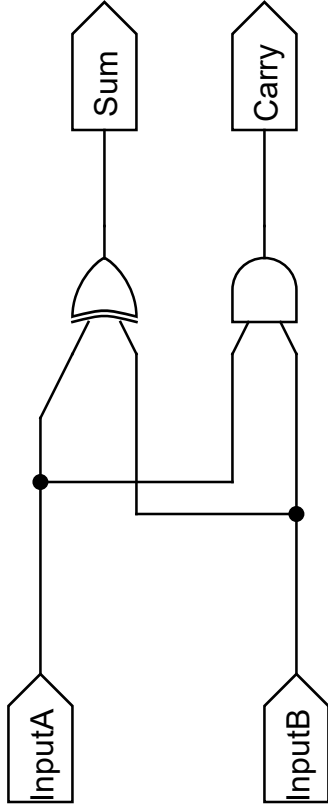
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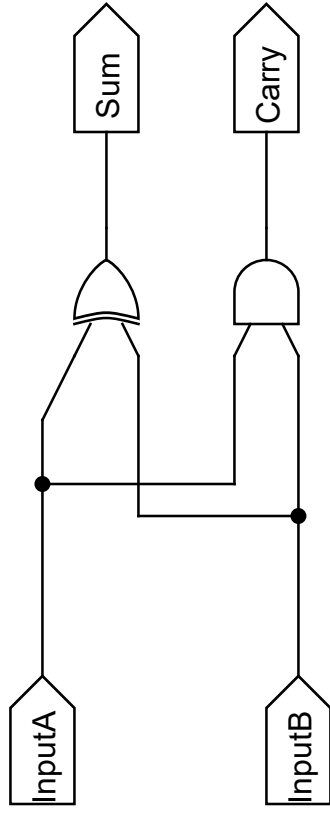
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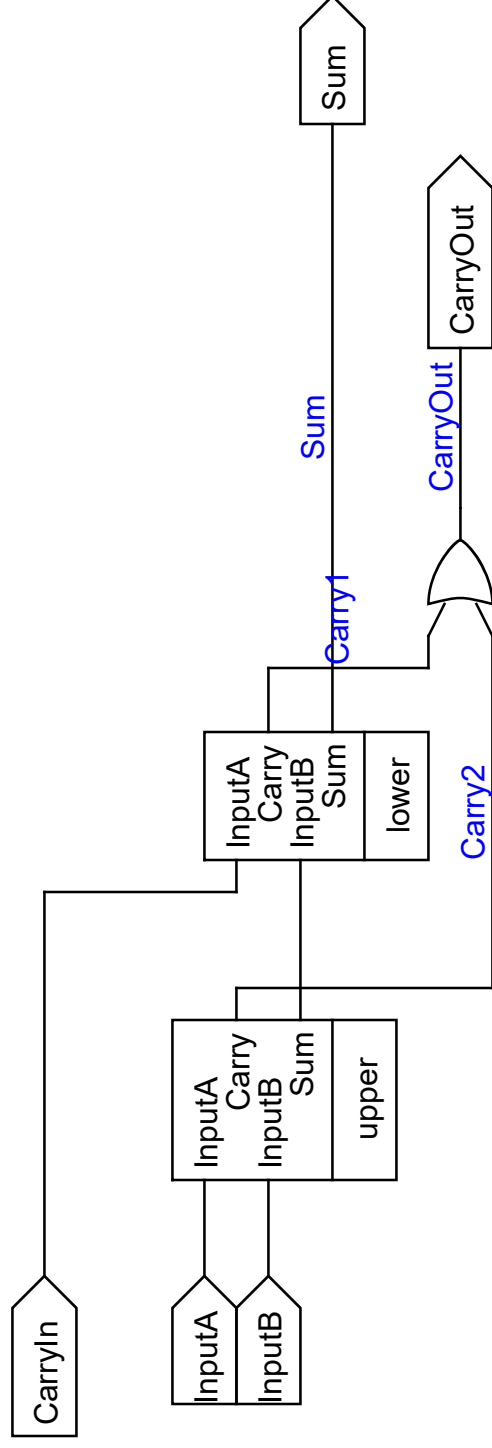
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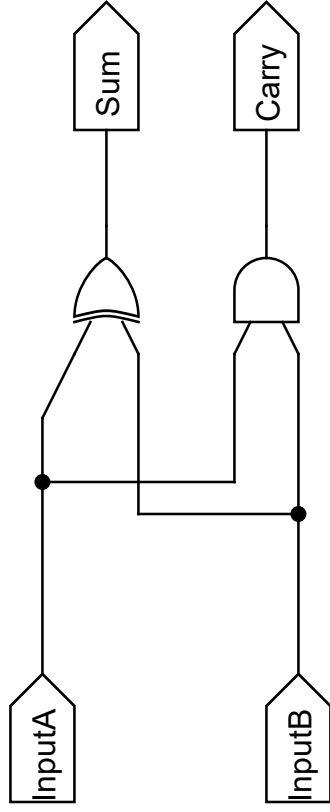
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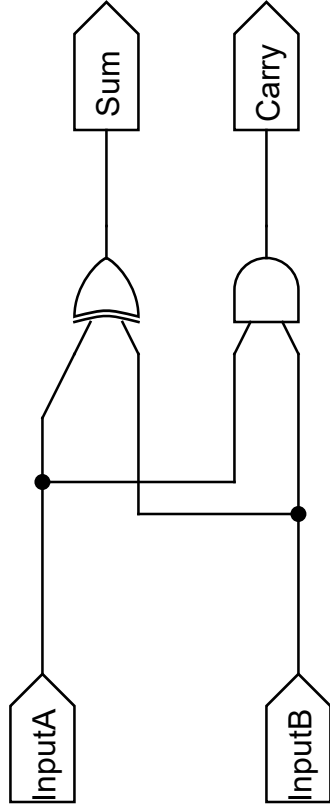
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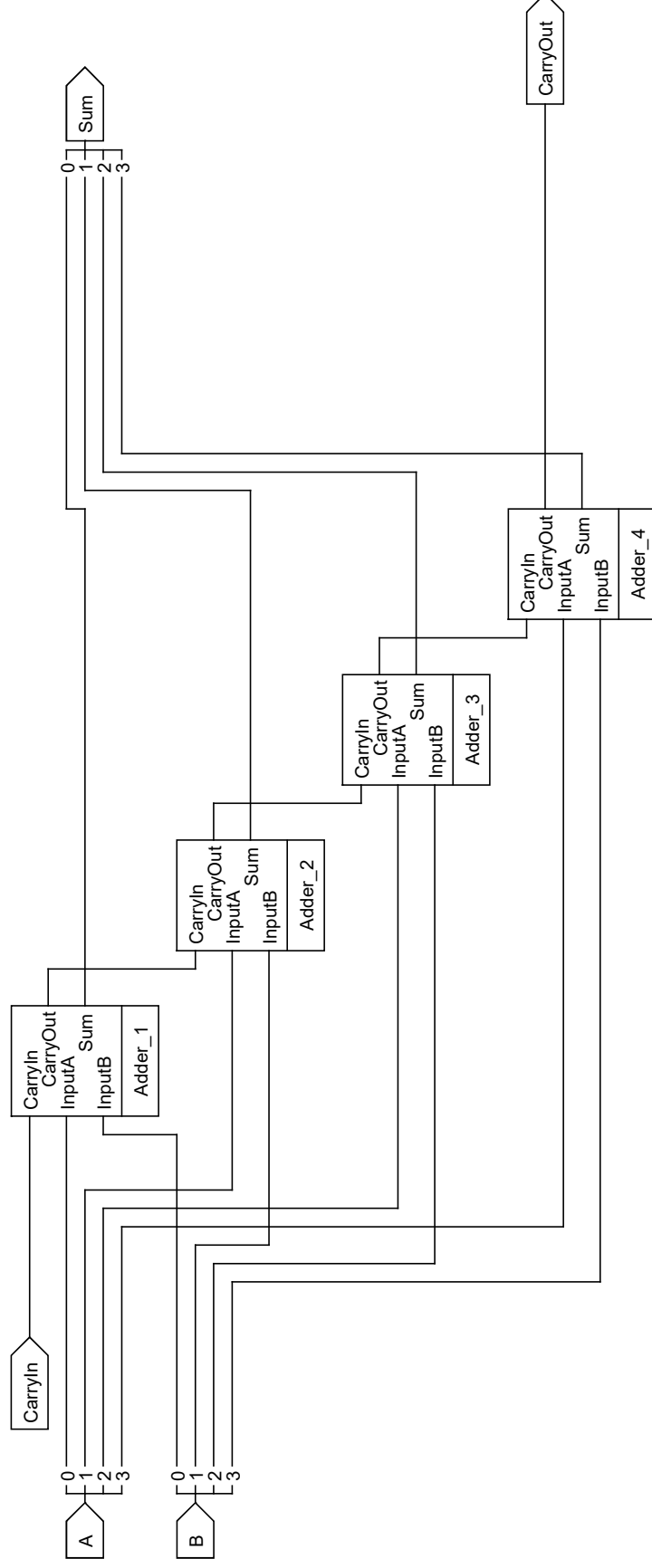
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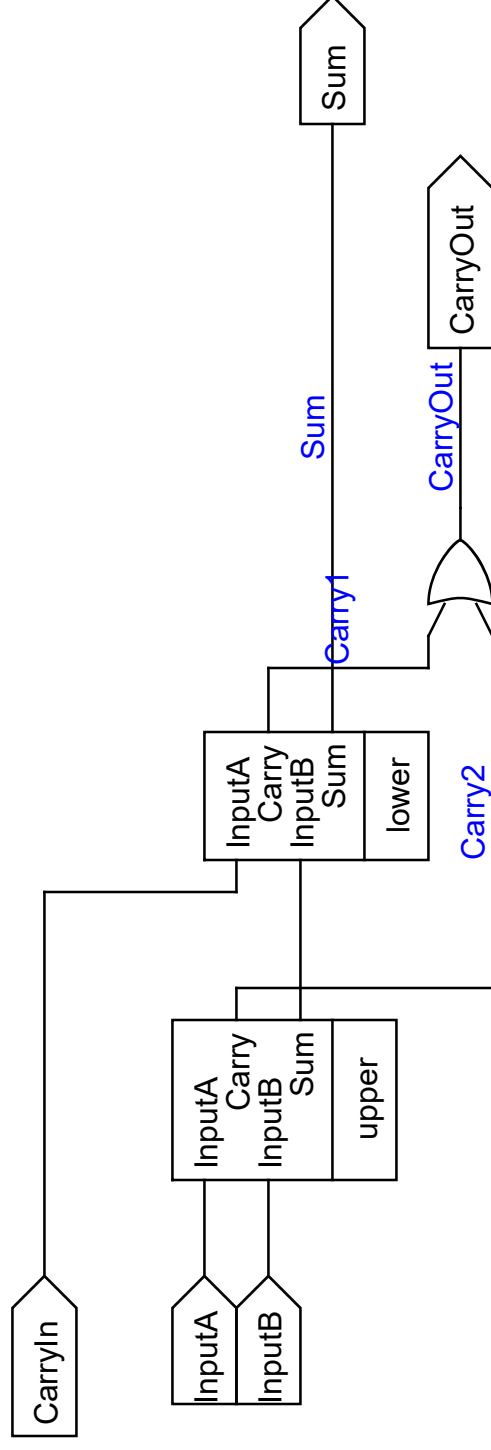
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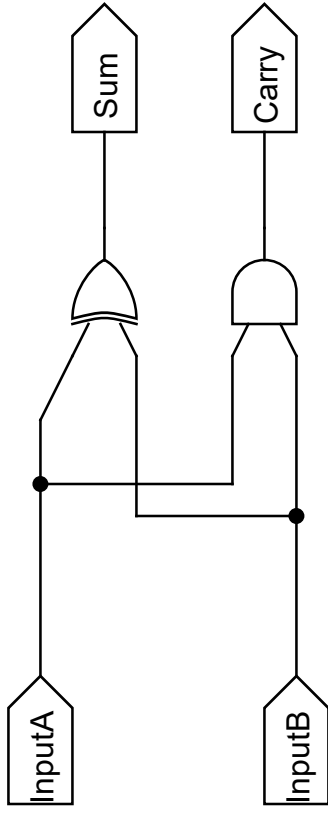
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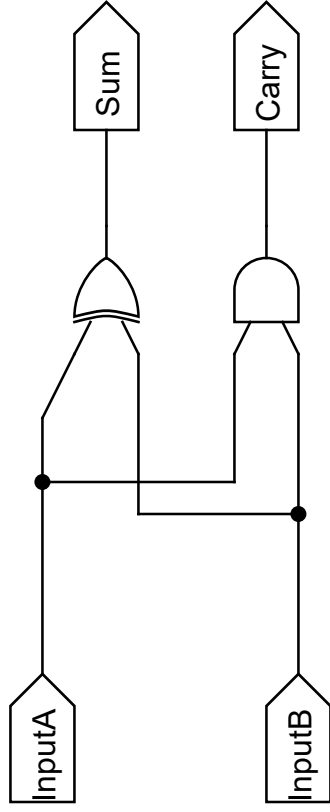
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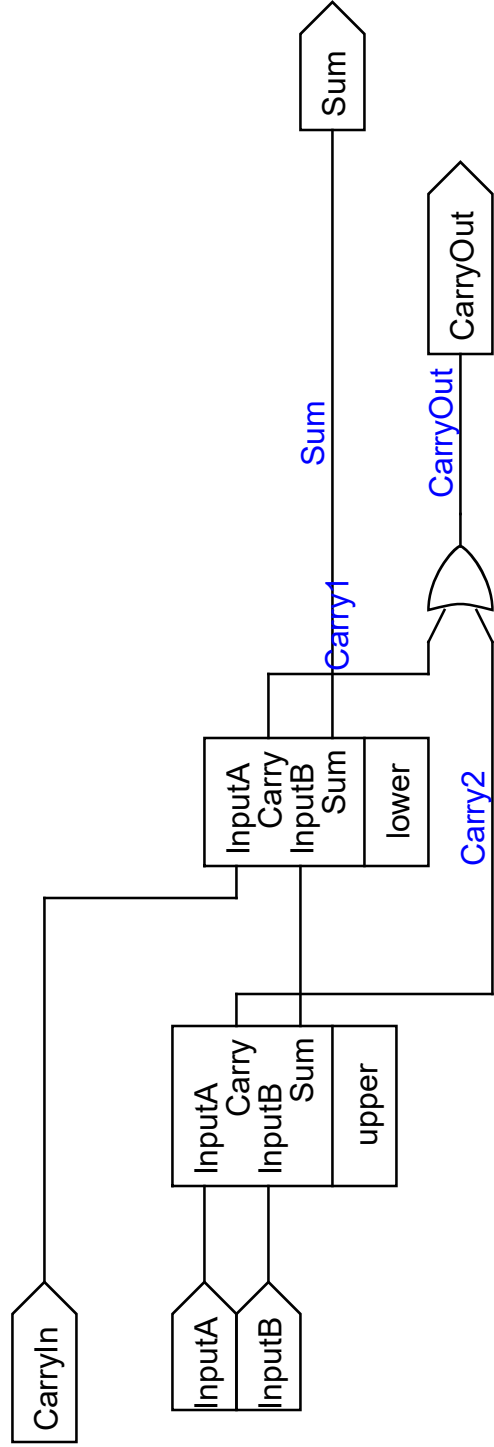
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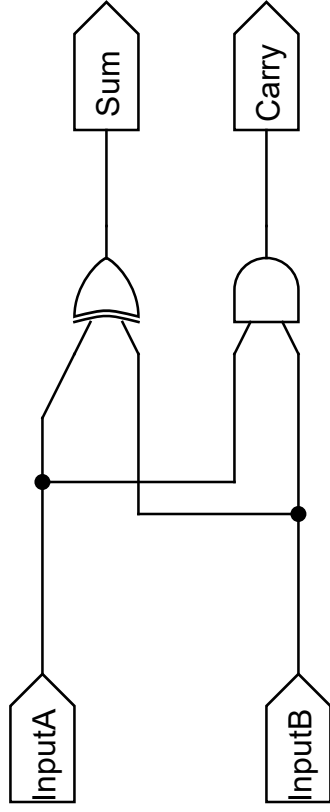
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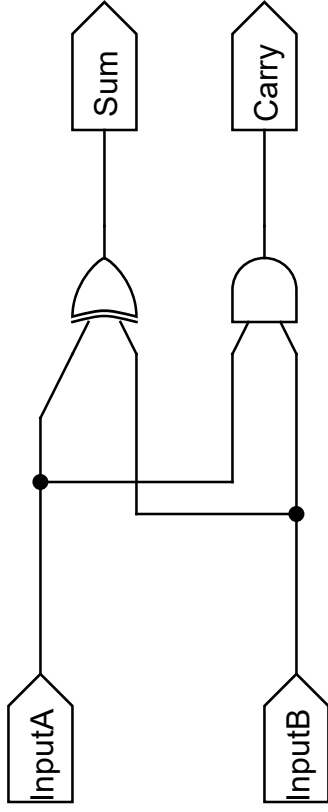
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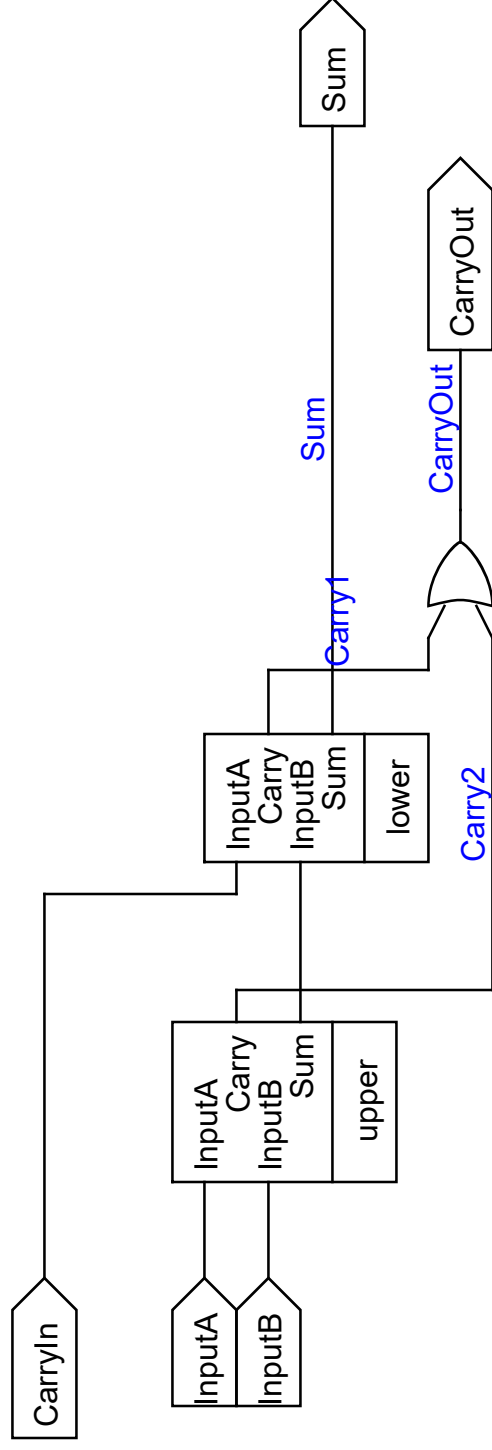
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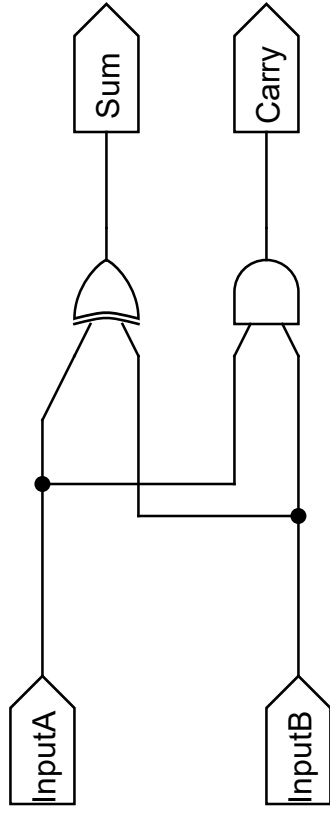
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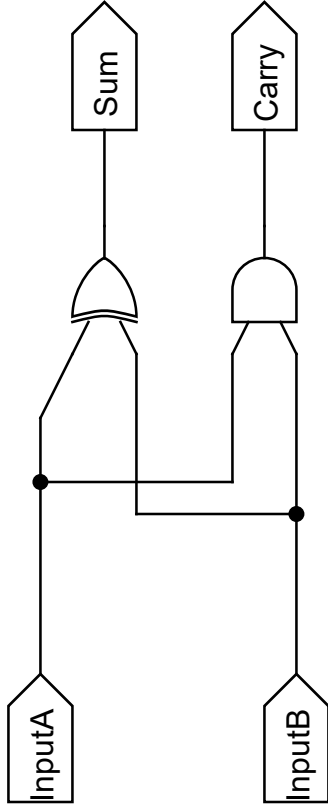
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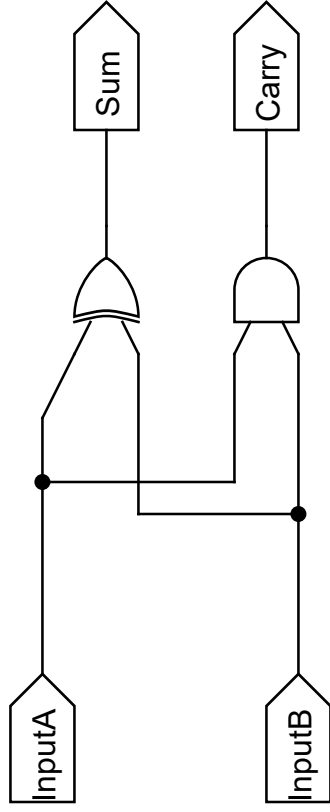
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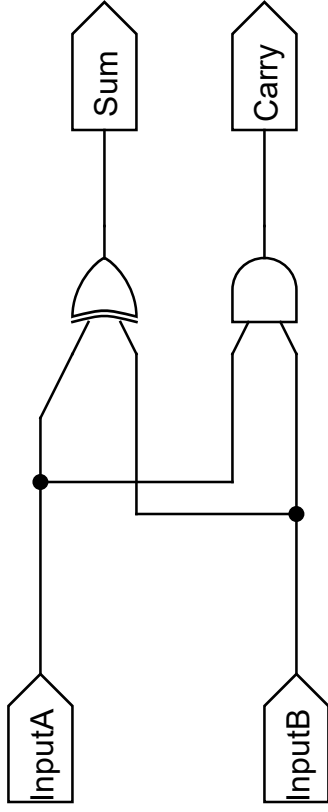
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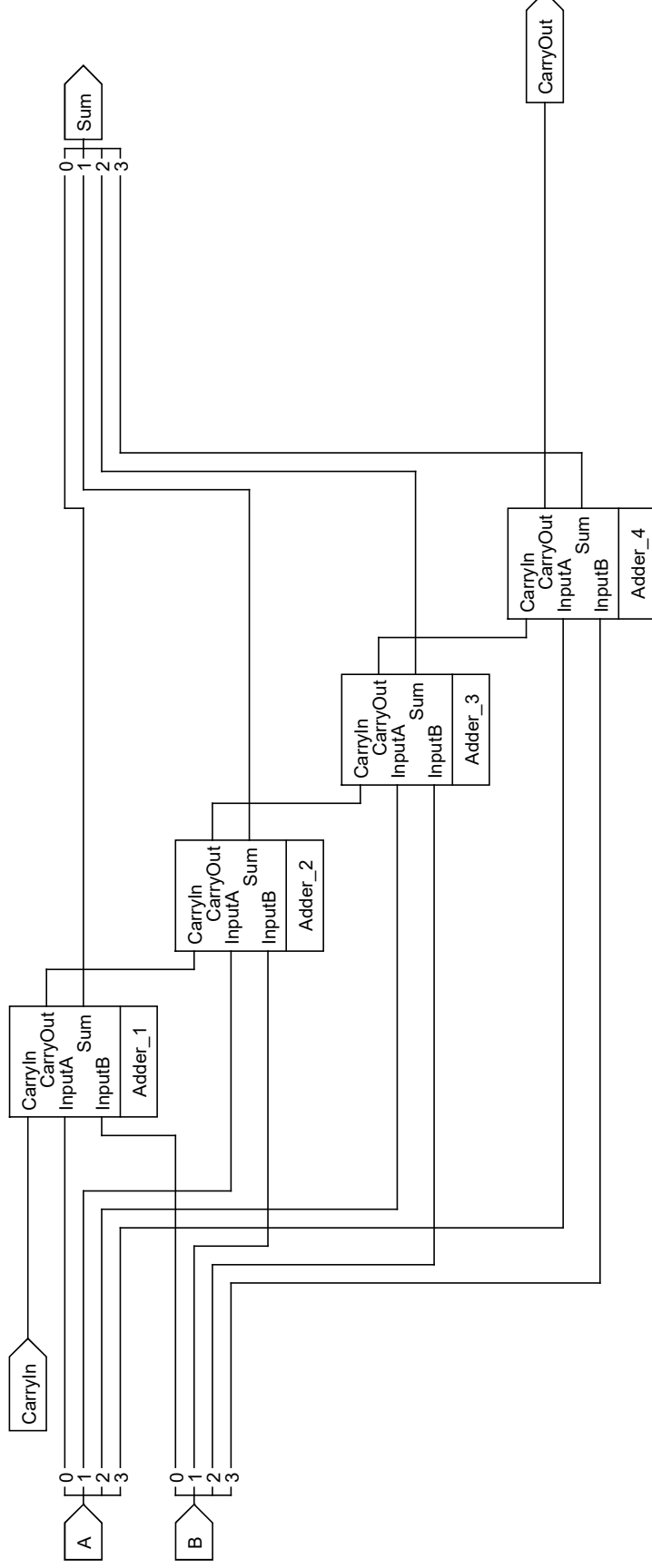
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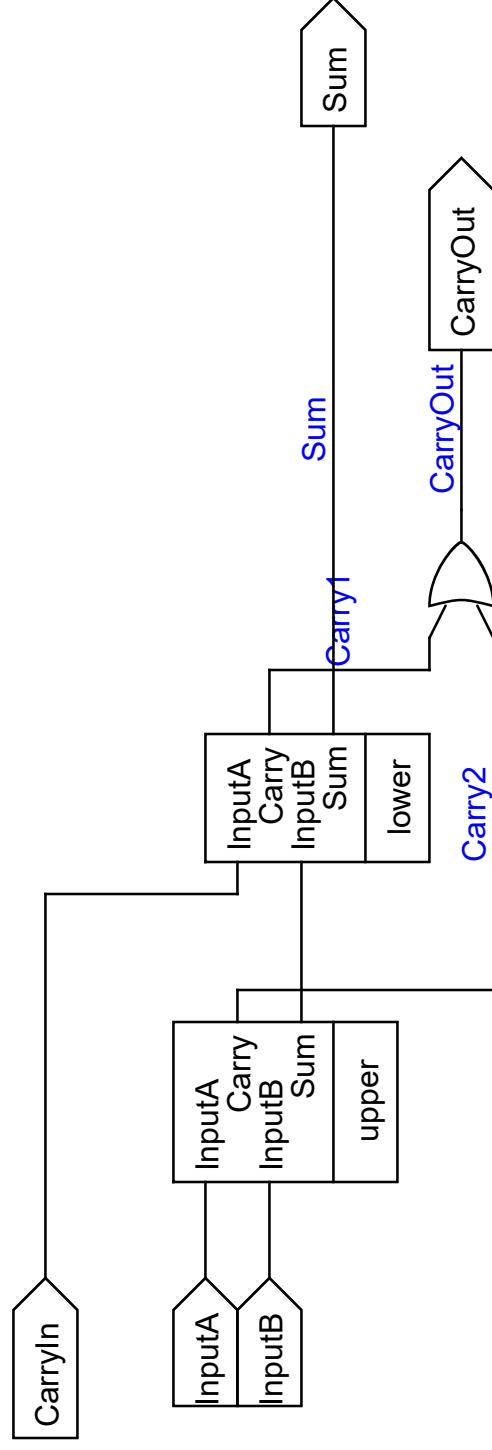
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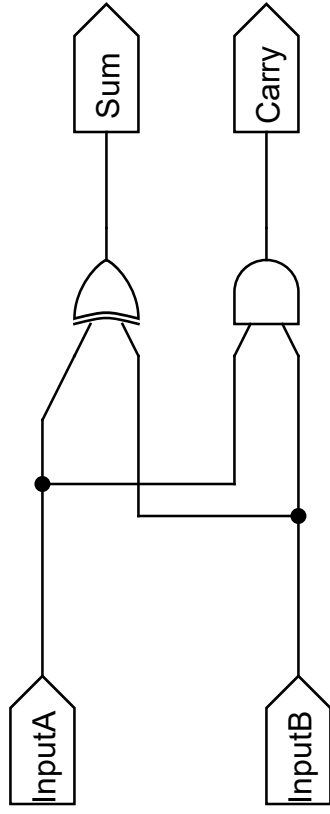
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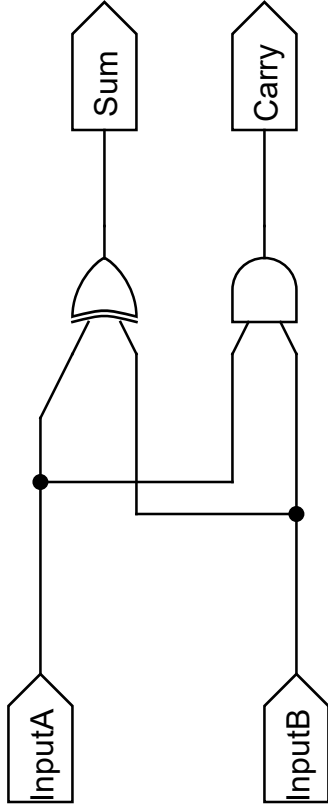
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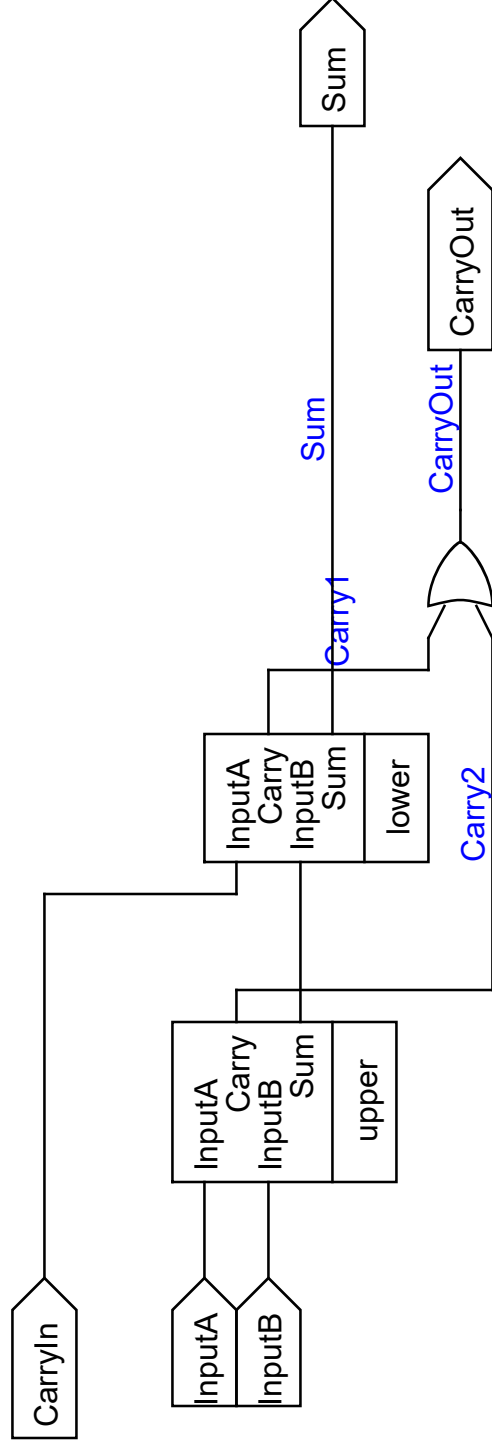
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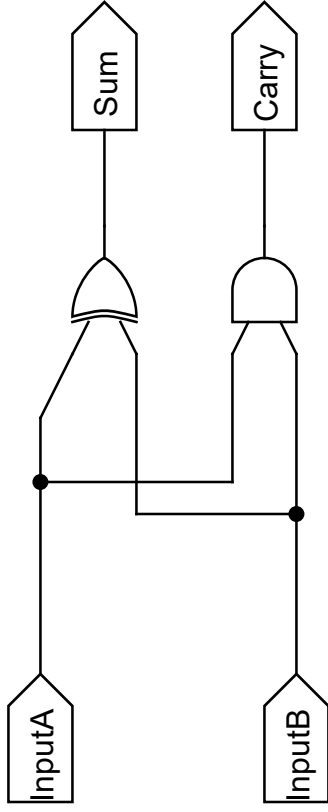
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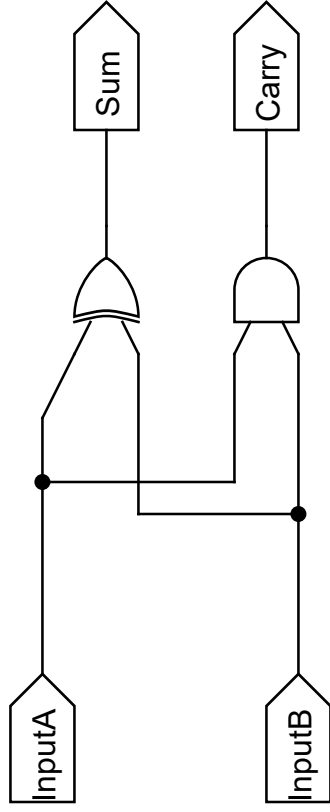
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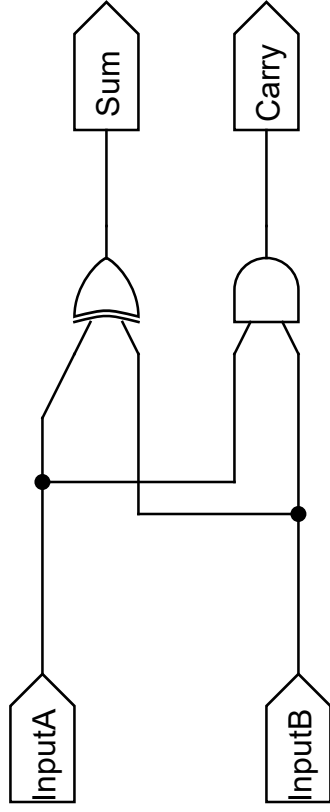
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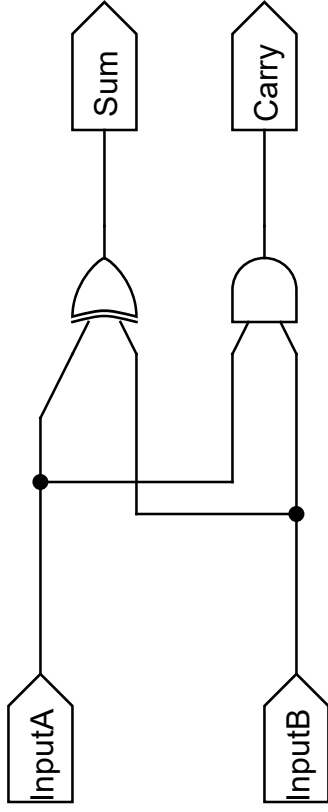
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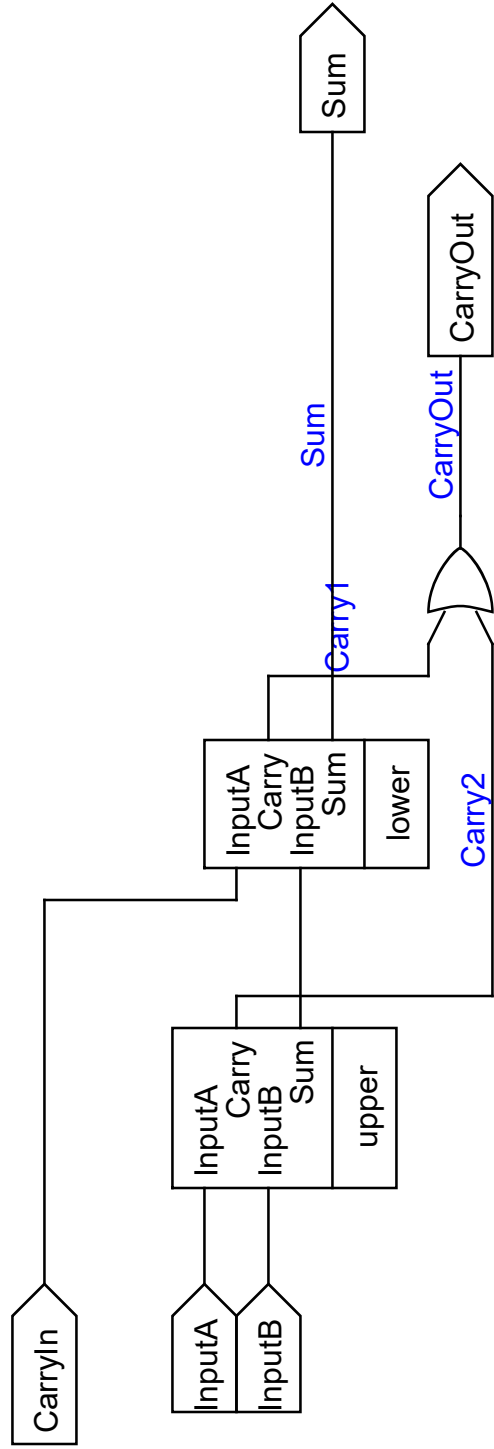
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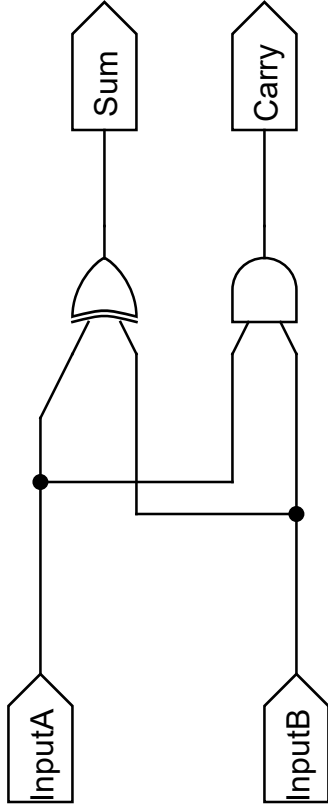
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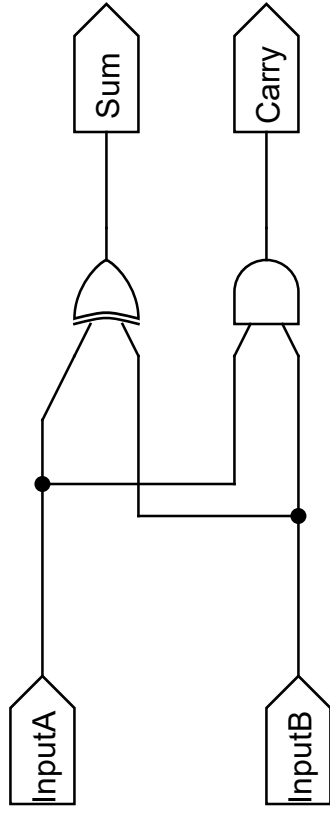
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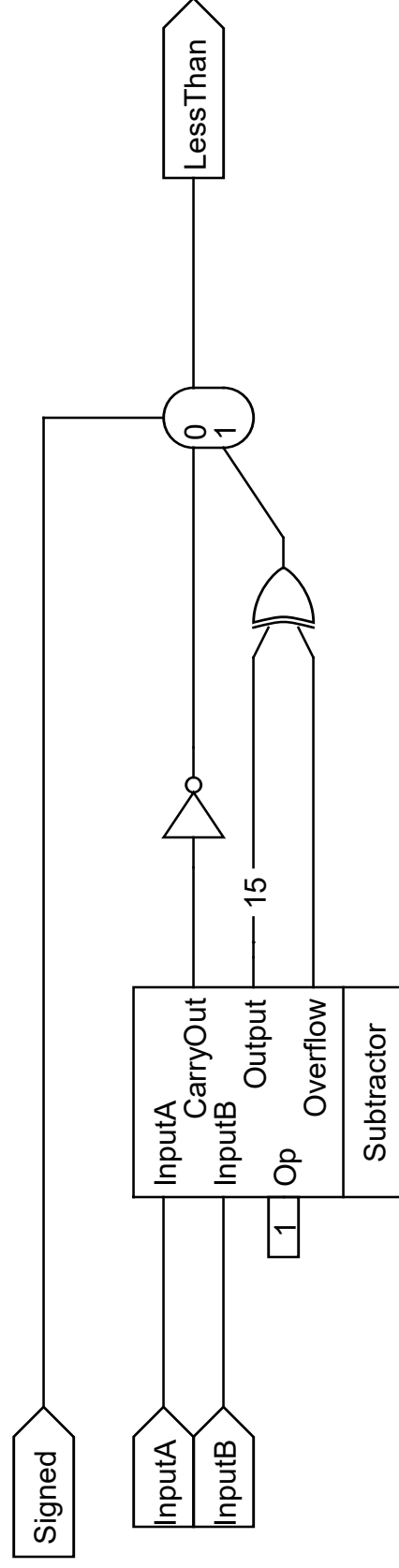


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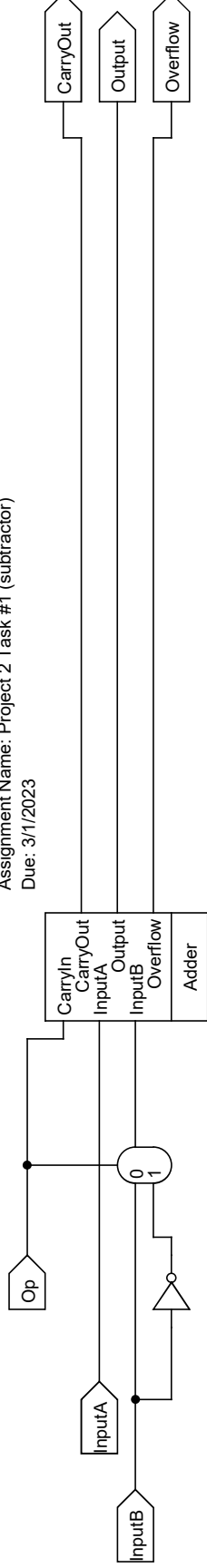
Comparator in ALU_16bit_starter

Name: James Baumhardt & Brendon Werner
File Name: project2_2.jls
Assignment Name: Project 2 Task #2 (slt)
Due: 3/1/2023
Total # of Gates in Circuit: 173
Total Time = 2215ns

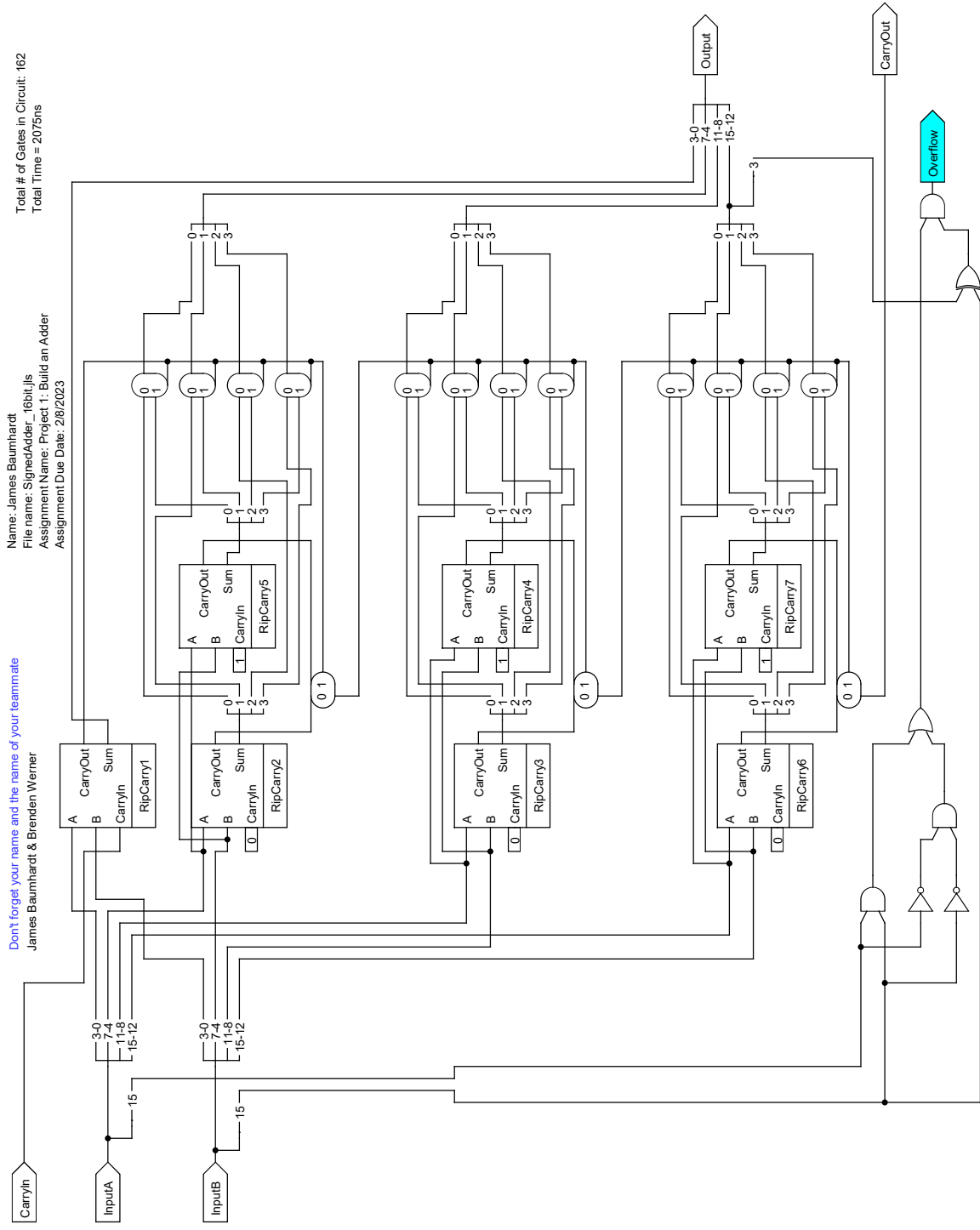


Subtractor in Comparator in ALU_16bit_starter

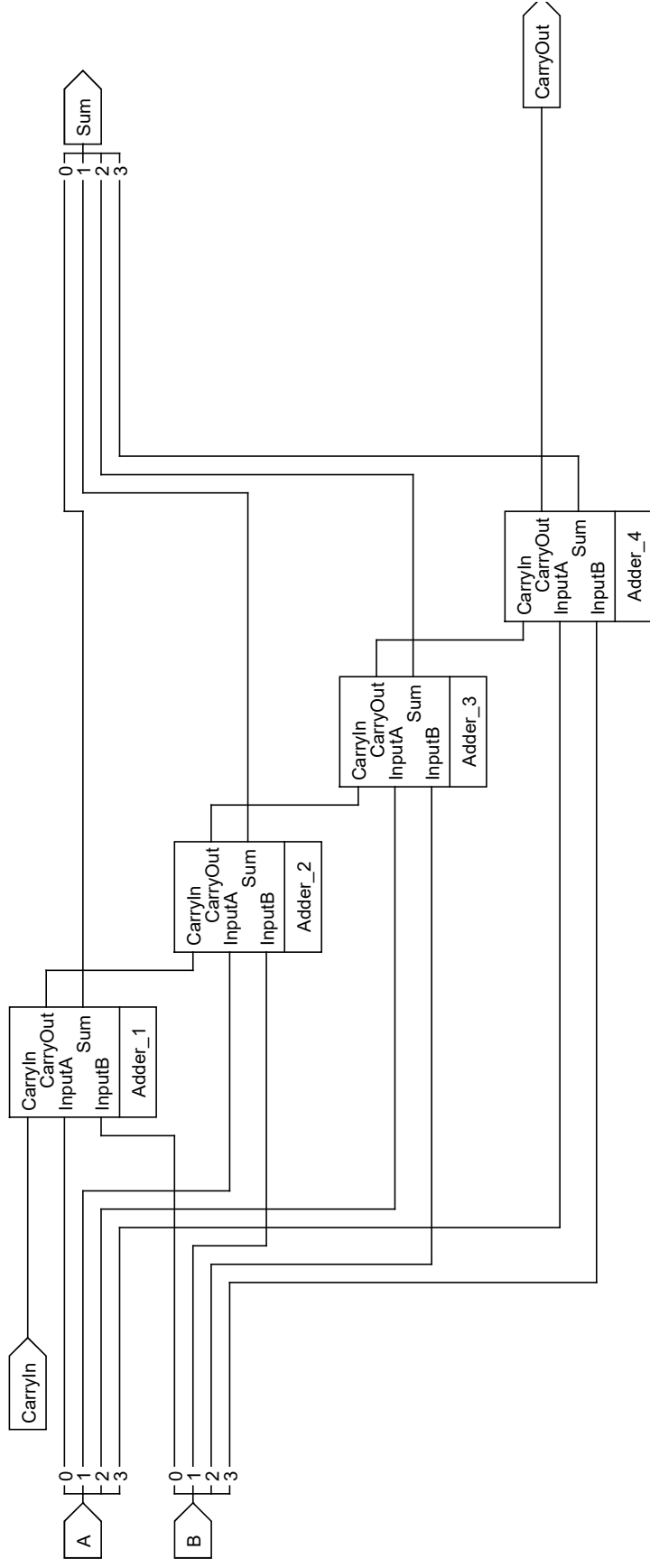
Name: James Baumhardt & Brendon Werner Total # of Gates in Circuit: 164
File Name: projec2.jls Total Time = 2110ns
Assignment Name: Project 2 Task #1 (subtractor)
Due: 3/1/2023



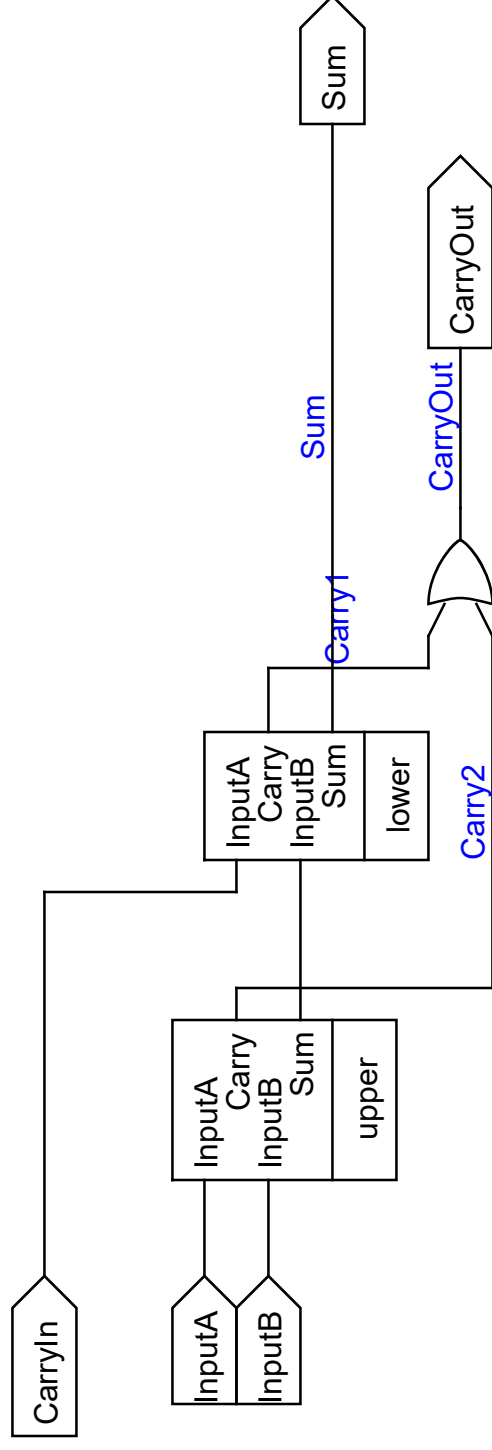
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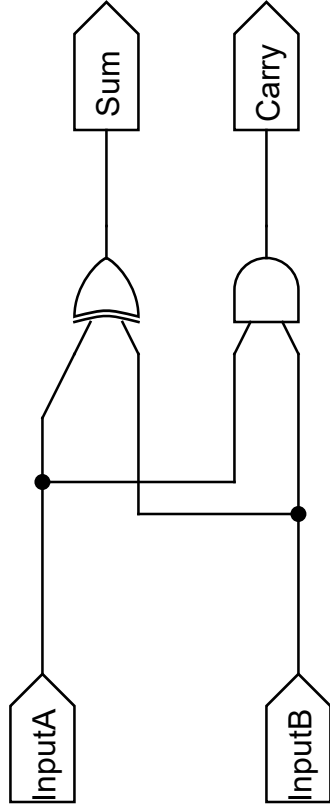
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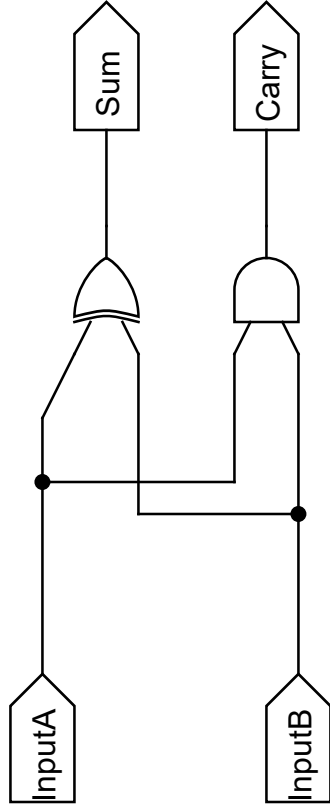
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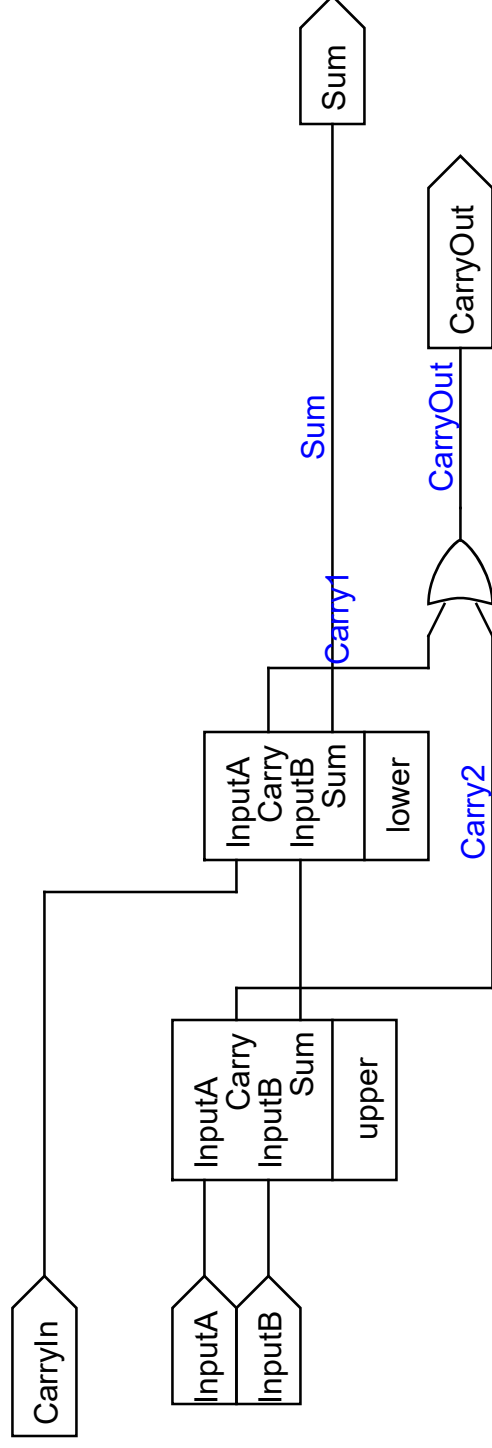
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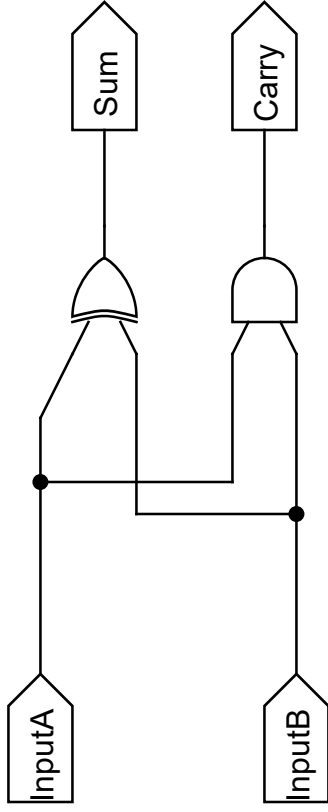
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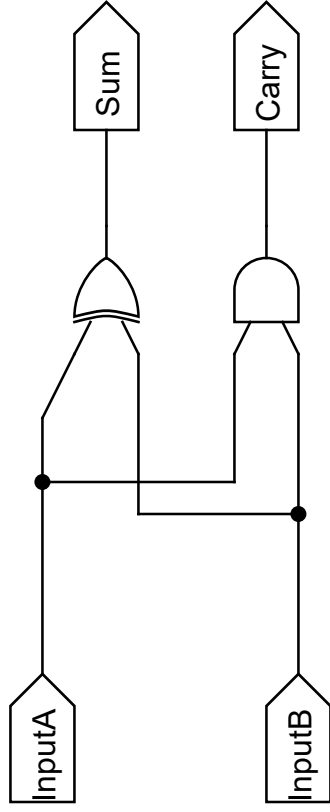
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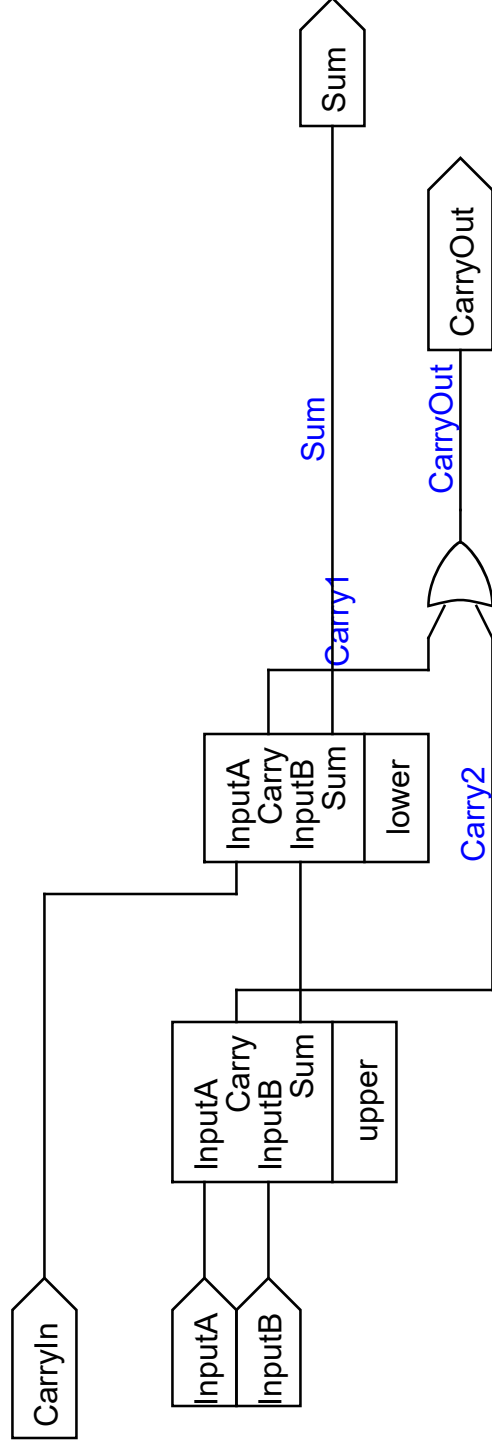
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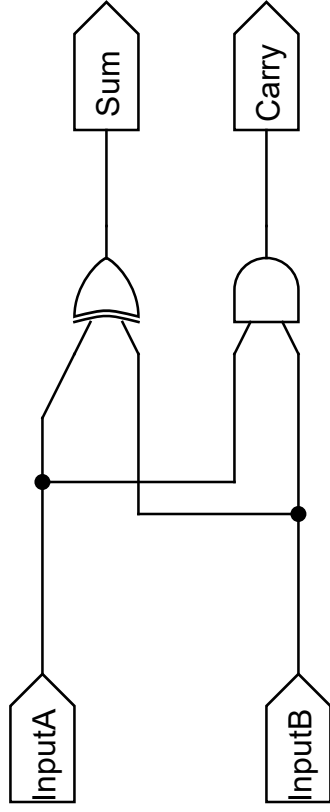
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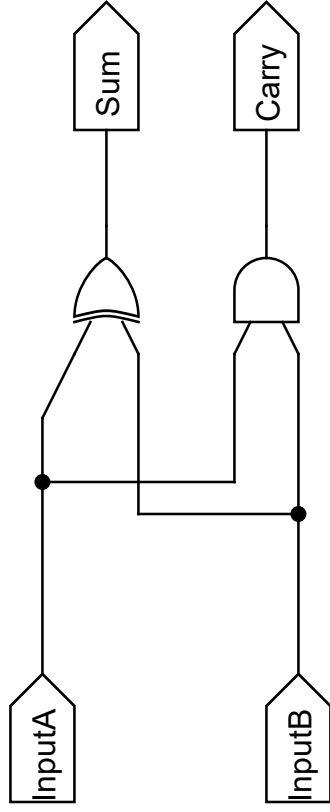
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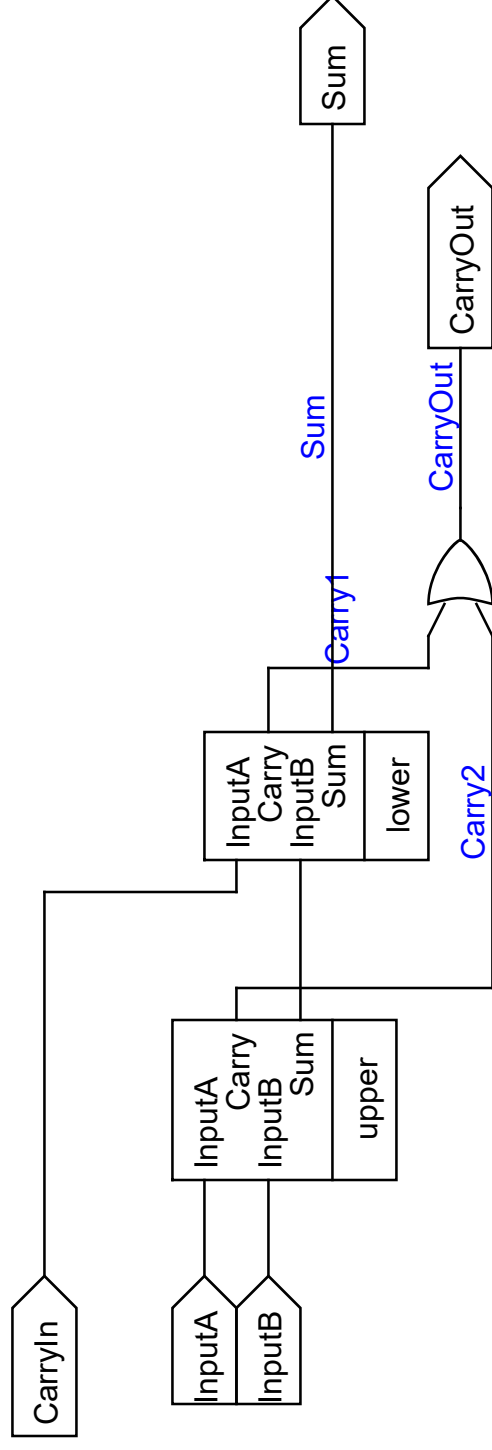
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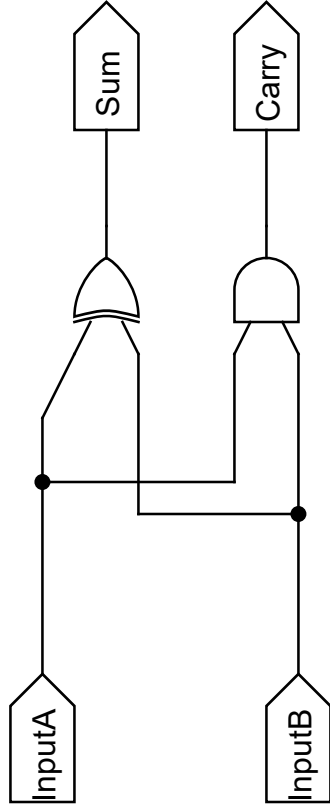
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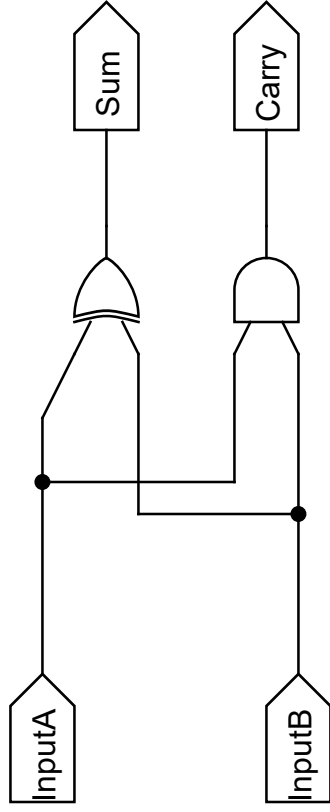
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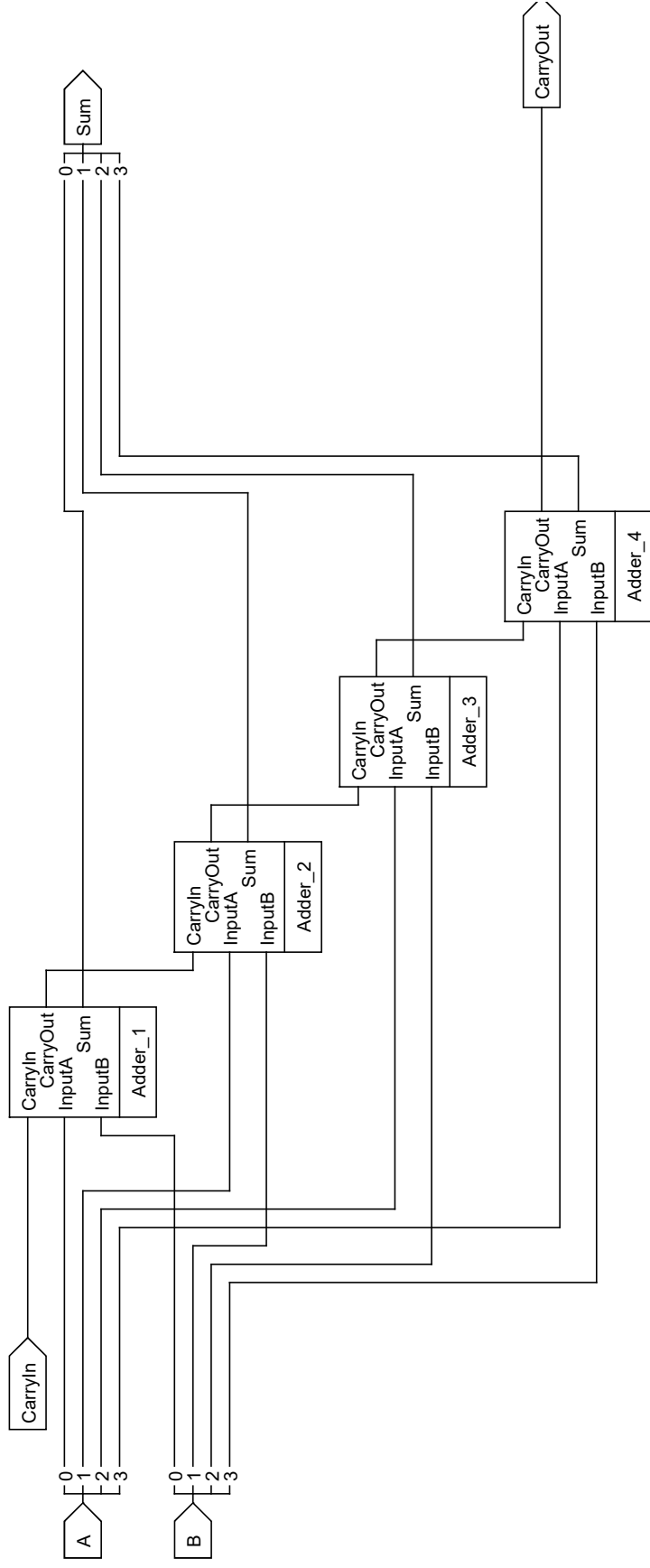
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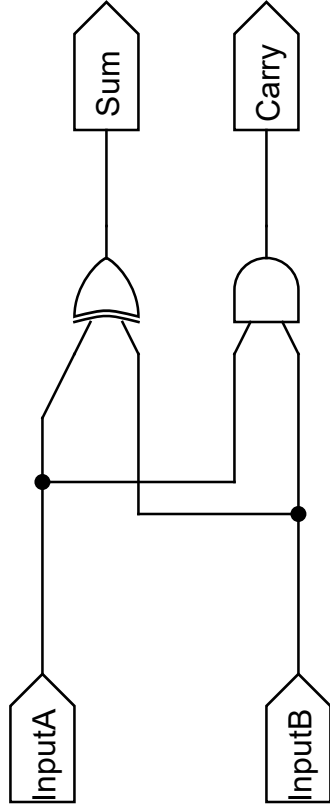
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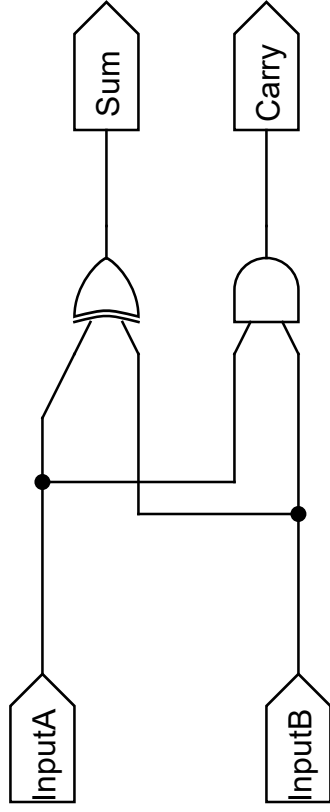
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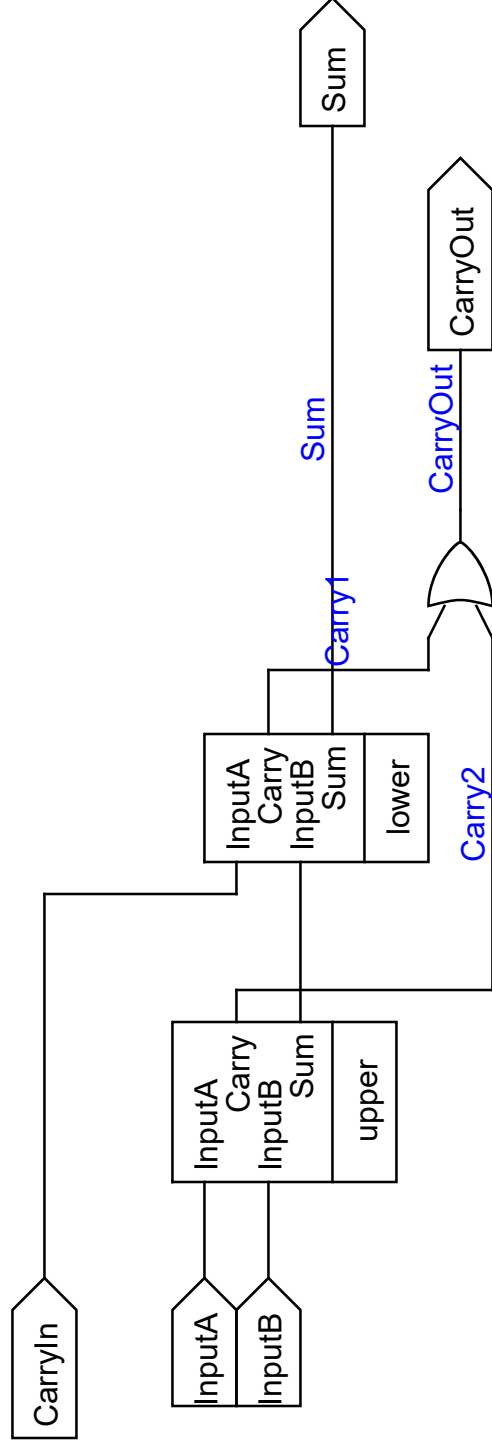
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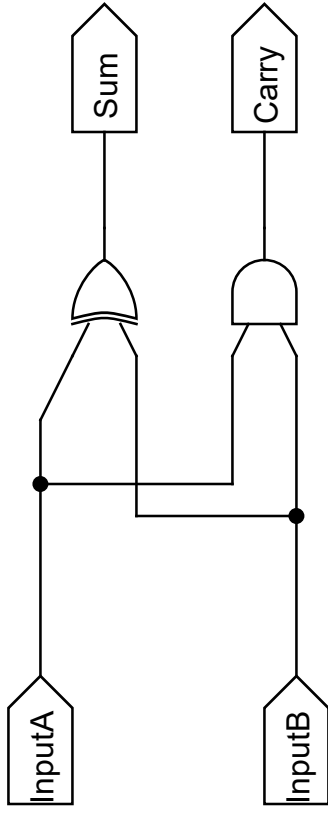
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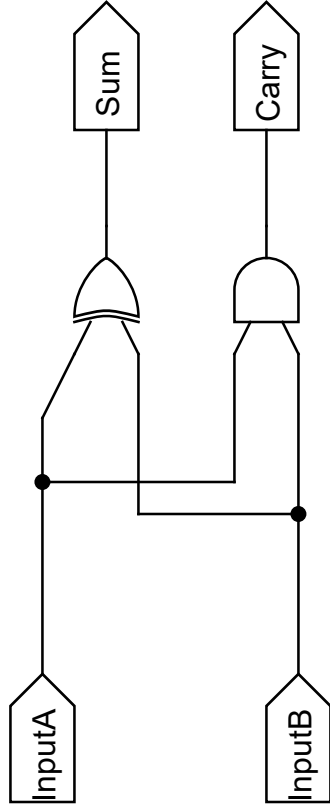
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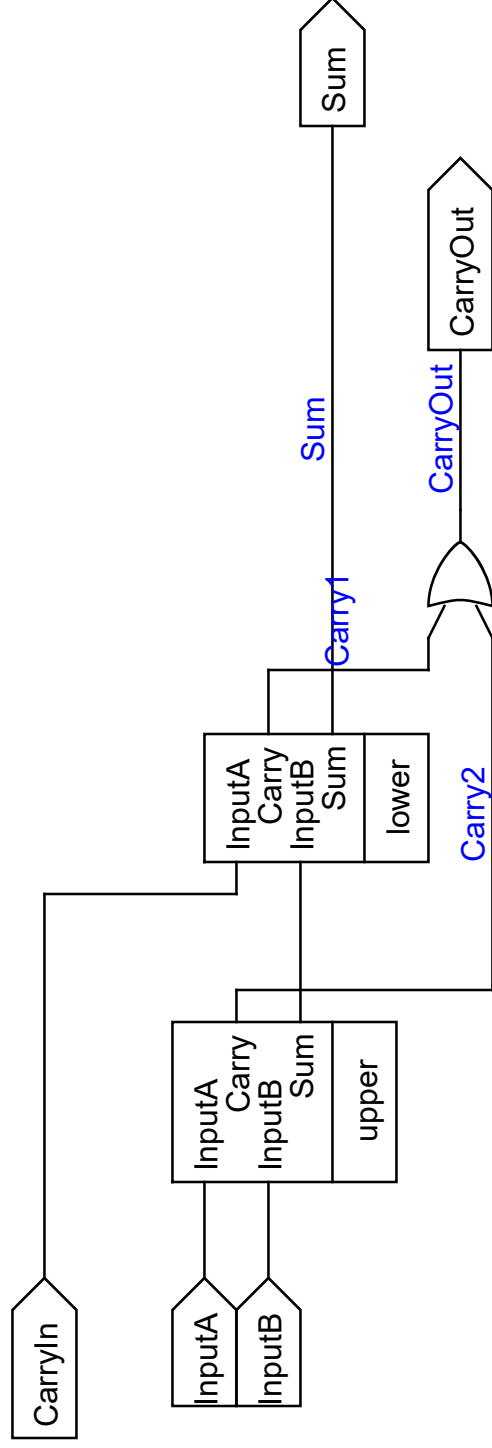
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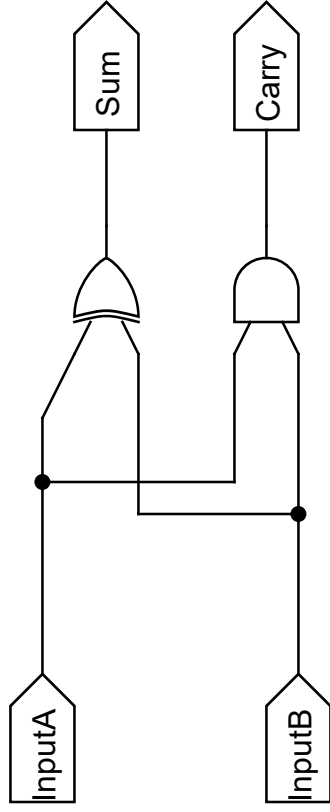
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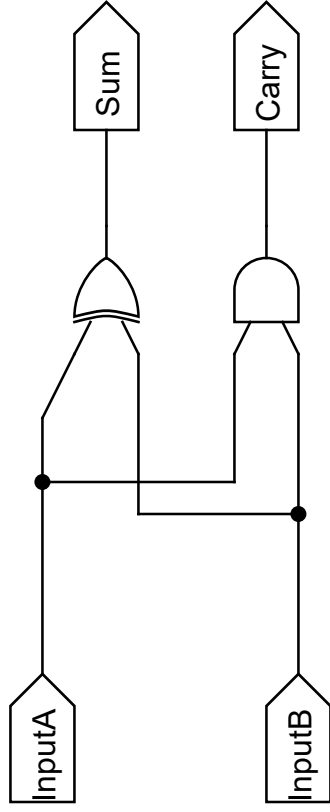
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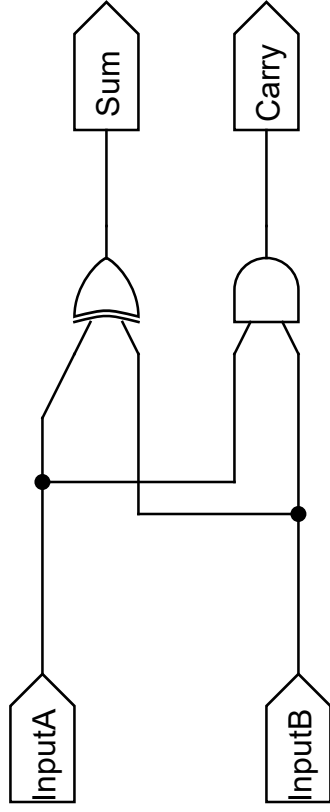
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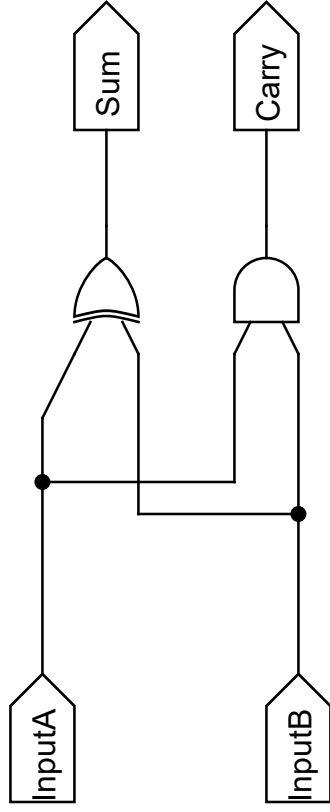
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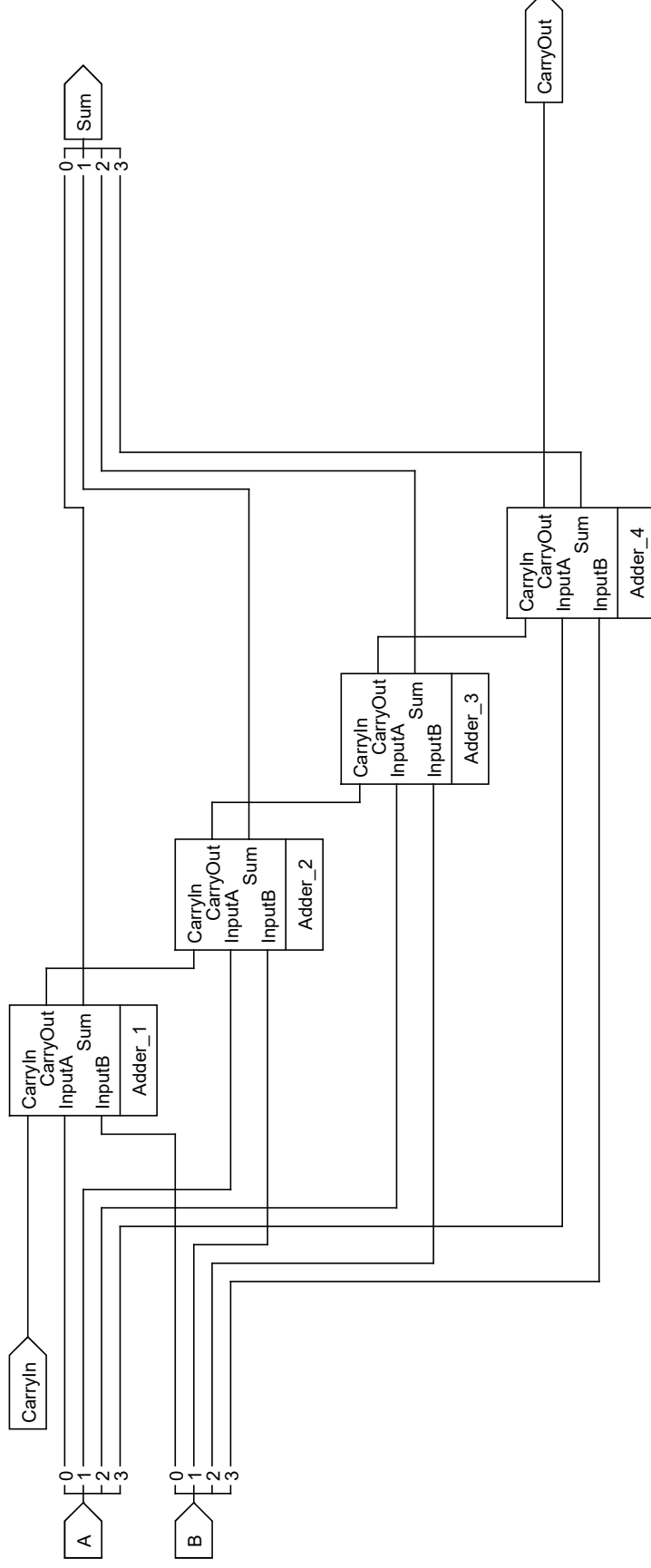
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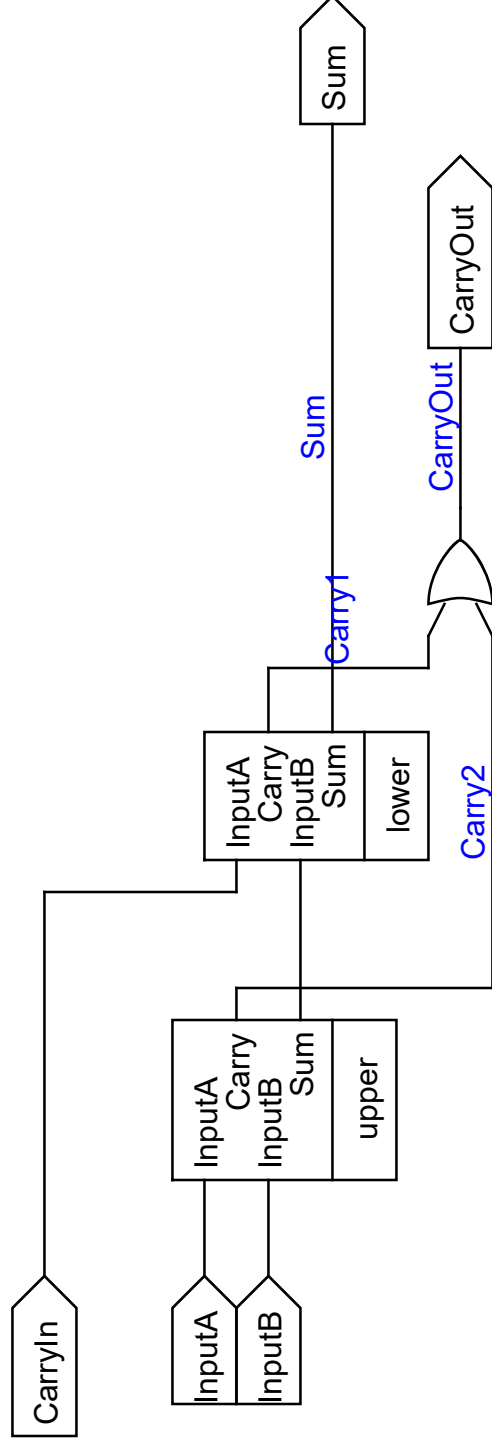
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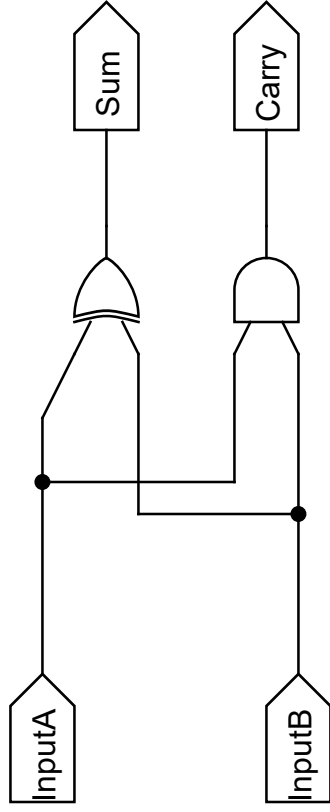
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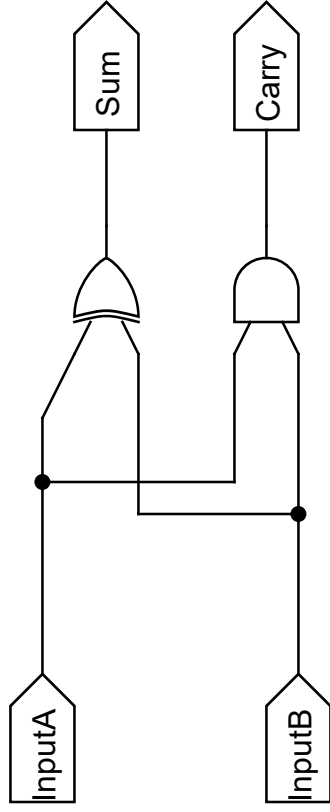
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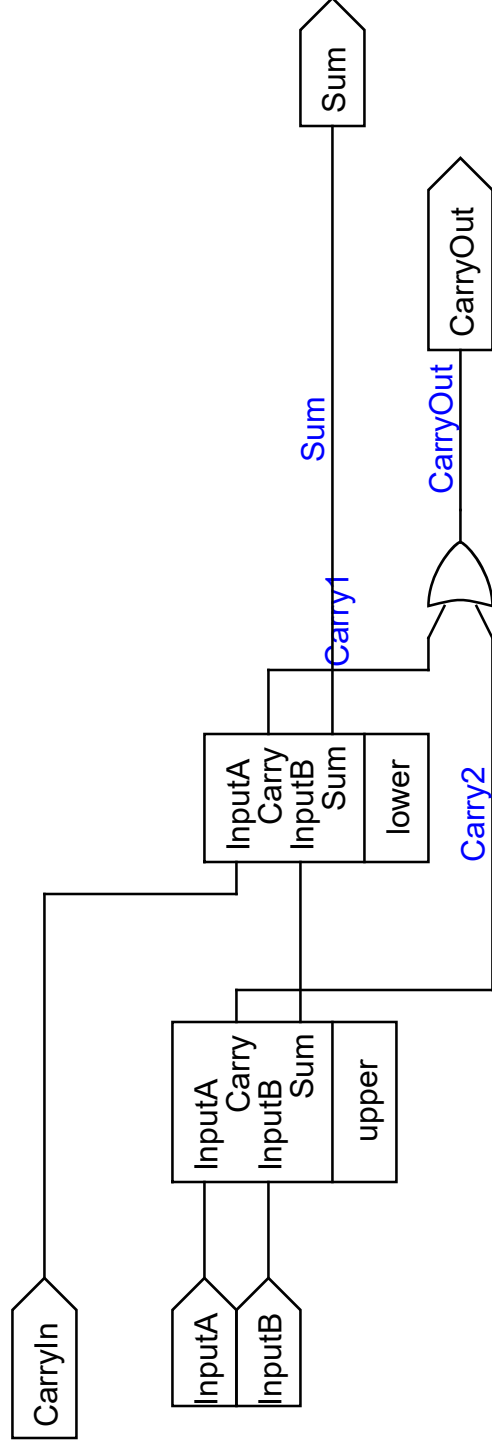
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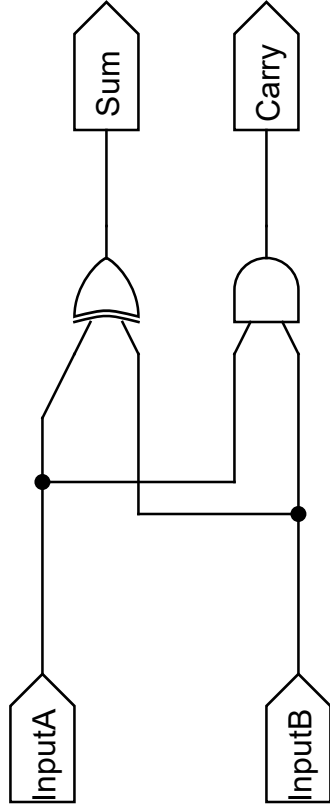
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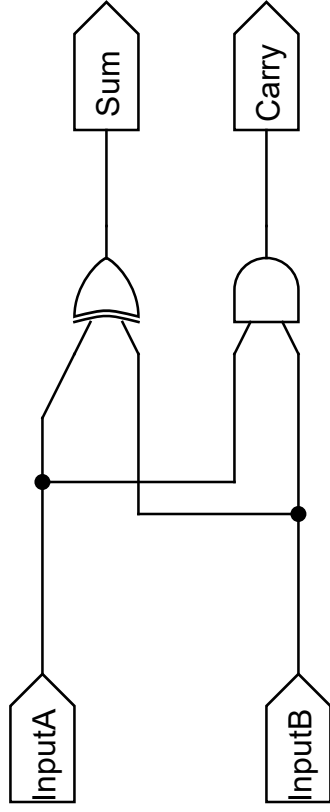
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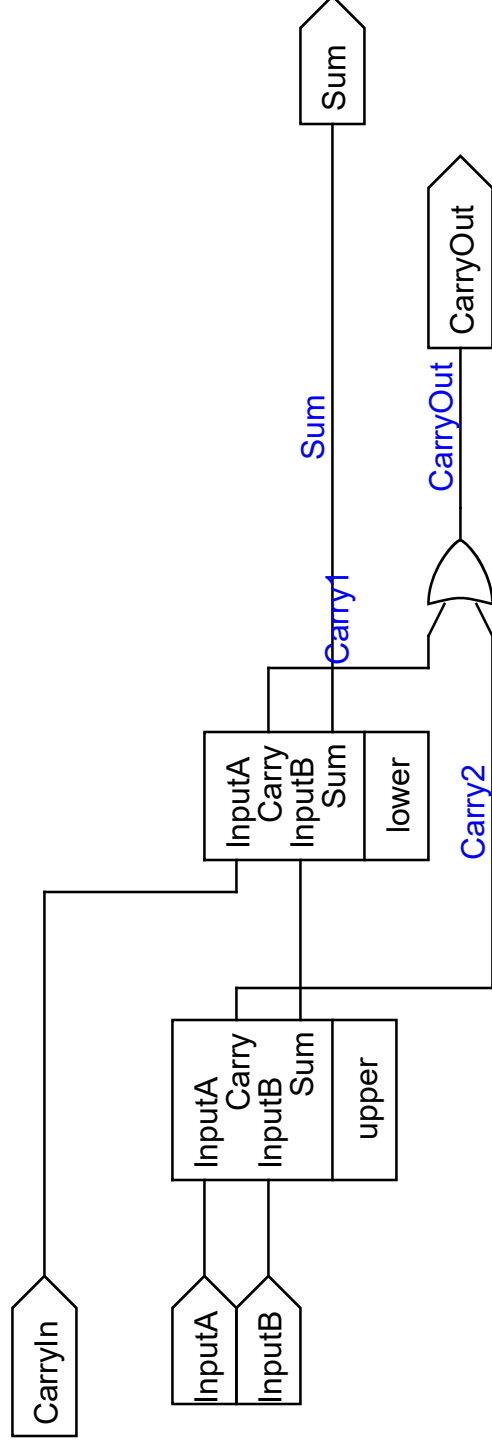
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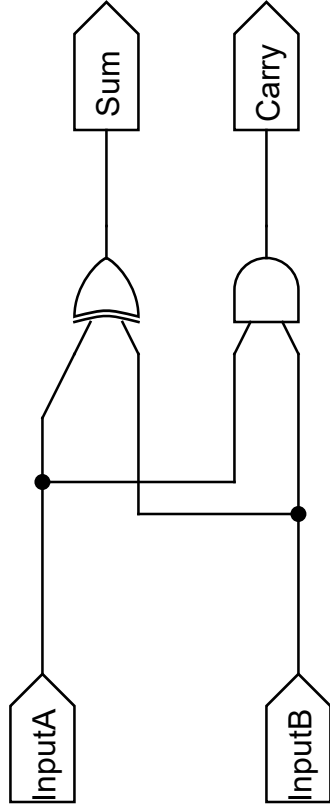
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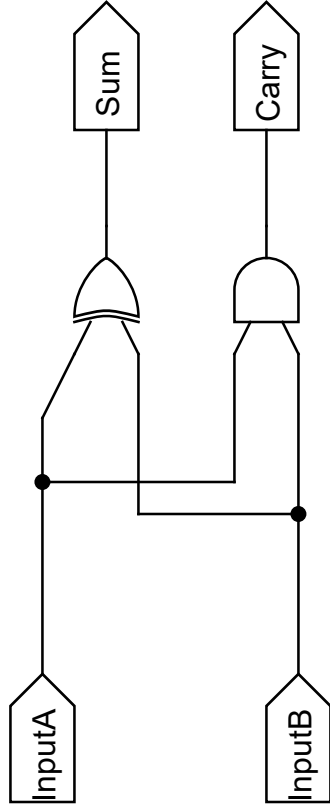
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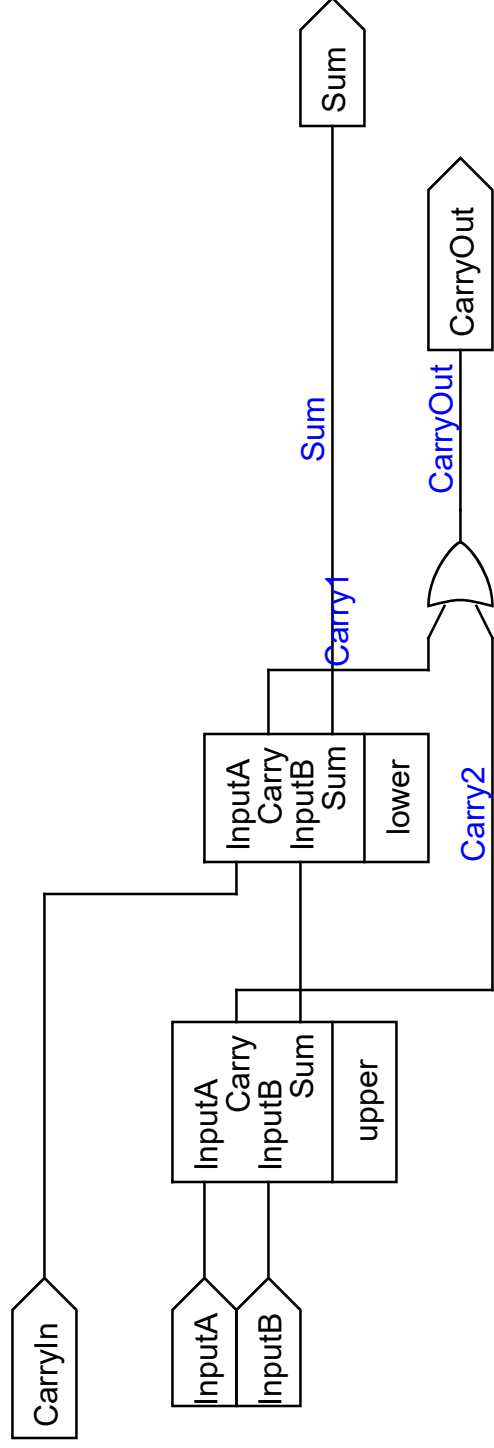
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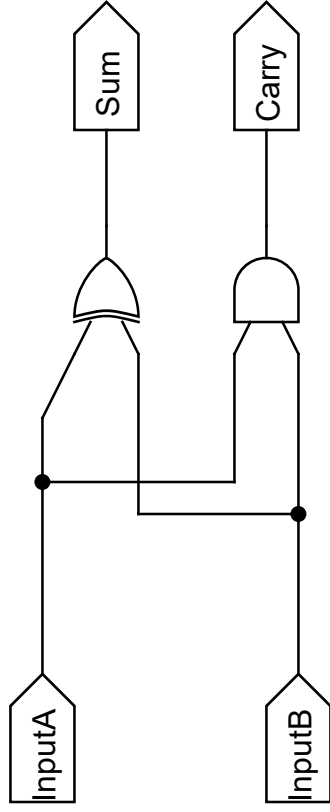
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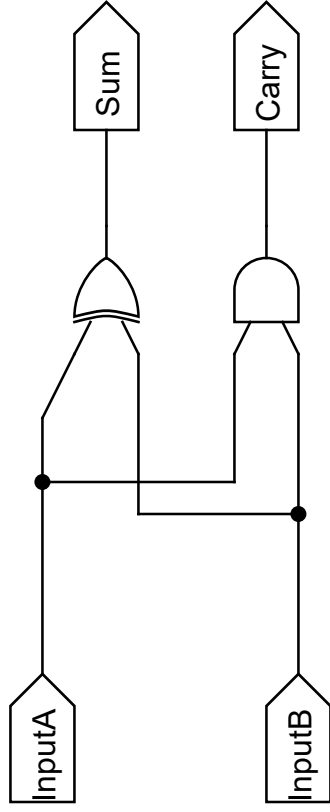
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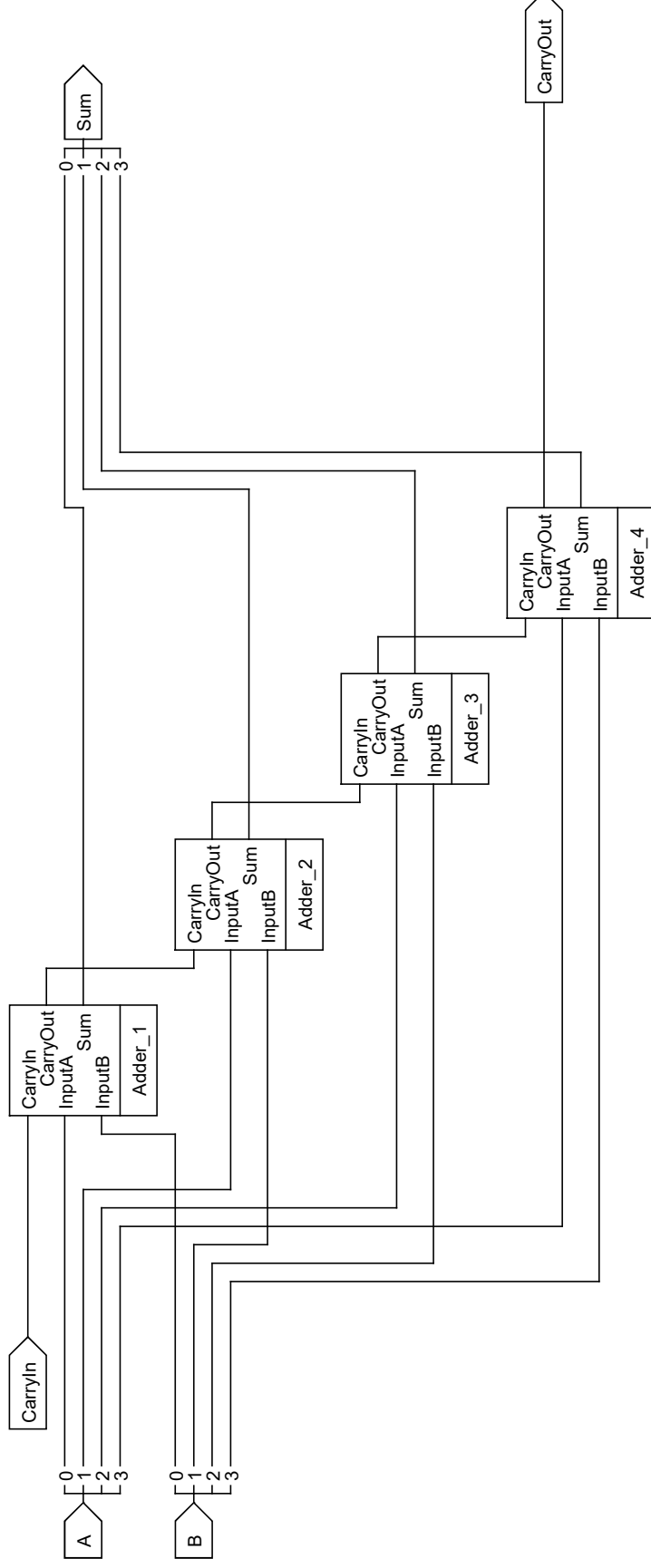
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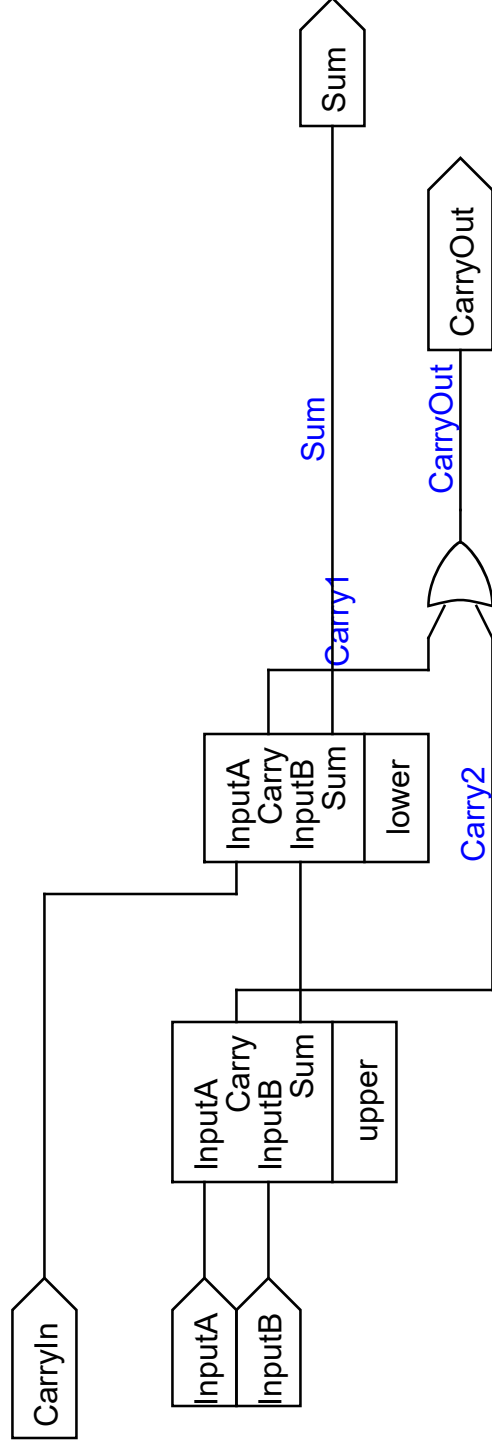
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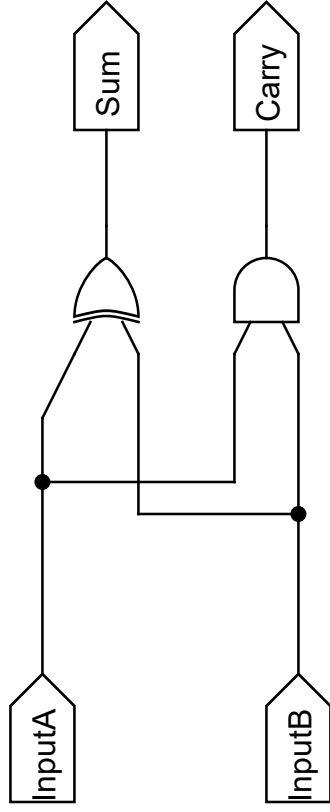
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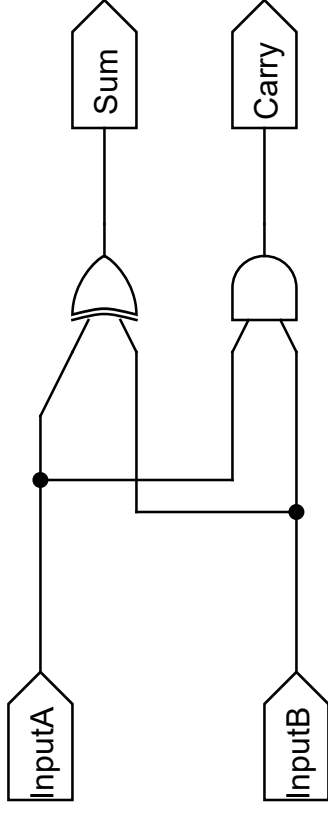
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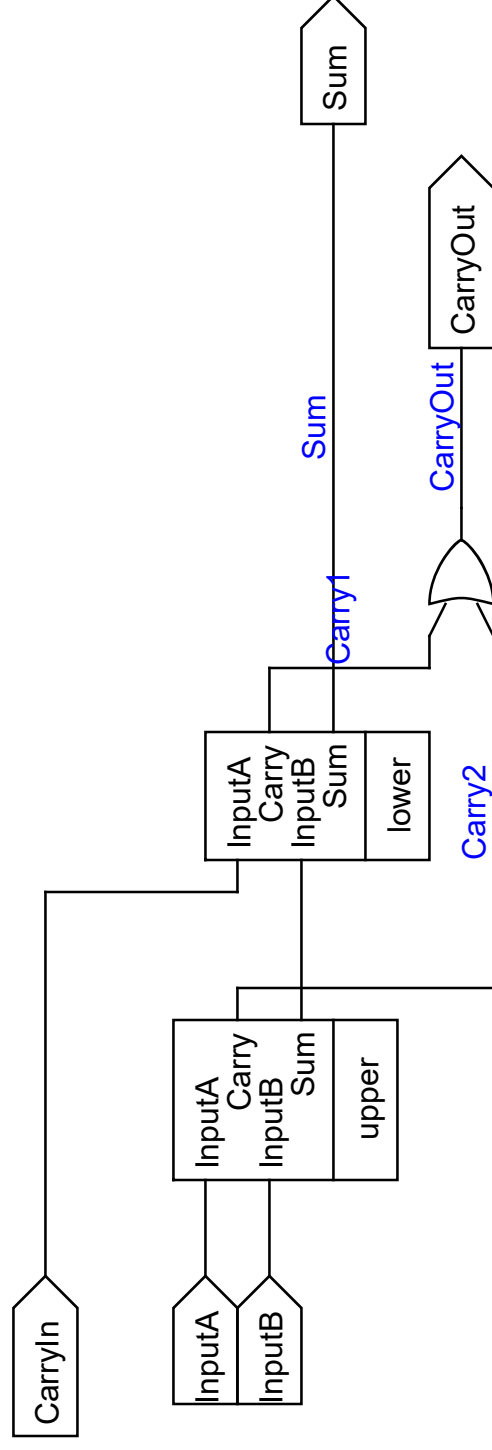
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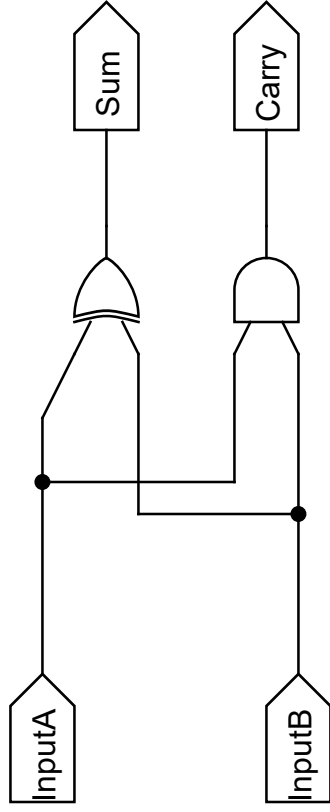
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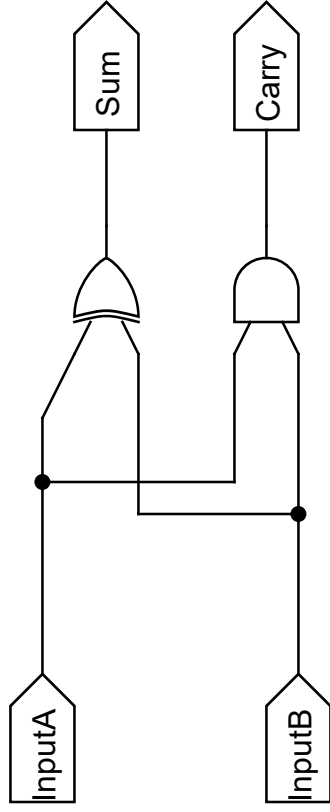
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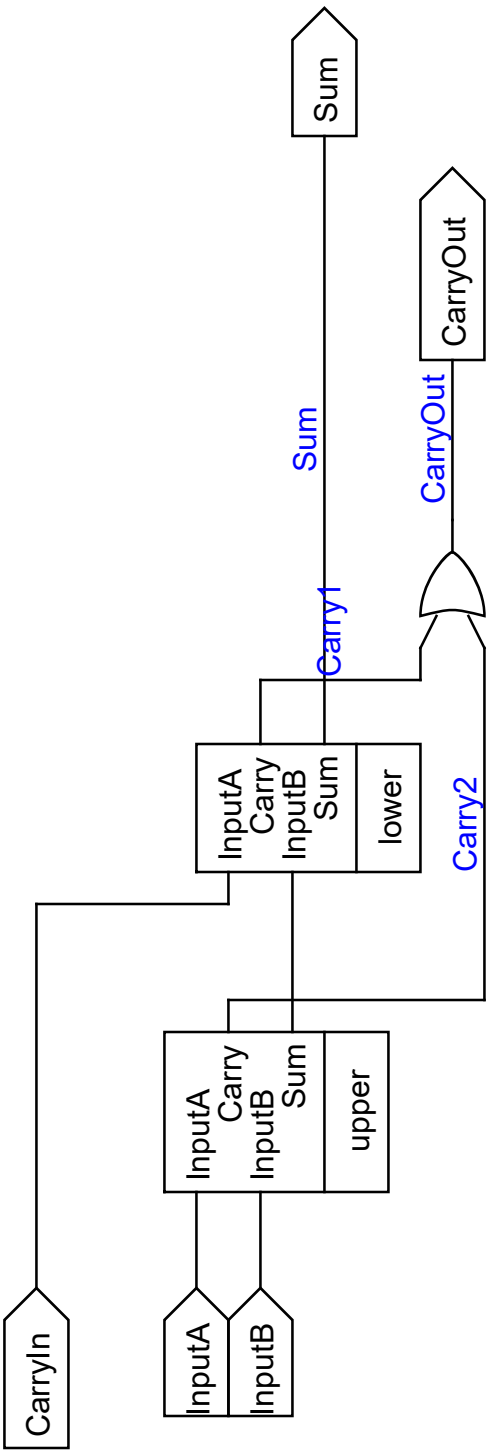
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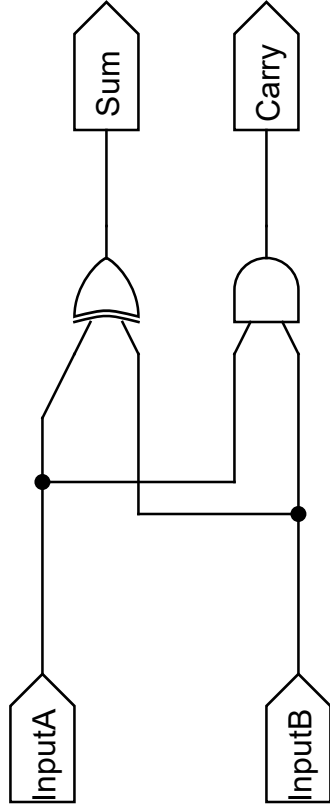
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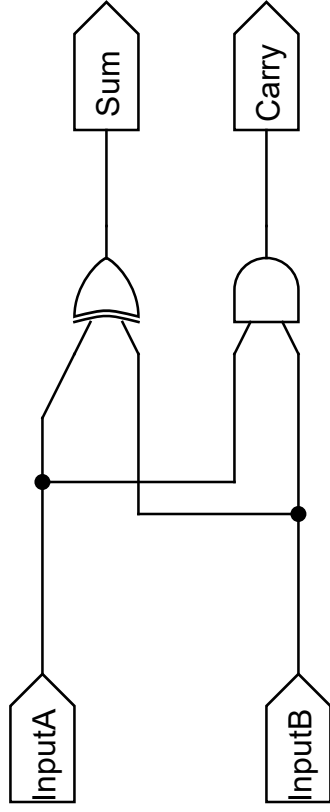
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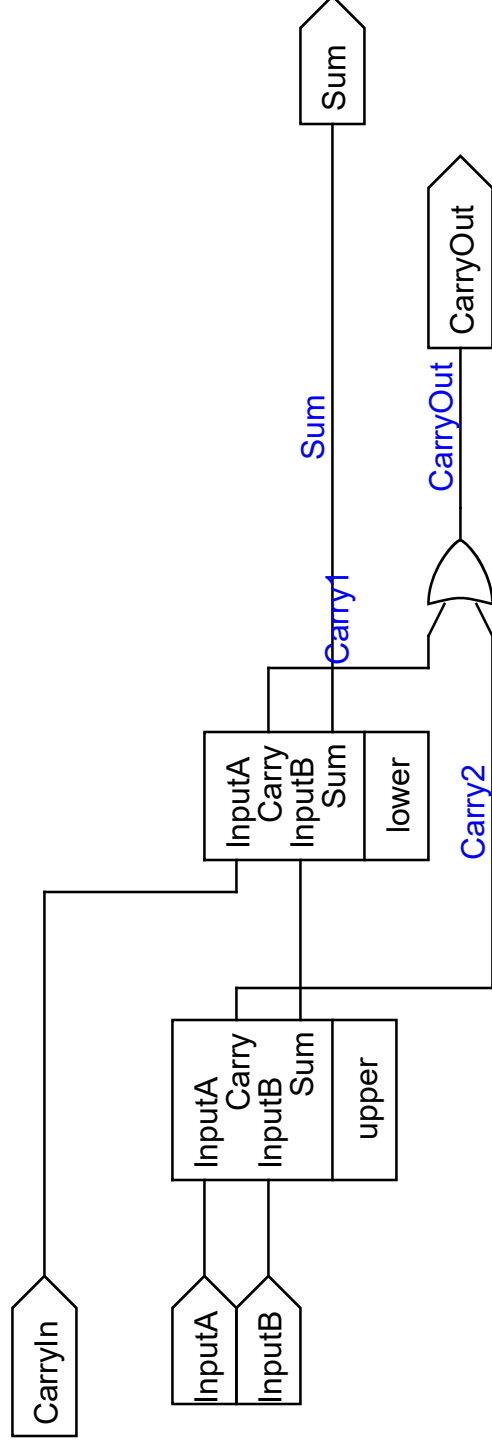
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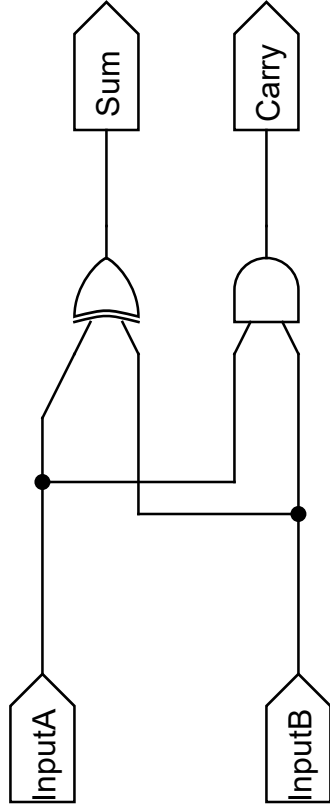
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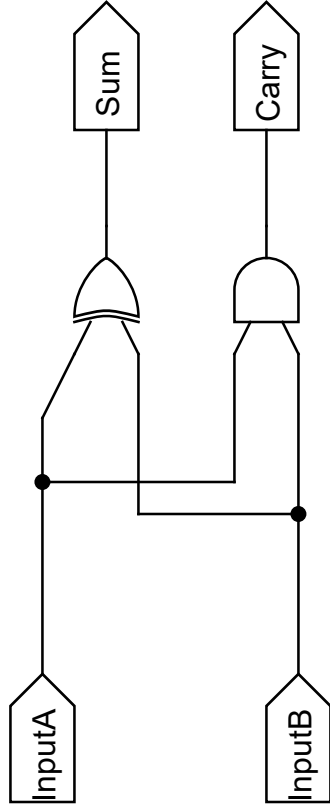
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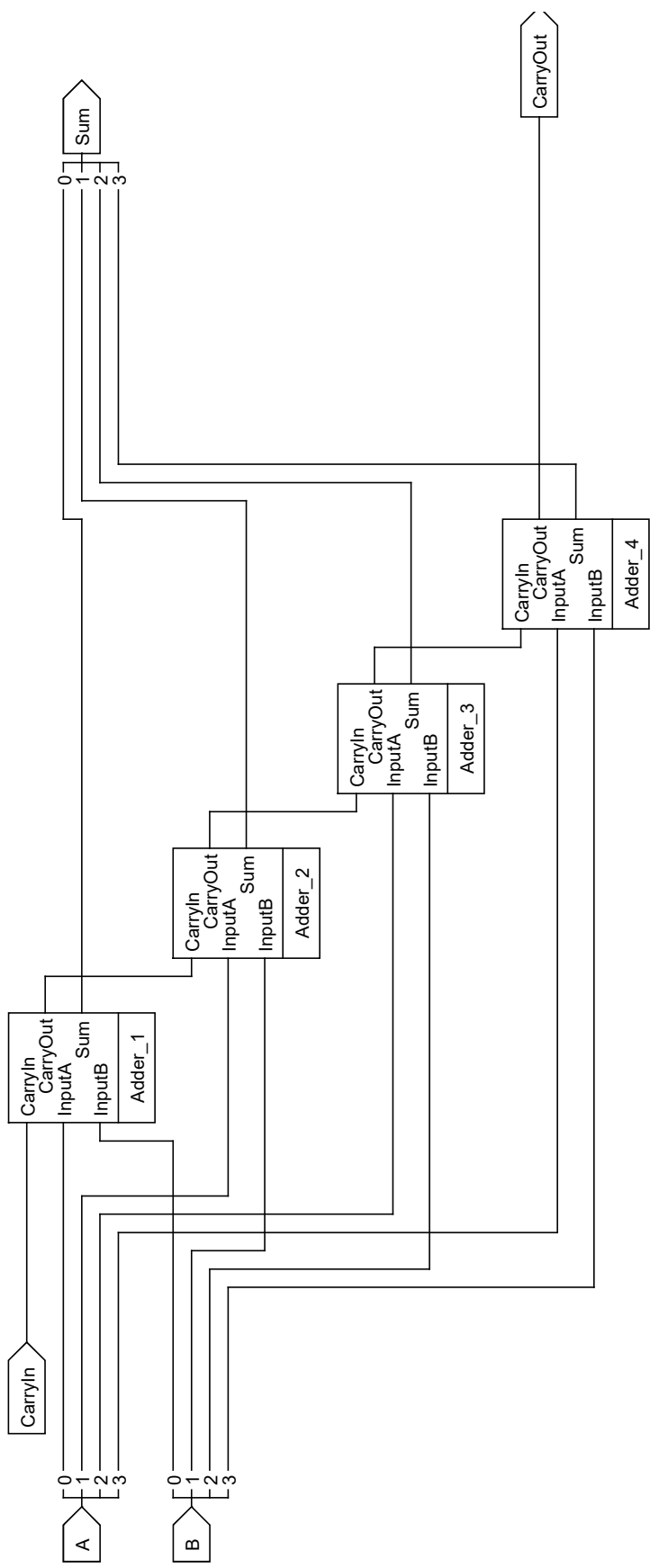
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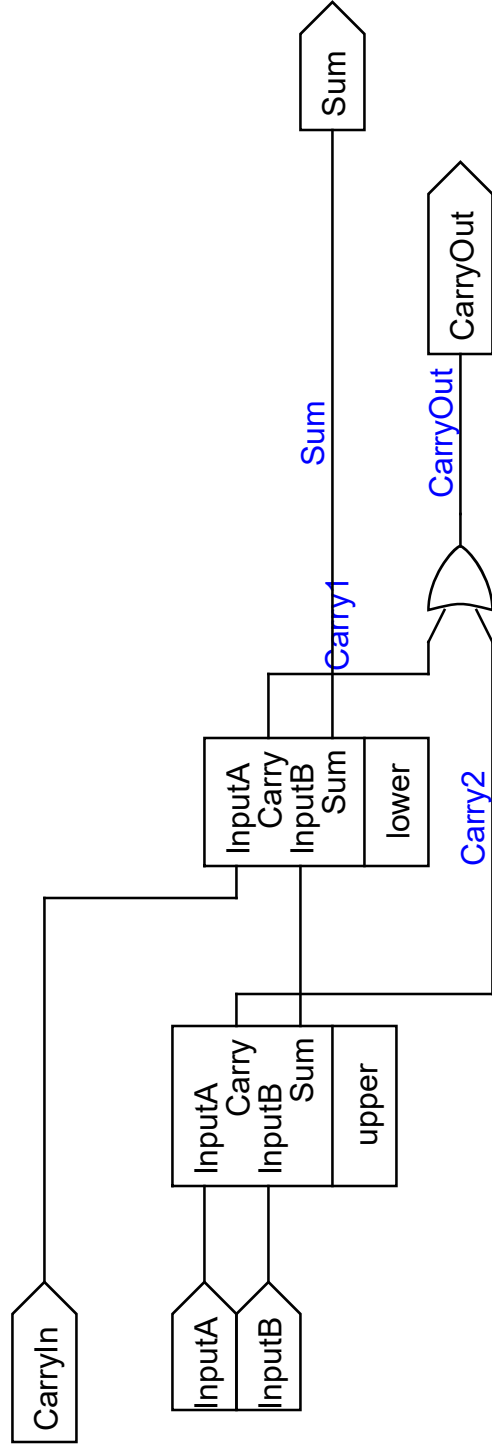
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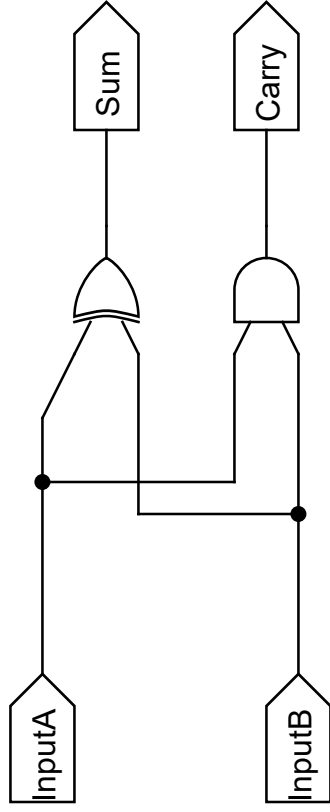
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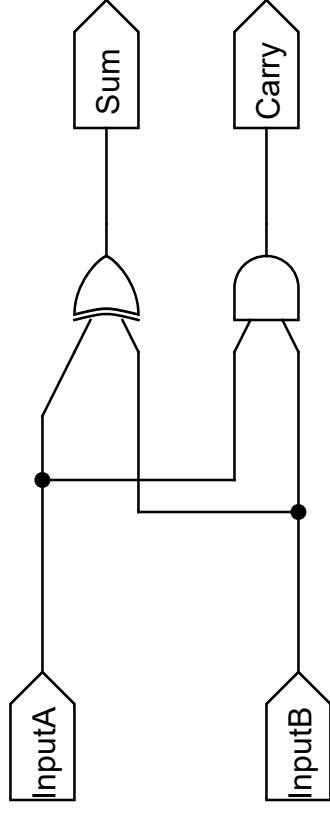
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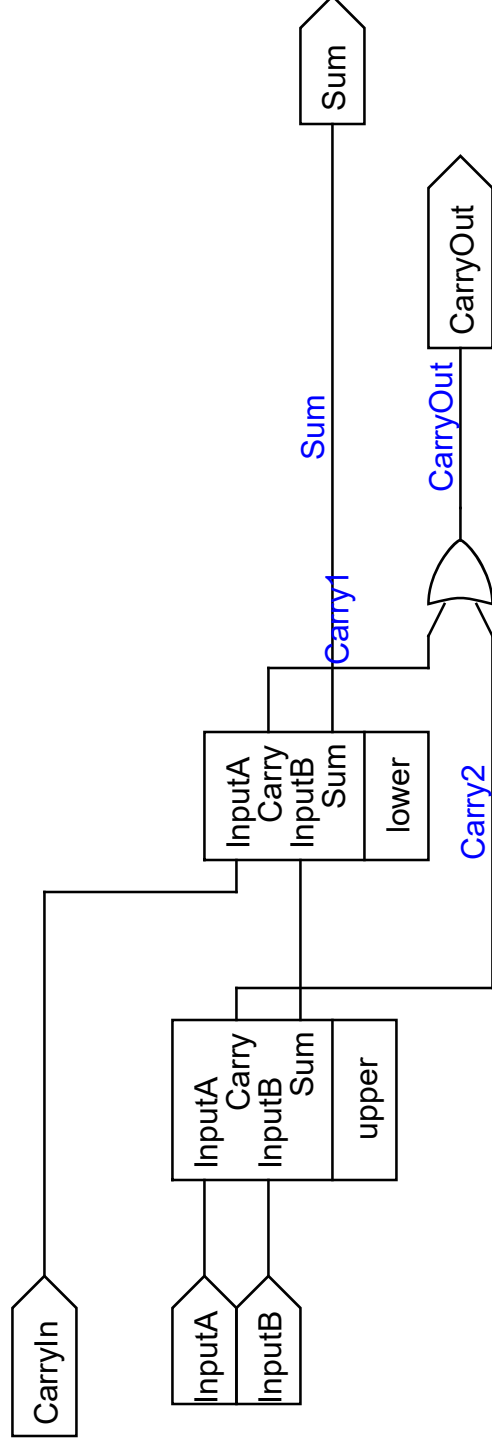
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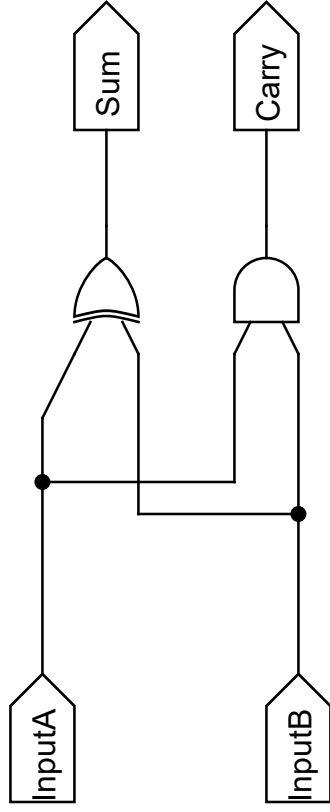
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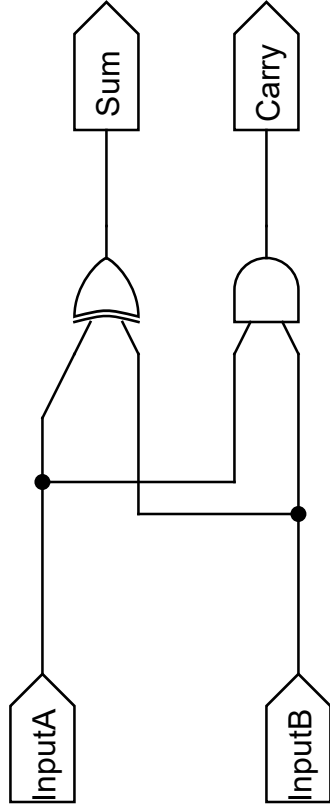
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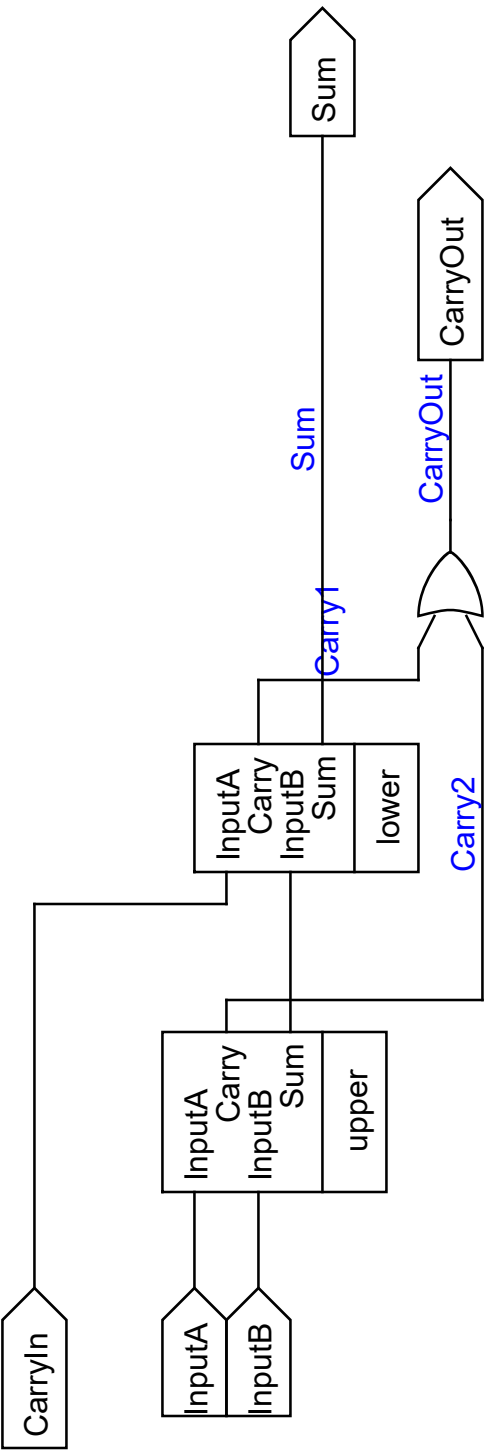
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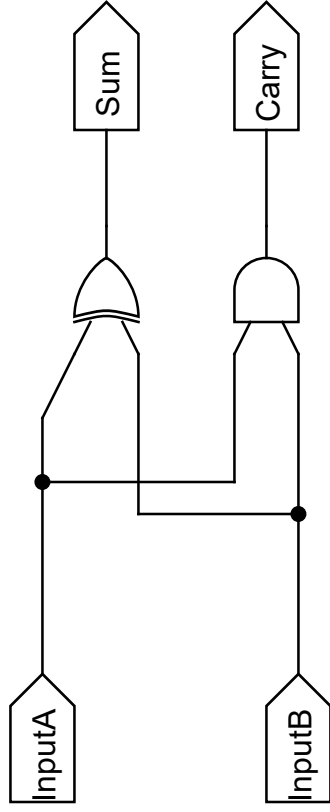
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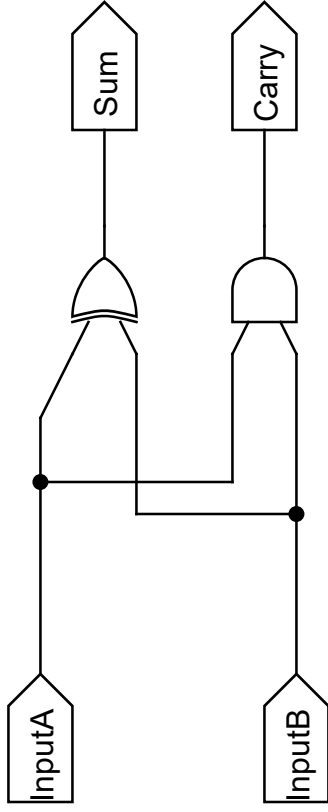
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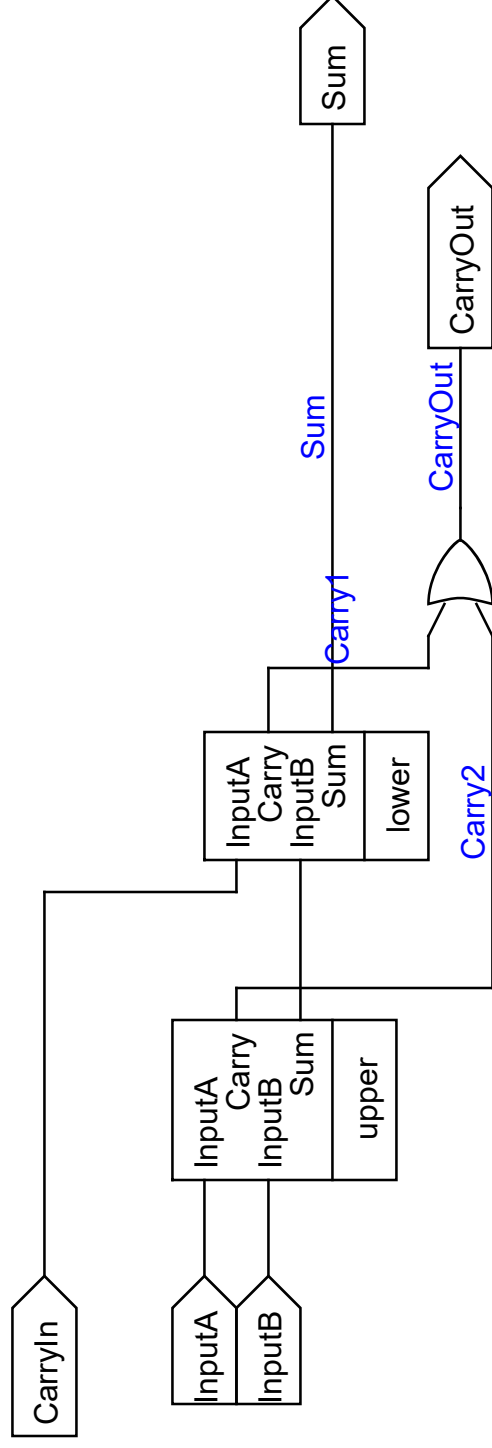
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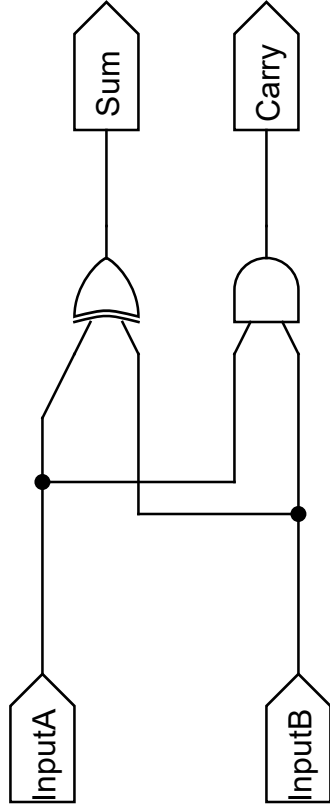
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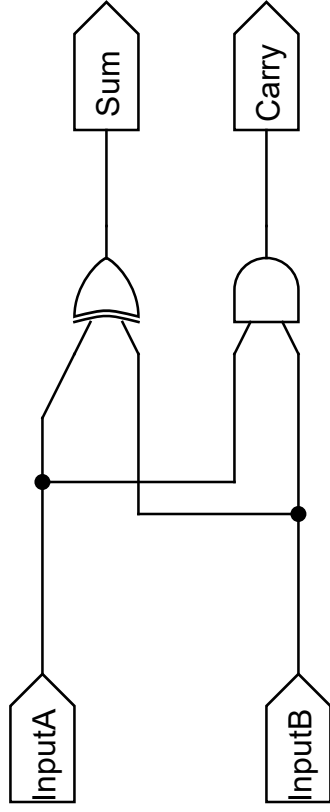
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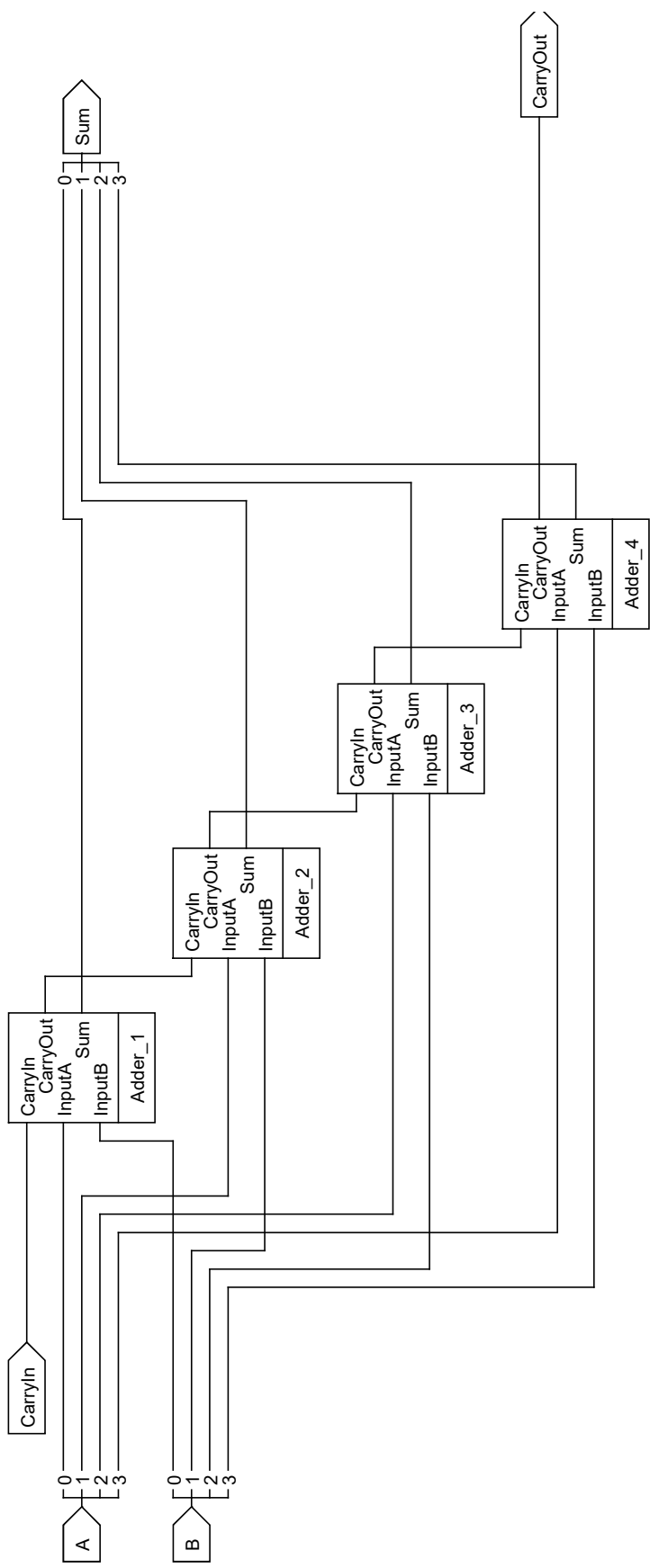
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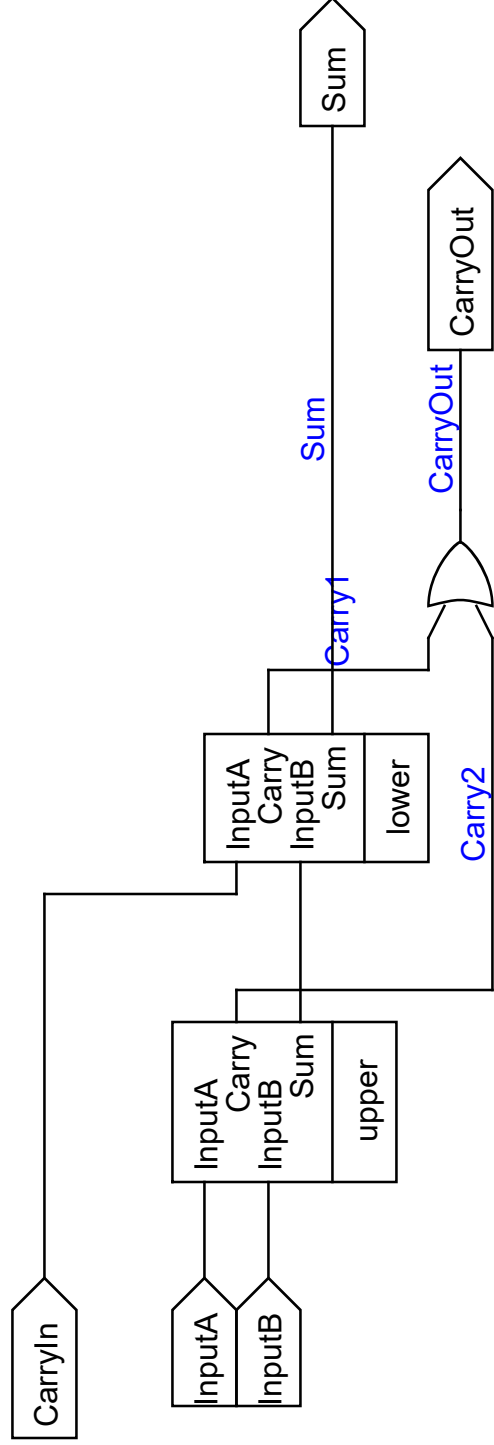
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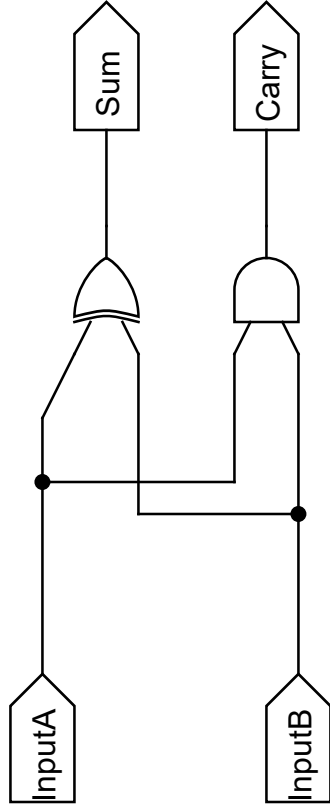
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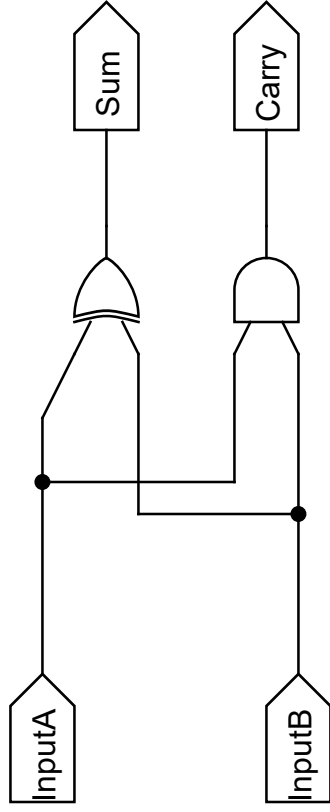
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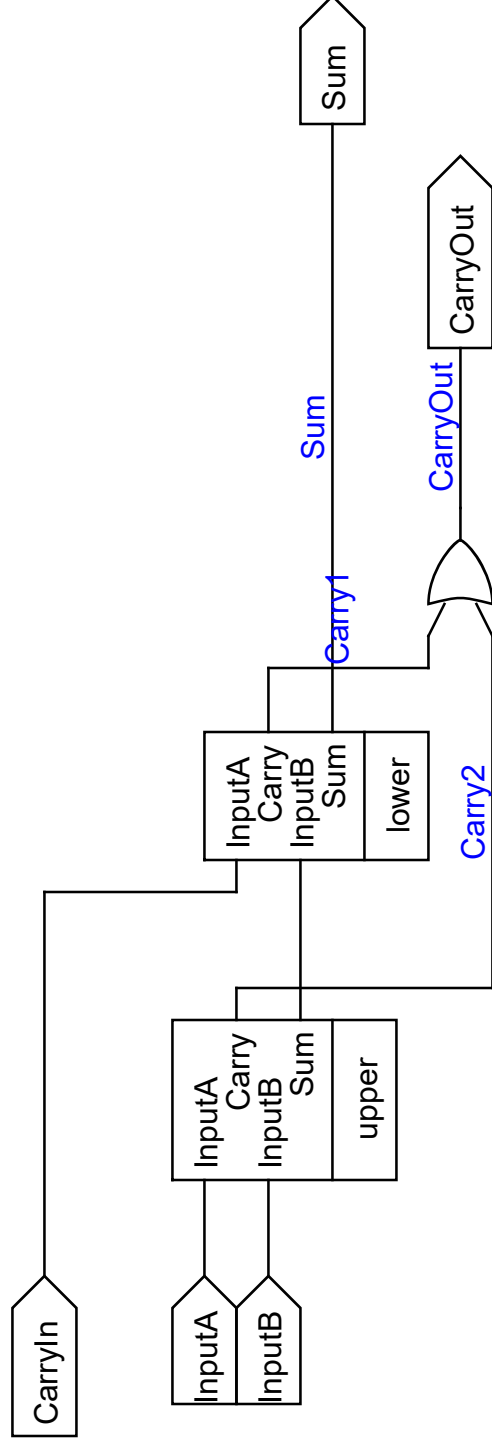
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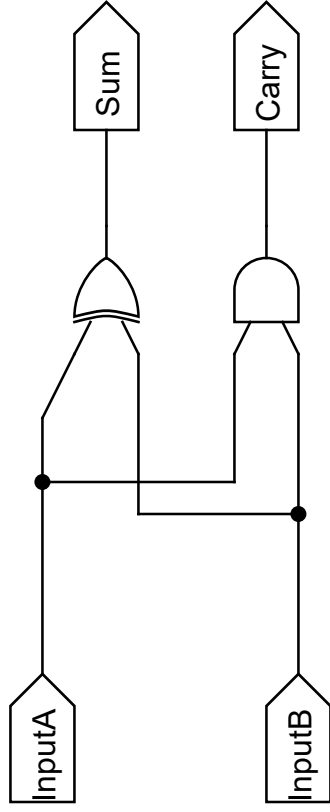
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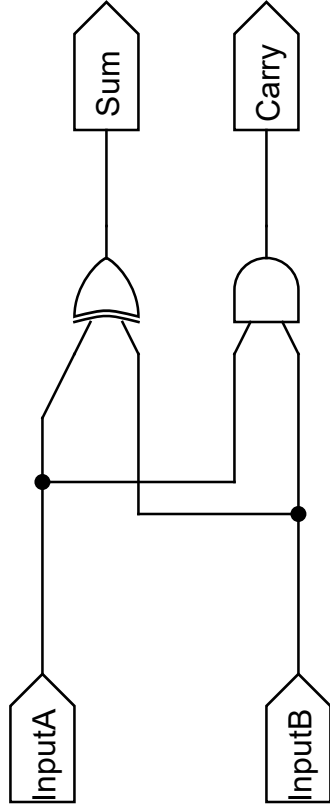
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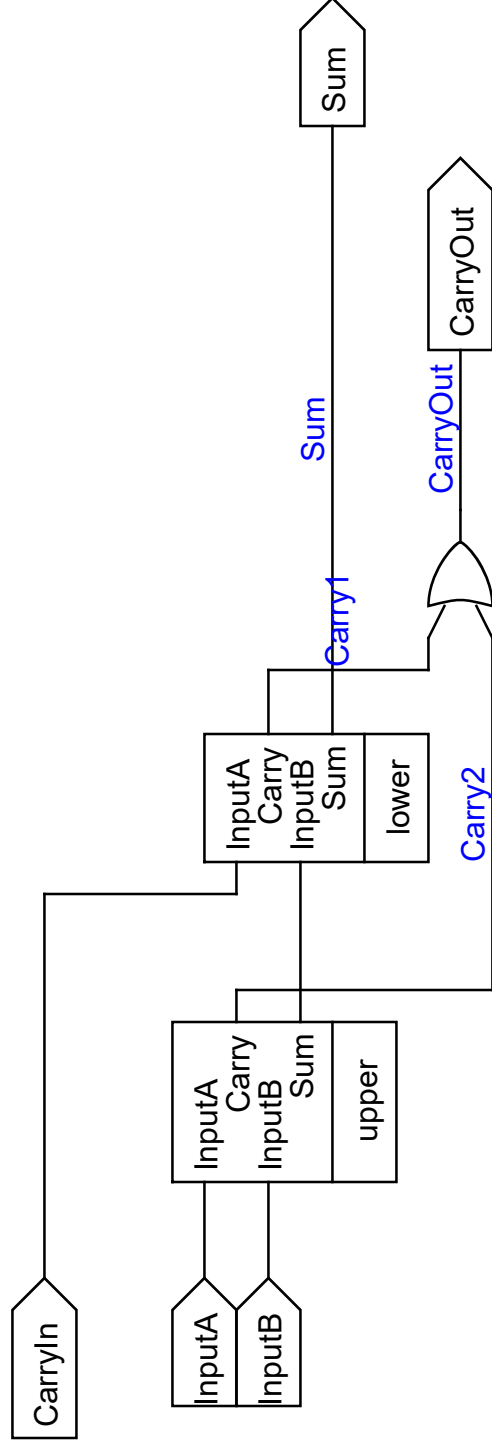
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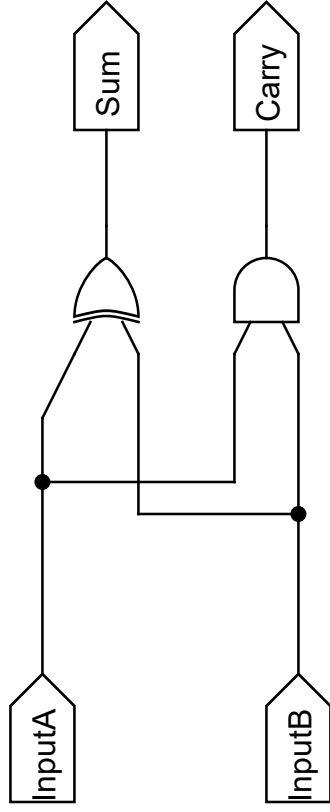
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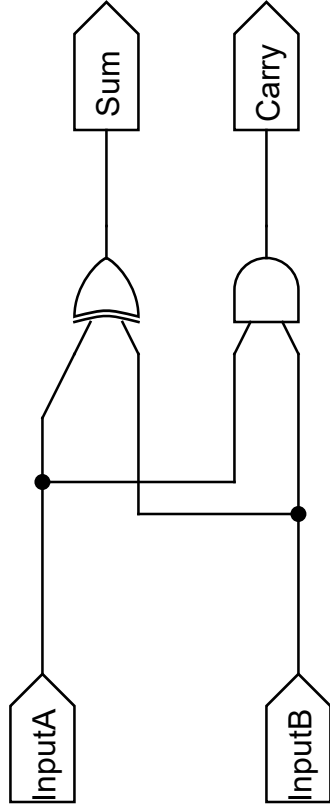
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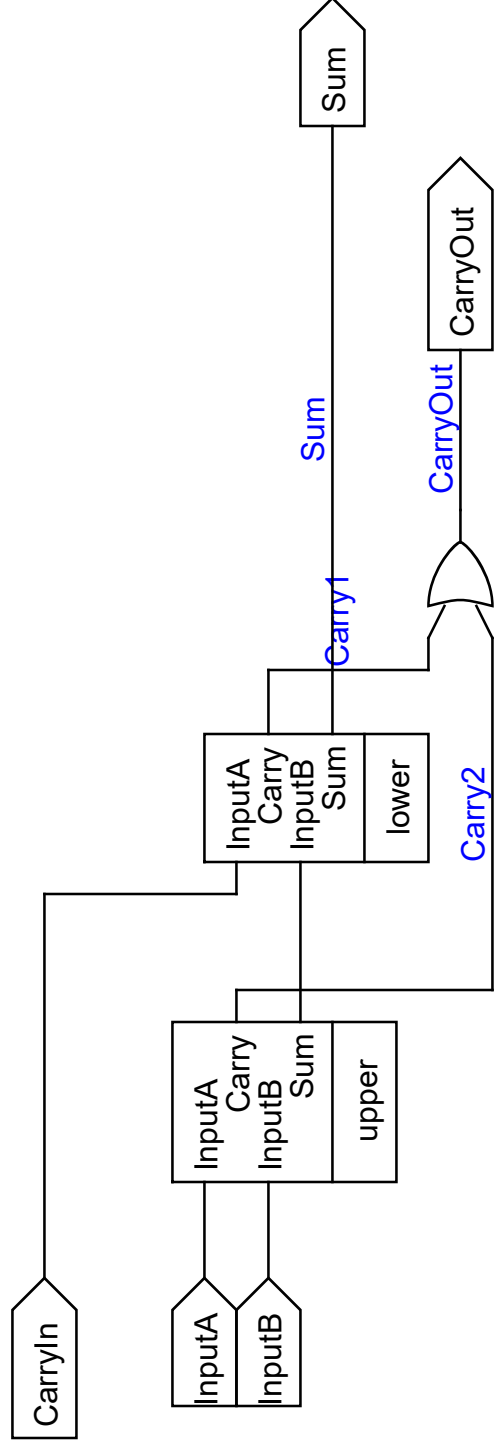
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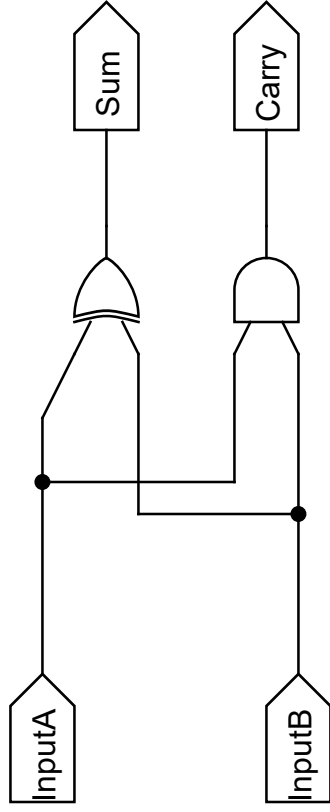
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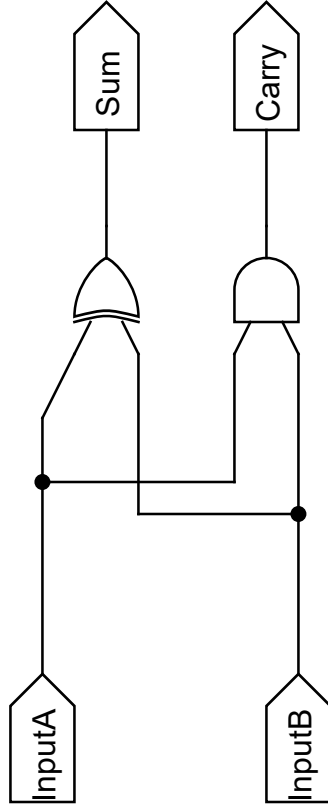
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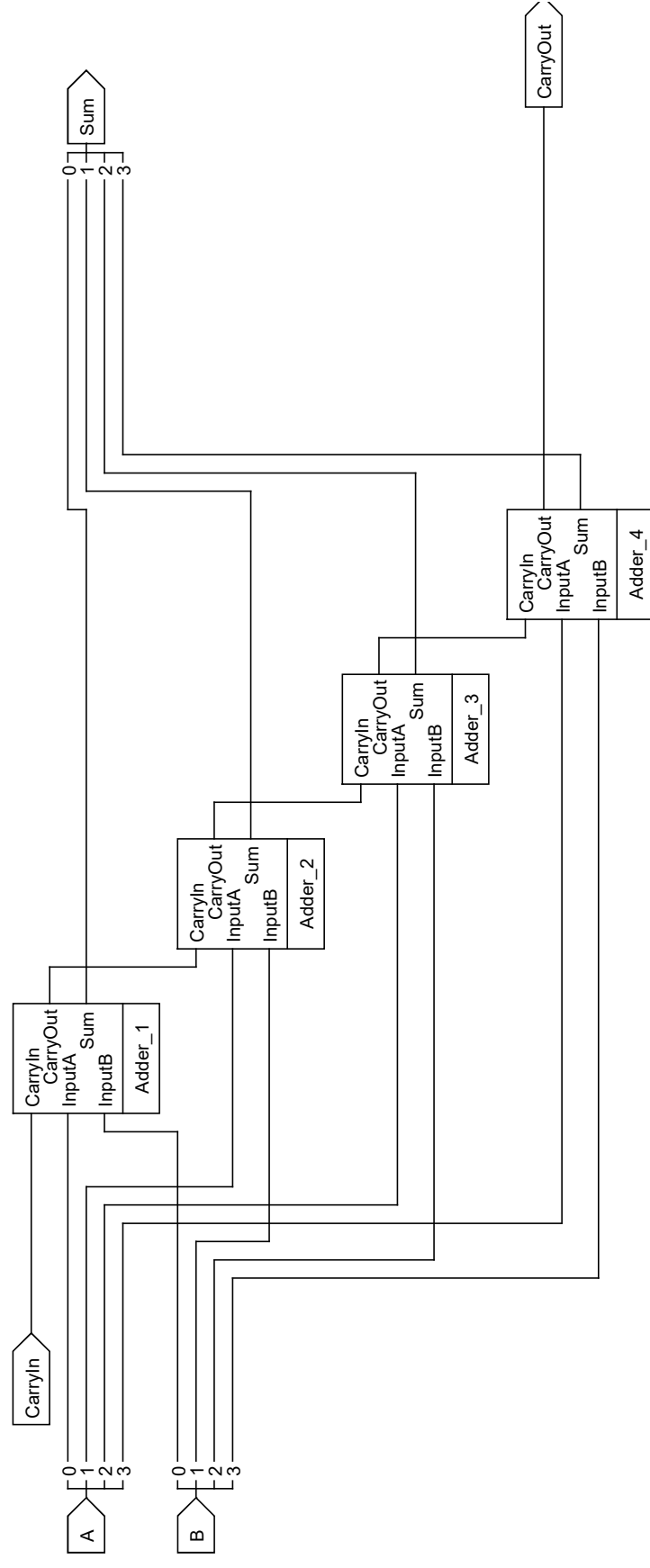
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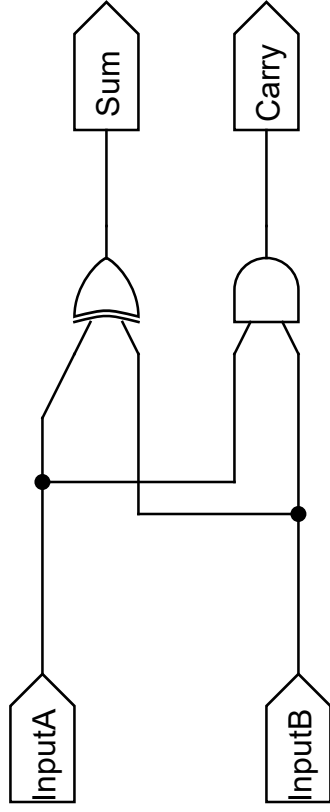
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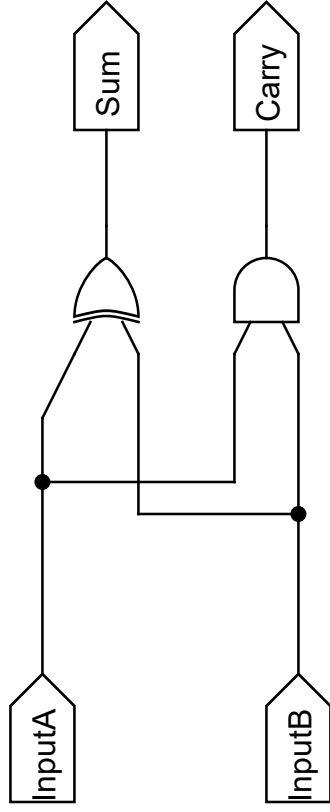
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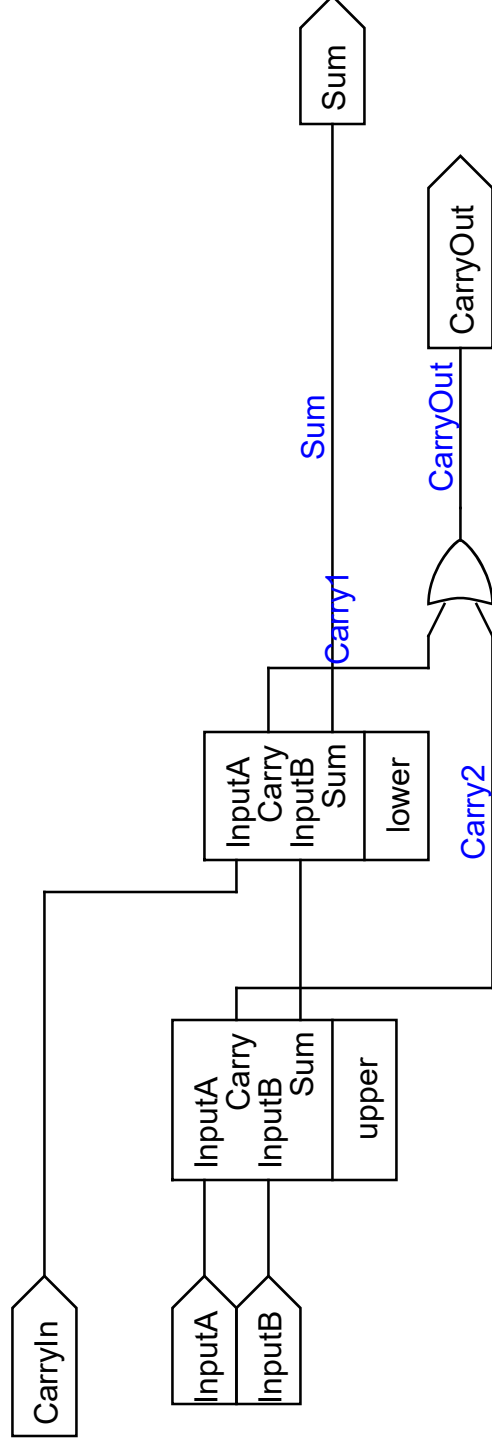
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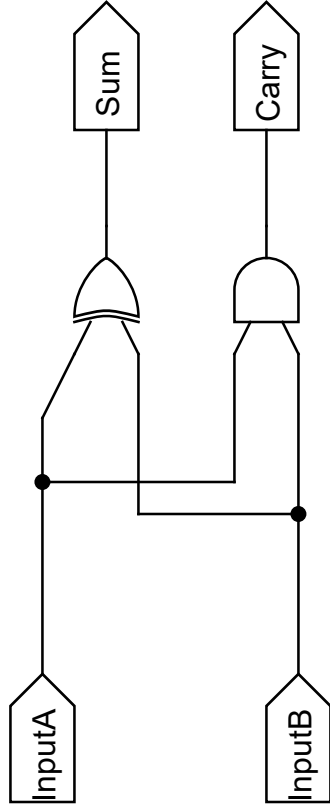
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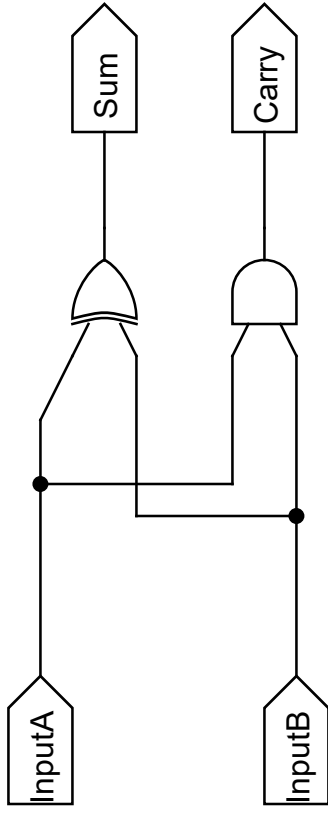
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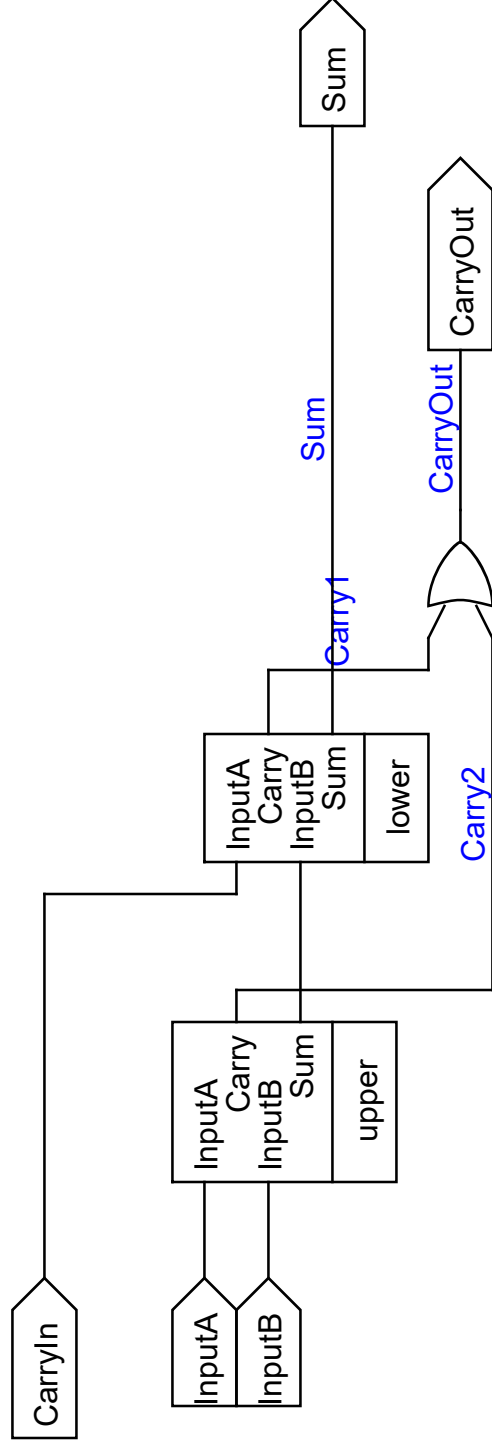
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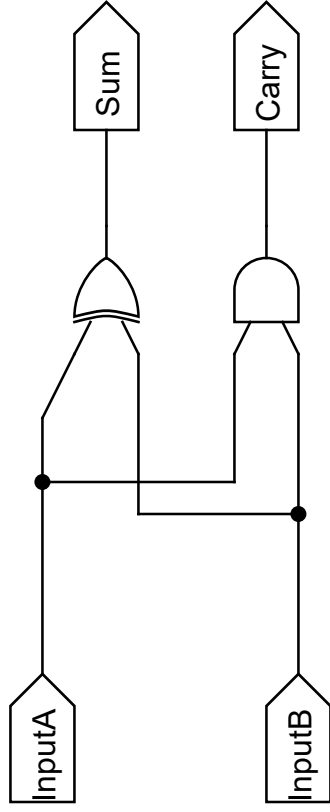
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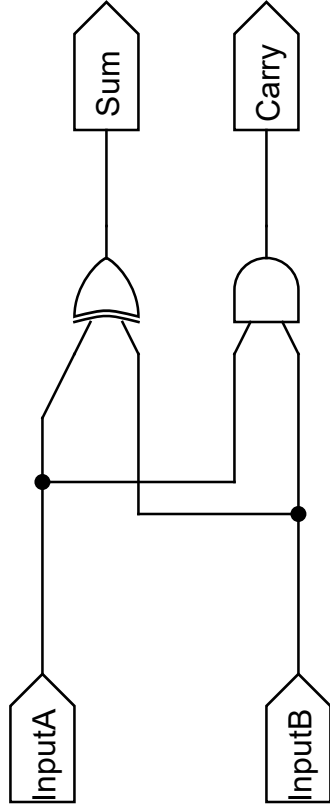
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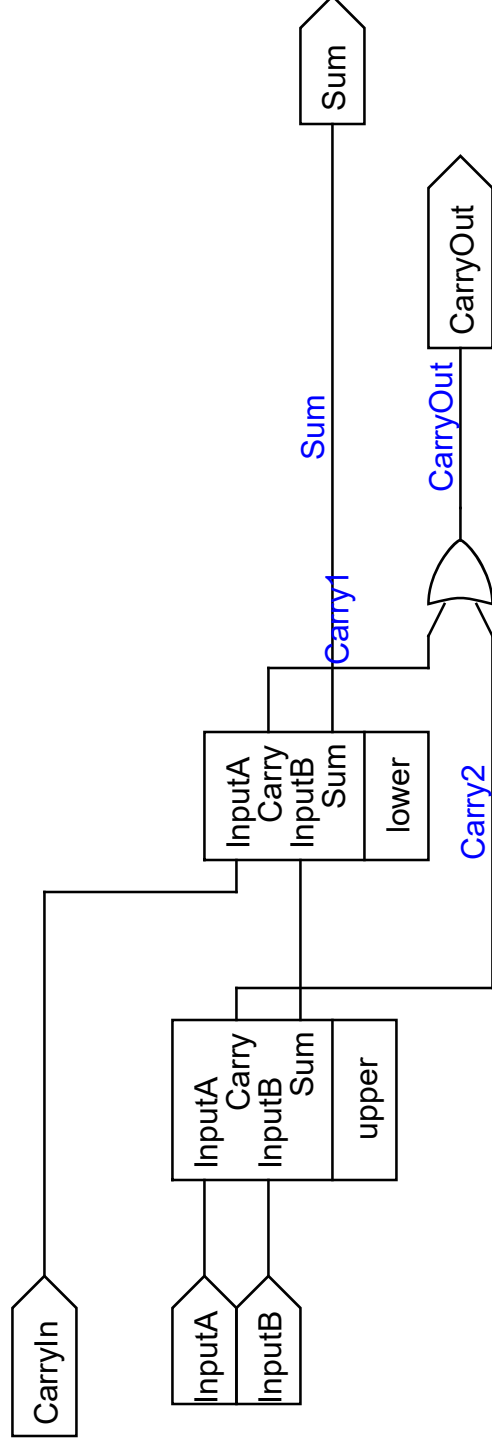
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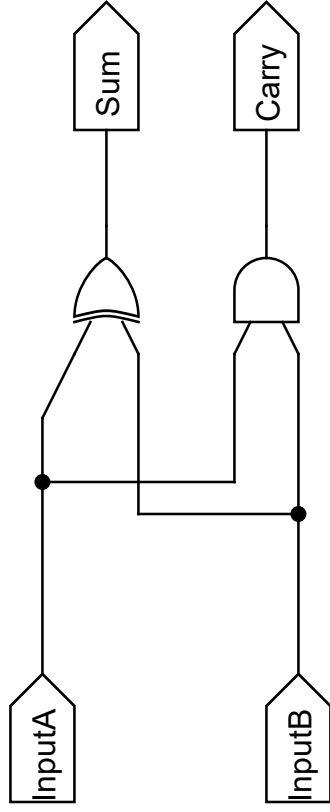
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Adder_4 in RipCarry4 in Adder in Subtractor in Comparator in ALU_16bit_starter



lower in Adder_4 in RipCarry4 in Adder in Subtractor in Comparator in ALU_16bit_starter



upper in Adder_4 in RipCarry4 in Adder in Subtractor in Comparator in ALU_16bit_starter

