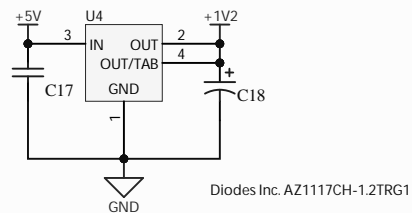
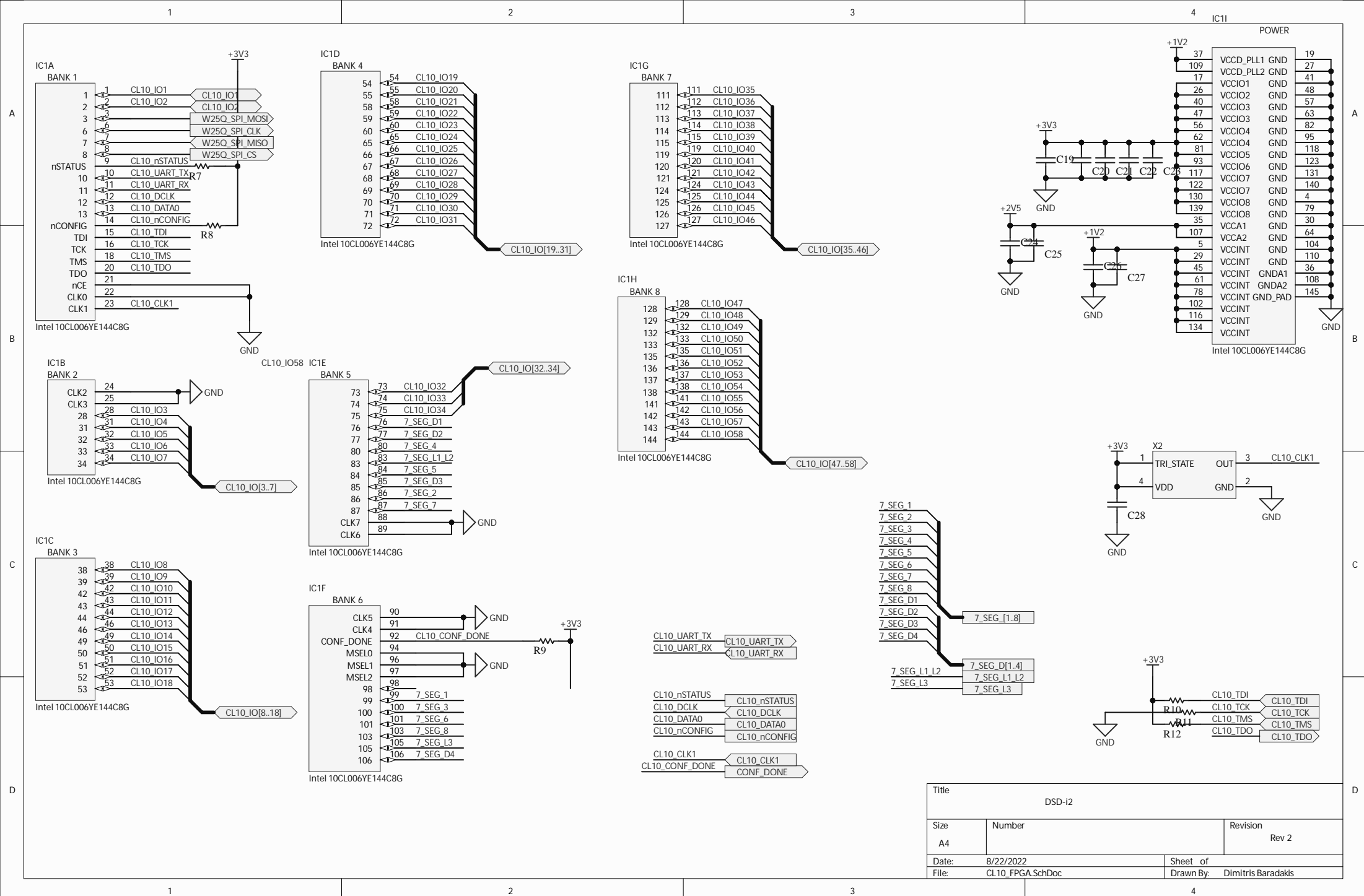


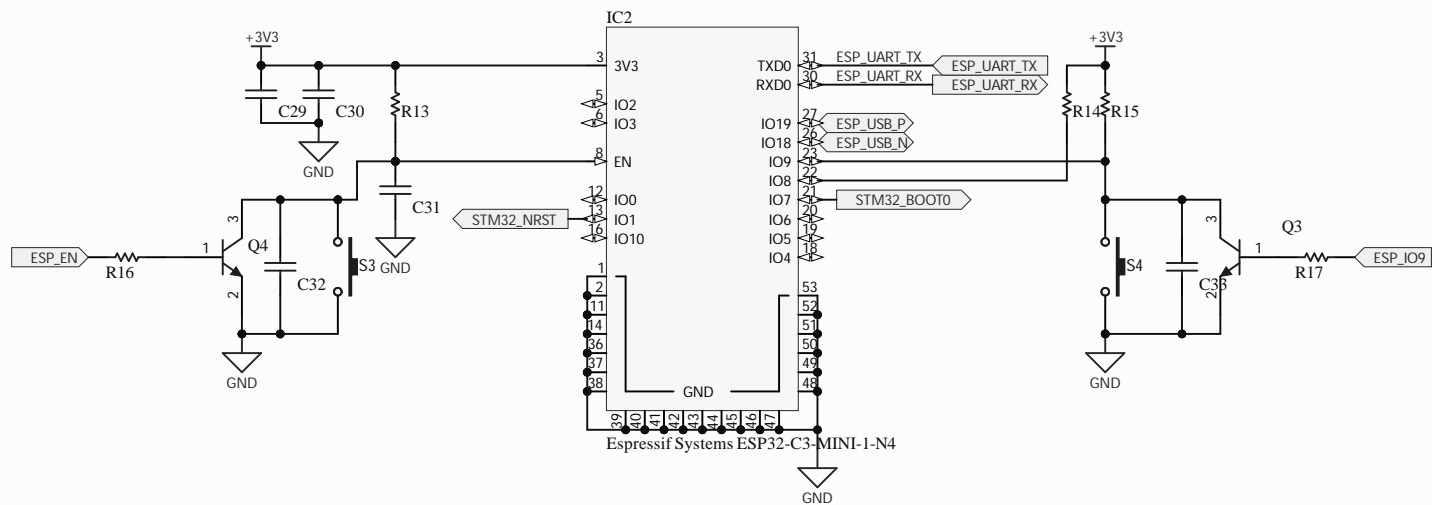
Selected LDOs are usable  
with low ESR Caps



Title			
DSD-i2			
Size	Number		Revision
A4			Rev 2
Date:	8/22/2022		Sheet of
File:	Power_Supply.SchDoc		Drawn By: Dimitris Baradakis







Title		
DSD-i2		
Size	Number	Revision
A4		Rev 2
Date:	8/22/2022	Sheet of
File:	ESP32-C3-Mini.SchDoc	Drawn By: Dimitris Baradakis

A

B

C

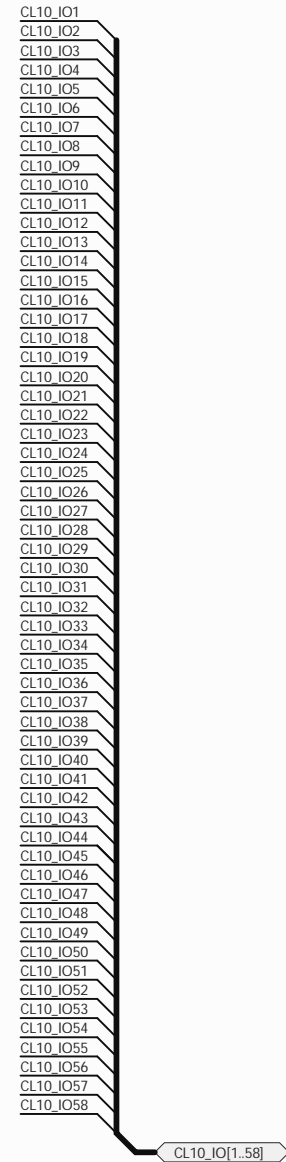
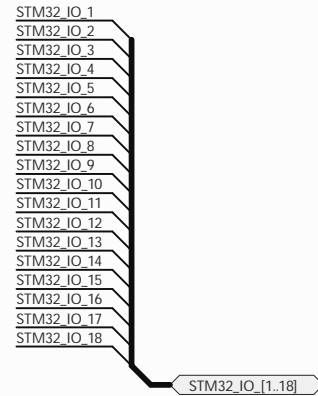
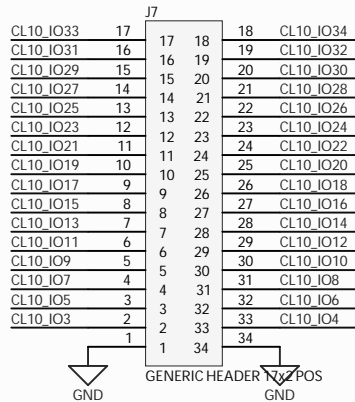
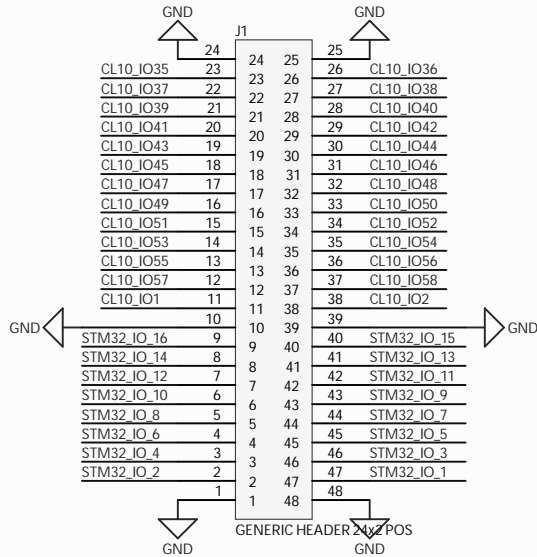
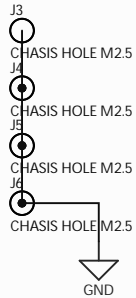
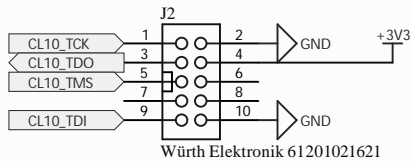
D

A

B

C

D



Title		
DSD-I2		
Size	Number	Revision
A4		Rev 2
Date:	8/22/2022	Sheet of
File:	Headers.SchDoc	Drawn By: Dimitris Baradakis

A

B

C

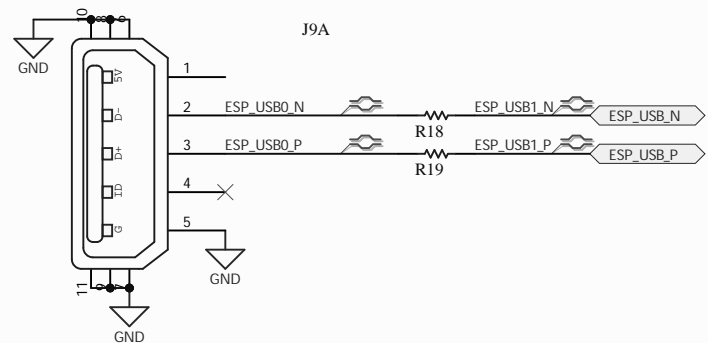
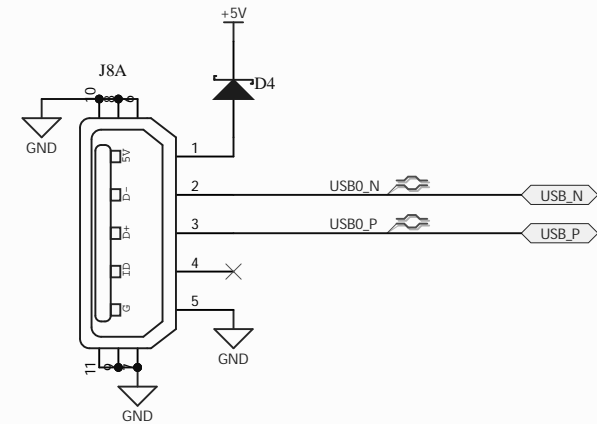
D

A

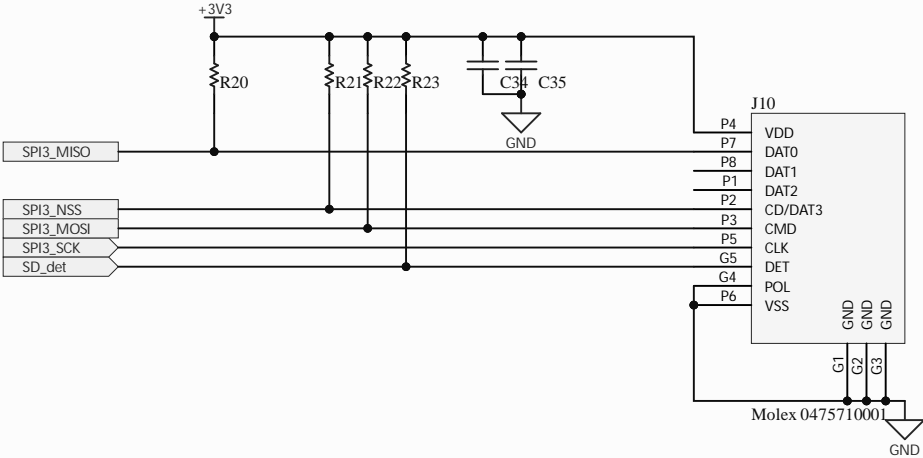
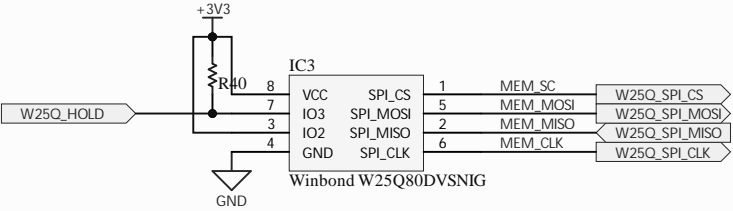
B

C

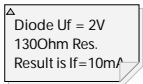
D



Title			
DSD-i2			
Size	Number		Revision
A4			Rev 2
Date:	8/22/2022		Sheet of
File:	USB.SchDoc		Drawn By: Dimitris Baradakis



Title			
DSD-I2			
Size	Number		Revision
A4			Rev 2
Date:	8/22/2022	Sheet of	
File:	Memory.SchDoc	Drawn By:	Dimitris Baradakis



Title				DSD-I2			
Size		Number				Revision	
A4						Rev 2	
Date:		8/22/2022		Sheet of			
File:		7SEG.SchDoc		Drawn By:		Dimitris Baradakis	