James Glanville

Flat 4, 174 Hills Road

Cambridge Cambridgeshire CB2 8PQ

OBJECTIVE Graduate Design Engineer

EDUCATION Cambridge University

Masters of Engineering, 2010-2014 Specialising in Electronic Engineering Class 2.II, MASTERS GRADE

Haberdashers' Aske's Boys School

2003-2010

A levels	${f Subject}$	\mathbf{Grade}
	Maths	A
	Further Maths	A^*
	Physics	A^*
	Chemistry	A

GCSEs	Subject	\mathbf{Grade}
	Maths	A*
	Physics	A*
	Chemistry	A^*
	Biology	A^*
	English	A
	English Literature	A
	French	A^*
	History	A^*
	Geography	A^*
	Ceramics	A^*

SKILLS

Programming

- Experience in C programming, particularly Windows driver development.
- Some experience with VHDL and hardware design.

Other

• Full clean UK driving licence

EXPERIENCE

Imagination Technologies

INSERT MULTIPLE DATES

and more dates blah

Work experience, driver debugging and build maintenance

Centre for Advanced Photonics and Electronics DATE

Wrote stability measurement control software for semiconductor testing which was later used for a research publication (A J Flewitt et al 2009 Semicond. Sci. Technol. 24 085002).

HOBBIES

Programming, Electronics, CNC/3d printing