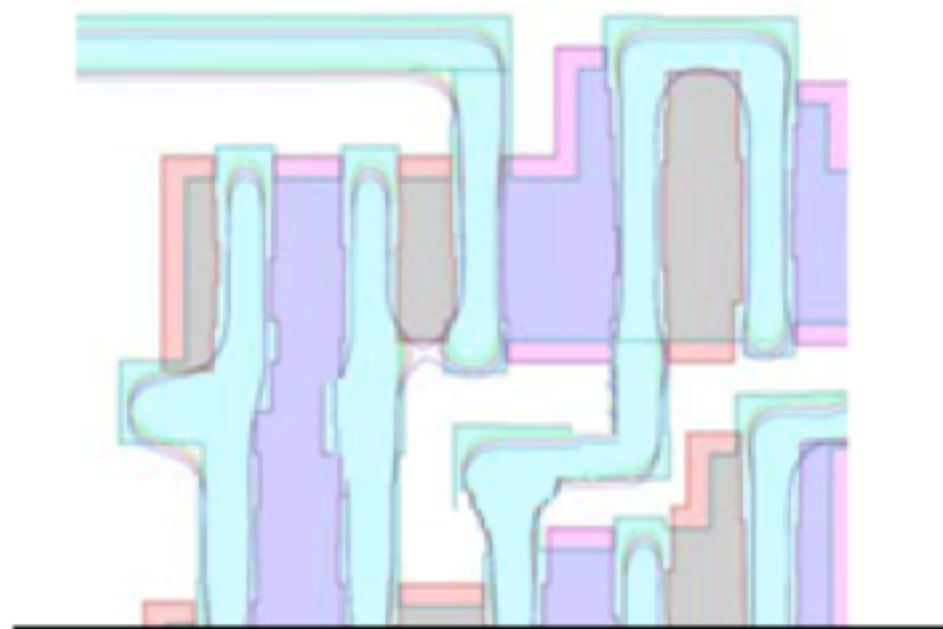
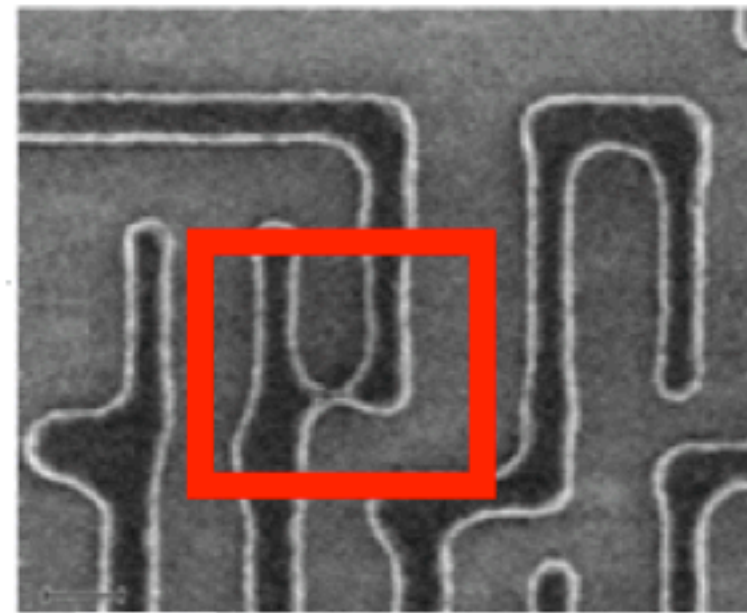


Pre-OPC Layout



Post-OPC Mask



Hotspot on Wafer