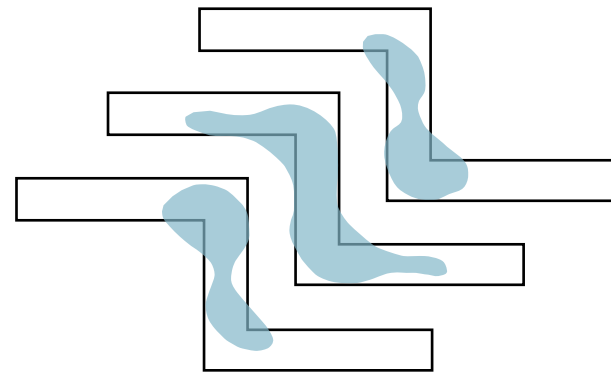
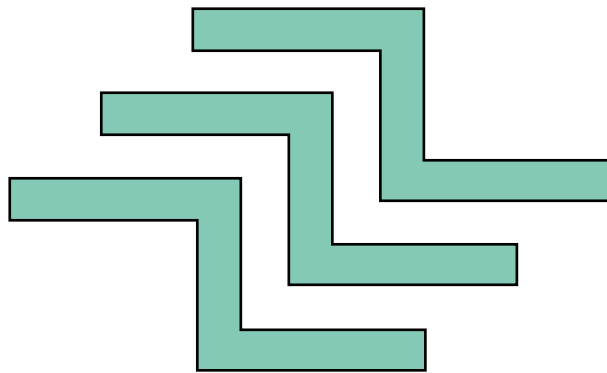
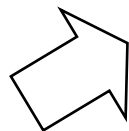


Design target

Mask

Wafer

without OPC



with OPC

