```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                        2 ************************************************************************
                                        3 *
                                        4 *            TRTE instruction tests
                                        5 *
                                        6 *        NOTE: This test is based the CLCL-et-al Test
                                        7 *              modified to only test the TRTE instruction.
                                        8 *
                                        9 *        James Wekel August 2022
                                       10 ************************************************************************



                                       12 ************************************************************************
                                       13 *
                                       14 *            TRTE basic instruction tests
                                       15 *
                                       16 ************************************************************
                                       17 *  This program tests proper functioning of the TRTE
                                       18 *  instructions. Specification exceptions are not tested.
                                       19 *
                                       20 *  PLEASE NOTE that the tests are very SIMPLE TESTS designed to catch
                                       21 *  obvious coding errors.  None of the tests are thorough.  They are
                                       22 *  NOT designed to test all aspects of any of the instructions.
                                       23 *
                                       24 ************************************************************************
                                       25 *
                                       26 *  Example Hercules Testcase:
                                       27 *
                                       28 *
                                       29 *     *Testcase TRTE-01-basic (Test TRTE instructions)
                                       30 *
                                       31 *     archlvl     390
                                       32 *     mainsize    3
                                       33 *     numcpu      1
                                       34 *     sysclear
                                       35 *
                                       36 *     loadcore    "$(testpath)/TRTE-01-basic" 0x0
                                       37 *
                                       38 *     runtest     1
                                       39 *
                                       40 *     *Done
                                       41 *
                                       42 *
                                       43 ************************************************************************
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                       45          PRINT OFF
                                     3426          PRINT ON

                                     3428 ********************************************************************************
                                     3429 *        SATK prolog stuff...
                                     3430 ********************************************************************************

                                     3432          ARCHLVL  SET=2,ZARCH=NO,MNOTE=NO
                                     3434+$AL      OPSYN AL
                                     3435+$ALR     OPSYN ALR
                                     3436+$B       OPSYN B
                                     3437+$BAS     OPSYN BAS
                                     3438+$BASR    OPSYN BASR
                                     3439+$BC      OPSYN BC
                                     3440+$BCTR    OPSYN BCTR
                                     3441+$BE      OPSYN BE
                                     3442+$BH      OPSYN BH
                                     3443+$BL      OPSYN BL
                                     3444+$BM      OPSYN BM
                                     3445+$BNE     OPSYN BNE
                                     3446+$BNH     OPSYN BNH
                                     3447+$BNL     OPSYN BNL
                                     3448+$BNM     OPSYN BNM
                                     3449+$BNO     OPSYN BNO
                                     3450+$BNP     OPSYN BNP
                                     3451+$BNZ     OPSYN BNZ
                                     3452+$BO      OPSYN BO
                                     3453+$BP      OPSYN BP
                                     3454+$BXLE    OPSYN BXLE
                                     3455+$BZ      OPSYN BZ
                                     3456+$CH      OPSYN CH
                                     3457+$L       OPSYN L
                                     3458+$LH      OPSYN LH
                                     3459+$LM      OPSYN LM
                                     3460+$LPSW    OPSYN LPSW
                                     3461+$LR      OPSYN LR
                                     3462+$LTR     OPSYN LTR
                                     3463+$NR      OPSYN NR
                                     3464+$SL      OPSYN SL
                                     3465+$SLR     OPSYN SLR
                                     3466+$SR      OPSYN SR
                                     3467+$ST      OPSYN ST
                                     3468+$STM     OPSYN STM
                                     3469+$X       OPSYN X
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                      3471 ********************************************************************
                                      3472 *        Initiate the TRTE1TST CSECT in the CODE region
                                      3473 *        with the location counter at 0
                                      3474 ********************************************************************

                                      3476 TRTE1TST ASALOAD  REGION=CODE
                           000000  0E3F4D  3477+TRTE1TST START 0,CODE
000000  000A0000 00000008           000000  3479+         PSW   0,0,2,0,X'008'      64-bit Restart ISR Trap New PSW
000008                      000008  000058  3480+         ORG   TRTE1TST+X'058'
000058  000A0000 00000018           000058  3482+         PSW   0,0,2,0,X'018'      64-bit External ISR Trap New PSW
000060  000A0000 00000020           000060  3483+         PSW   0,0,2,0,X'020'      64-bit Supervisor Call ISR Trap New PSW
000068  000A0000 00000028           000068  3484+         PSW   0,0,2,0,X'028'      64-bit Program ISR Trap New PSW
000070  000A0000 00000030           000070  3485+         PSW   0,0,2,0,X'030'      64-bit Machine Check Trap New PSW
000078  000A0000 00000038           000078  3486+         PSW   0,0,2,0,X'038'      64-bit Input/Output Trap New PSW
000080                      000080  000200  3487+         ORG   TRTE1TST+512



                                      3489 ********************************************************************
                                      3490 *        Create IPL (restart) PSW
                                      3491 ********************************************************************

                                      3493         ASAIPL   IA=BEGIN
                           000000  0E3F4D  3494+TRTE1TST CSECT
000200                     000200  000000  3495+         ORG   TRTE1TST
000000  00080000 00000200  000000  000200  3496+         PSW   0,0,0,0,BEGIN,24
000008                      000008  000200  3497+         ORG   TRTE1TST+512     Reset CSECT to end of assigned storage area
                            000000  0E3F4D  3498+TRTE1TST CSECT
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                      3500 ************************************************************************
                                      3501 *               The actual "TRTE1TST" program itself...
                                      3502 ************************************************************************
                                      3503 *
                                      3504 *  Architecture Mode: 370
                                      3505 *  Register Usage:
                                      3506 *
                                      3507 *   R0       (work)
                                      3508 *   R1       TRTE - Function-Code Table Address
                                      3509 *   R2       TRTE - First-Operand Address
                                      3510 *   R3       TRTE - First-Operand Length
                                      3511 *   R4       TRTE - Function-Code
                                      3512 *   R5       Testing control table - base current entry
                                      3513 *   R6-R7    (work)
                                      3514 *   R8       First base register
                                      3515 *   R9       Second base register
                                      3516 *   R10-R13  (work)
                                      3517 *   R14      Subroutine call
                                      3518 *   R15      Secondary Subroutine call or work
                                      3519 *
                                      3520 ************************************************************************

000200                      000000    3522          USING  ASA,R0          Low core addressability
000200                      000200    3523          USING  BEGIN,R8        FIRST Base Register
000200                      001200    3524          USING  BEGIN+4096,R9   SECOND Base Register

000200  0580                         3526 BEGIN    BALR   R8,0            Initalize FIRST base register
000202  0680                         3527          BCTR   R8,0            Initalize FIRST base register
000204  0680                         3528          BCTR   R8,0            Initalize FIRST base register

000206  4190 8800           000800   3530          LA     R9,2048(,R8)    Initalize SECOND base register
00020A  4190 9800           000800   3531          LA     R9,2048(,R9)    Initalize SECOND base register
                                      3532 *
                                      3533 **       Run the tests...
                                      3534 *
00020E  45E0 8302           000502   3535          BAL    R14,TEST01      Test TRTE   instruction
                                      3536 *
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3538 ************************************************************************
                                     3539 *         Test for normal or unexpected test completion...
                                     3540 ************************************************************************

000212  95FC 8200                   000400  3542          CLI   TESTNUM,X'FC'    Did we end on expected test?
000216  4770 83D8                   0005D8  3543          BNE   FAILTEST          No?! Then FAIL the test!

00021A  9503 8201                   000401  3545          CLI   SUBTEST,X'03'    Did we end on expected SUB-test?
00021E  4770 83D8                   0005D8  3546          BNE   FAILTEST          No?! Then FAIL the test!

000222  47F0 83C8                   0005C8  3548          B     EOJ               Yes, then normal completion!




                                     3550 ************************************************************************
                                     3551 *        Fixed test storage locations ...
                                     3552 ************************************************************************



000226                      000226  000400  3554          ORG   BEGIN+X'200'
                                     3555
000400                               3556 TESTADDR DS    0D                 Where test/subtest numbers will go
000400  99                           3557 TESTNUM  DC    X'99'      Test number of active test
000401  99                           3558 SUBTEST  DC    X'99'      Active test sub-test number

000402                      000402  000502  3560          ORG   *+X'100'
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3562 ************************************************************************
                                     3563 *        TEST01                    Test TRTE instruction
                                     3564 ************************************************************************

000502  9201 8200                  000400  3566 TEST01   MVI   TESTNUM,X'01'
                                     3567
000506  4150 83EC                  0005EC  3568          LA    R5,TRTECTL          Point R5 --> testing control table
00050A                      000000       3569          USING TRTETEST,R5         What each table entry looks like
                                     3570
                            00050A  000001  3571 TST1LOOP EQU   *
00050A  4360 5000                  000000  3572          IC    R6,TNUM             Set test number
00050E  4260 8200                  000400  3573          STC   R6,TESTNUM
                                     3574 *
                                     3575 **       Initialize operand data  (move data to testing address)
                                     3576 *
000512  58A0 5018                  000018  3577          L     R10,OP1WHERE          Where to move operand-1 data to
000516  58B0 5008                  000008  3578          L     R11,OP1LEN            operand-1 length
00051A  50B0 501C                  00001C  3579          ST    R11,OP1WLEN             and save for later
00051E  5860 5004                  000004  3580          L     R6,OP1DATA            Where op1 data is right now
000522  5870 5008                  000008  3581          L     R7,OP1LEN             How much of it there is
000526  0EA6                             3582          MVCL  R10,R6
                                     3583 *
000528  58A0 5014                  000014  3584          L     R10,OP2WHERE          Where to move operand-2 data to
00052C  58B0 5010                  000010  3585          L     R11,OP2LEN            How much of it there is
000530  5860 500C                  00000C  3586          L     R6,OP2DATA            Where op2 data is right now
000534  5870 5010                  000010  3587          L     R7,OP2LEN              How much of it there is
000538  0EA6                             3588          MVCL  R10,R6


                                     3590 **       Execute TRTE instruction and check for expected condition code

00053A  9814 5014                  000014  3592          LM    R1,R4,OPSWHERE     get TRTE input
                                     3593
00053E  1B77                             3594          SR    R7,R7              get M3 bits for TRTE
000540  4370 5003                  000003  3595          IC    R7,M3              (M3)
000544  4270 8356                  000556  3596          STC   R7,TRTEMOD+2       DYNAMICALLY MODIFIED CODE
                                     3597
000548  58B0 5024                  000024  3598          L     R11,FAILMASK       (failure CC)
00054C  89B0 0004                  000004  3599          SLL   R11,4              (shift to BC instr CC position)
                                     3600
000550  9200 8201                  000401  3601          MVI   SUBTEST,X'00'      (primary TRT)
000554  B9BF 0024                  000024  3602 TRTEMOD  TRTE  R2,R4,0             Start with TRTE and m3=0
                                     3603
000558  9014 83A8                  0005A8  3604          STM   R1,R4,SAVETRT      (save R1/R4 results)
00055C  44B0 839E                  00059E  3605          EX    R11,TRTEBC          fail if...
000560  4710 8354                  000554  3606          BC    B'0001',TRTEMOD     cc=3, not finished
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3608 **       Verify R2,R3,R4 contain (or still contain!) expected values
                                     3609
000564  98AC 5028                    000028  3610          LM    R10,R12,ENDREGS
                                     3611
000568  9201 8201                    000401  3612          MVI   SUBTEST,X'01'      (R2 result - op1 found addr)
00056C  152A                         000598  3613          CLR   R2,R10              R2 correct?
00056E  4770 8398                    000598  3614          BNE   TRTEFAIL           No, FAILTEST!
                                     3615
000572  9202 8201                    000401  3616          MVI   SUBTEST,X'02'      (R3 result - op1 remaining len)
000576  153B                         000598  3617          CLR   R3,R11              R3 correct
000578  4770 8398                    000598  3618          BNE   TRTEFAIL           No, FAILTEST!
                                     3619
00057C  9203 8201                    000401  3620          MVI   SUBTEST,X'03'      (R4 result - FC code)
000580  154C                         000598  3621          CLR   R4,R12              R4 correct
000582  4770 8398                    000598  3622          BNE   TRTEFAIL           No, FAILTEST!
                                     3623
000586  4150 5034                    000034  3624          LA    R5,TRTENEXT        Go on to next table entry
00058A  D503 83E8 5000       0005E8  000000  3625          CLC   =F'0',0(R5)        End of table?
000590  4770 830A                    00050A  3626          BNE   TST1LOOP           No, loop...
000594  47F0 839C                    00059C  3627          B     TRTEDONE            Done! (success!)


000598  41E0 83D8                    0005D8  3629 TRTEFAIL LA    R14,FAILTEST       Unexpected results!
00059C  07FE                         000598  3630 TRTEDONE BR    R14                Return to caller or FAILTEST


00059E  4700 8398                    000598  3632 TRTEBC   BC    0,TRTEFAIL         (fail if unexpected condition code)


0005A8  00000000 00000000                    3634 SAVETRT  DC    4D'0'              (saved R1/R4 from TRT results)


0005C8                               000200  3636          DROP  R5
0005C8                               000200  3637          DROP  R15
0005C8                               000200  3638          USING BEGIN,R8
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             3640 ************************************************************************
                                             3641 *        Normal completion or Abnormal termination PSWs
                                             3642 ************************************************************************




                                             3644 EOJ      DWAITEND LOAD=YES          Normal completion
0005C8                                       3646+EOJ      DS    0H
0005C8  8200 83D0                 0005D0     3647+         LPSW  DWAT0008
0005D0  000A0000 00000000                    3648+DWAT0008 PSW   0,0,2,0,X'000000'




                                             3650 FAILTEST DWAIT LOAD=YES,CODE=BAD    Abnormal termination
0005D8                                       3651+FAILTEST DS    0H
0005D8  8200 83E0                 0005E0     3652+         LPSW  DWAT0009
0005E0  000A0000 00010BAD                    3653+DWAT0009 PSW   0,0,2,0,X'010BAD'



                                             3655 ************************************************************************
                                             3656 *        Working Storage
                                             3657 ************************************************************************


0005E8                                       3659          LTORG ,                    Literals pool
0005E8  00000000                             3660                =F'0'

                          000400  000001     3662 K        EQU   1024                 One KB
                          001000  000001     3663 PAGE     EQU   (4*K)                Size of one page
                          010000  000001     3664 K64      EQU   (64*K)               64 KB
                          100000  000001     3665 MB       EQU   (K*K)                 1 MB
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                            000000  0E3F4D  3667 TRTE1TST CSECT ,

                                            3669 ********************************************************************
                                            3670 *        TRTETEST DSECT
                                            3671 ********************************************************************

                                            3673 TRTETEST DSECT ,
000000  00                                  3674 TNUM     DC    X'00'          TRTE table Number
000001  00                                  3675          DC    X'00'
000002  00                                  3676          DC    X'00'
000003  00                                  3677 M3       DC    X'00'          M3 byte stored into TRTE instruction

000004  00000000                            3679 OP1DATA  DC    A(0)           Pointer to Operand-1 data
000008  00000000                            3680 OP1LEN   DC    F'0'           How much data is there - 1
00000C  00000000                            3681 OP2DATA  DC    A(0)           Pointer to FC table data
000010  00000000                            3682 OP2LEN   DC    F'0'           How much data is there - FC Table

                            000014  000001  3684 OPSWHERE EQU   *
000014  00000000                            3685 OP2WHERE DC    A(0)           Where FC Table  data should be placed
000018  00000000                            3686 OP1WHERE DC    A(0)           Where Operand-1 data should be placed
00001C  00000000                            3687 OP1WLEN  DC    F'0'           How much data is there - 1
000020  00000000                            3688          DC    A(0)           pollute - found FC

000024  00000000                            3690 FAILMASK DC    A(0)           Failure Branch on Condition mask

                                            3692 *                             Ending register values
000028  00000000                            3693 ENDREGS  DC    A(0)              Operand 1 address
00002C  00000000                            3694          DC    A(0)              Operand 1 length
000030  00000000                            3695          DC    A(0)              Function Code

                        000034  000001  3697 TRTENEXT EQU   *                  Start of next table entry...

                            BBCCDD  000001  3699 REG2PATT EQU   X'AABBCCDD'    Polluted Register pattern
                            0000DD  000001  3700 REG2LOW  EQU         X'DD'    (last byte above)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                            000000  0E3F4D  3702 TRTE1TST CSECT ,


                                            3704 ********************************************************************
                                            3705 *        TRTE Testing Control tables   (ref: TRTETEST DSECT)
                                            3706 ********************************************************************
                                            3707          PRINT DATA
0005EC                                      3708 TRTECTL  DC    0A(0)    start of table


                                            3710 ********************************************************************
                                            3711 *        tests with   M3: A=0,F=0,L=0, reserved=0    (0)
                                            3712 *                            FC Table = 1 byte
                                            3713 ********************************************************************



0005EC                                      3715 M0T1     DS    0F
0005EC  01                                  3716          DC    X'01'                       Test Num
0005ED  0000                                3717          DC    X'00',X'00'
0005EF  00                                  3718          DC    X'00'                       M3: A=0,F=0,L=0
0005F0  0000142C 00000001                   3719          DC    A(TRTOP10),A(001)           Source - Op 1 & length
0005F8  0000312C 00000100                   3720          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                            3721 *                                          Target -
000600  00110000 00210000                   3722          DC    A(1*MB+(1*K64)),A(2*MB+(1*K64)),A(0)  FC, Op1, Op1L
000608  00000000
00060C  AABBCCDD                            3723          DC    A(REG2PATT)
000610  00000007                            3724          DC    A(7) CC0
000614  00210001 00000000                   3725          DC    A(2*MB+(1*K64)+001),A(000),A(0)
00061C  00000000



000620                                      3727 M0T2     DS    0F
000620  02                                  3728          DC    X'02'                       Test Num
000621  0000                                3729          DC    X'00',X'00'
000623  00                                  3730          DC    X'00'                       M3: A=0,F=0,L=0
000624  0000142C 00000002                   3731          DC    A(TRTOP10),A(002)           Source - Op 1 & length
00062C  0000312C 00000100                   3732          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                            3733 *                                          Target -
000634  00120000 00220000                   3734          DC    A(MB+(2*K64)),A(2*MB+(2*K64)),A(0)  FC, Op1, Op1L
00063C  00000000
000640  AABBCCDD                            3735          DC    A(REG2PATT)
000644  00000007                            3736          DC    A(7) CC0
000648  00220002 00000000                   3737          DC    A(2*MB+(2*K64)+002),A(000),A(0)
000650  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000654                                     3739 M0T3     DS    0F
000654  03                                 3740          DC    X'03'                       Test Num
000655  0000                               3741          DC    X'00',X'00'
000657  00                                 3742          DC    X'00'                       M3: A=0,F=0,L=0
000658  0000142C 00000004                  3743          DC    A(TRTOP10),A(004)           Source - Op 1 & length
000660  0000312C 00000100                  3744          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                           3745 *                                          Target -
000668  00130000 00230000                  3746          DC    A(MB+(3*K64)),A(2*MB+(3*K64)),A(0)  FC, Op1, Op1L
000670  00000000
000674  AABBCCDD                           3747          DC    A(REG2PATT)
000678  00000007                           3748          DC    A(7) CC0
00067C  00230004 00000000                  3749          DC    A(2*MB+(3*K64)+004),A(000),A(0)
000684  00000000



000688                                     3751 M0T4     DS    0F
000688  04                                 3752          DC    X'04'                       Test Num
000689  0000                               3753          DC    X'00',X'00'
00068B  00                                 3754          DC    X'00'                       M3: A=0,F=0,L=0
00068C  0000142C 00000008                  3755          DC    A(TRTOP10),A(008)           Source - Op 1 & length
000694  0000312C 00000100                  3756          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                           3757 *                                          Target -
00069C  00140000 00240000                  3758          DC    A(MB+(4*K64)),A(2*MB+(4*K64)),A(0)  FC, Op1, Op1L
0006A4  00000000
0006A8  AABBCCDD                           3759          DC    A(REG2PATT)
0006AC  00000007                           3760          DC    A(7) CC0
0006B0  00240008 00000000                  3761          DC    A(2*MB+(4*K64)+008),A(000),A(0)
0006B8  00000000



0006BC                                     3763 M0T5     DS    0F
0006BC  05                                 3764          DC    X'05'                       Test Num
0006BD  0000                               3765          DC    X'00',X'00'
0006BF  00                                 3766          DC    X'00'                       M3: A=0,F=0,L=0
0006C0  0000142C 00000100                  3767          DC    A(TRTOP10),A(256)           Source - Op 1 & length
0006C8  0000312C 00000100                  3768          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                           3769 *                                          Target -
0006D0  00150000 00250000                  3770          DC    A(MB+(5*K64)),A(2*MB+(5*K64)),A(0)  FC, Op1, Op1L
0006D8  00000000
0006DC  AABBCCDD                           3771          DC    A(REG2PATT)
0006E0  00000007                           3772          DC    A(7) CC0
0006E4  00250100 00000000                  3773          DC    A(2*MB+(5*K64)+256),A(000),A(0)
0006EC  00000000



0006F0                                     3775 M0T6     DS    0F
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0006F0  06                                3776          DC    X'06'                        Test Num
0006F1  0000                              3777          DC    X'00',X'00'
0006F3  00                                3778          DC    X'00'                        M3: A=0,F=0,L=0
0006F4  0000152C 00000100                 3779          DC    A(TRTOP111),A(256)           Source - Op 1 & length
0006FC  0002322C 00000100                 3780          DC    A(TRTOP211),A(256)           Source - FC Table & length
                                          3781 *                                           Target -
000704  0015FFE0 0025FFF4                 3782          DC    A(MB+(6*K64)-32),A(2*MB+(6*K64)-12),A(0)  FC, Op1, Op1L
00070C  00000000
000710  AABBCCDD                          3783          DC    A(REG2PATT)
000714  0000000A                          3784          DC    A(10) CC1 or CC3
000718  00260005 000000EF                 3785          DC    A(2*MB+(6*K64)-12+X'11'),A(256-X'11'),XL4'11'
000720  00000011



000724                                    3787 M0T7     DS    0F
000724  07                                3788          DC    X'07'                        Test Num
000725  0000                              3789          DC    X'00',X'00'
000727  00                                3790          DC    X'00'                        M3: A=0,F=0,L=0
000728  0000162C 00000100                 3791          DC    A(TRTOP1F0),A(256)           Source - Op 1 & length
000730  0002332C 00000100                 3792          DC    A(TRTOP2F0),A(256)           Source - FC Table & length
                                          3793 *                                           Target -
000738  00170000 0026FFF4                 3794          DC    A(MB+(7*K64)),A(2*MB+(7*K64)-12),A(0)   FC, Op1, Op1L
000740  00000000
000744  AABBCCDD                          3795          DC    A(REG2PATT)
000748  0000000A                          3796          DC    A(10) CC1 or CC3
00074C  002700F3 00000001                 3797          DC    A(2*MB+(7*K64)-12+255),A(256-255),XL4'F0'
000754  000000F0
                                          3798



000758                                    3800 M0T8     DS    0F
000758  08                                3801          DC    X'08'                        Test Num
000759  0000                              3802          DC    X'00',X'00'
00075B  00                                3803          DC    X'00'                        M3: A=0,F=0,L=0
00075C  0000152C 00000100                 3804          DC    A(TRTOP111),A(256)           Source - Op 1 & length
000764  0002322C 00000100                 3805          DC    A(TRTOP211),A(256)           Source - FC Table & length
                                          3806 *                                           Target -
00076C  0017FFE0 00280000                 3807          DC    A(MB+(8*K64)-32),A(2*MB+(8*K64)),A(0)  FC, Op1, Op1L
000774  00000000
000778  AABBCCDD                          3808          DC    A(REG2PATT)
00077C  0000000B                          3809          DC    A(11) CC1
000780  00280011 000000EF                 3810          DC    A(2*MB+(8*K64)+X'11'),A(256-X'11'),XL4'11'
000788  00000011



00078C                                    3812 M0T9     DS    0F
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00078C  09                                3813          DC    X'09'                          Test Num
00078D  0000                              3814          DC    X'00',X'00'
00078F  00                                3815          DC    X'00'                          M3: A=0,F=0,L=0
000790  0000192C 00000800                 3816          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
000798  0000312C 00000100                 3817          DC    A(TRTOP20),A(256)              Source - FC Table & length
                                          3818 *                                             Target -
0007A0  00190000 00290000                 3819          DC    A(MB+(9*K64)),A(2*MB+(9*K64)),A(0)  FC, Op1, Op1L
0007A8  00000000
0007AC  AABBCCDD                          3820          DC    A(REG2PATT)
0007B0  00000007                          3821          DC    A(7) CC0
0007B4  00290800 00000000                 3822          DC    A(2*MB+(9*K64)+2048),A(000),A(0)
0007BC  00000000



0007C0                                    3824 M0T10    DS    0F
0007C0  0A                                3825          DC    X'0A'                          Test Num
0007C1  0000                              3826          DC    X'00',X'00'
0007C3  00                                3827          DC    X'00'                          M3: A=0,F=0,L=0
0007C4  0000212C 00000800                 3828          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
0007CC  0002322C 00000100                 3829          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                          3830 *                                             Target -
0007D4  001A0000 0029FF38                 3831          DC    A(MB+(10*K64)),A(2*MB+(10*K64)-200),A(0)  FC, Op1, Op1L
0007DC  00000000
0007E0  AABBCCDD                          3832          DC    A(REG2PATT)
0007E4  0000000A                          3833          DC    A(10) CC1 or CC3
0007E8  002A0339 000003FF                 3834          DC    A(2*MB+(10*K64)-200+(4*256)+1),A(1023),Xl4'11'
0007F0  00000011



0007F4                                    3836 M0T11    DS    0F
0007F4  0B                                3837          DC    X'0B'                          Test Num
0007F5  0000                              3838          DC    X'00',X'00'
0007F7  00                                3839          DC    X'00'                          M3: A=0,F=0,L=0
0007F8  0000292C 00000800                 3840          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
000800  0002332C 00000100                 3841          DC    A(TRTOP2F0),A(256)             Source - FC Table & length
                                          3842 *                                             Target -
000808  001AFFC0 002B0000                 3843          DC    A(MB+(11*K64)-64),A(2*MB+(11*K64)),A(0) FC, Op1, Op1L
000810  00000000
000814  AABBCCDD                          3844          DC    A(REG2PATT)
000818  0000000B                          3845          DC    A(11) CC1
00081C  002B07FF 00000001                 3846          DC    A(2*MB+(11*K64)+2048-1),A(1),Xl4'F0'
000824  000000F0
```

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 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             3848 ************************************************************************
                                             3849 *        tests with   M3: A=0,F=1,L=0, reserved=0    (4)
                                             3850 *                              FC Table = 2 bytes
                                             3851 ************************************************************************


000828                                       3853 M4T1     DS    0F
000828  41                                   3854          DC    X'41'                       Test Num
000829  0000                                 3855          DC    X'00',X'00'
00082B  40                                   3856          DC    X'40'                       M3: A=0,F=1,L=0
00082C  0000142C 00000001                    3857          DC    A(TRTOP10),A(001)           Source - Op 1 & length
000834  0000312C 00000200                    3858          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             3859 *                                          Target -
00083C  00310000 00410000                    3860          DC    A(3*MB+(1*K64)),A(4*MB+(1*K64)),A(0)  FC, Op1, Op1L
000844  00000000
000848  AABBCCDD                             3861          DC    A(REG2PATT)
00084C  00000007                             3862          DC    A(7) CC0
000850  00410001 00000000                    3863          DC    A(4*MB+(1*K64)+001),A(000),A(0)
000858  00000000


00085C                                       3865 M4T2     DS    0F
00085C  42                                   3866          DC    X'42'                       Test Num
00085D  0000                                 3867          DC    X'00',X'00'
00085F  40                                   3868          DC    X'40'                       M3: A=0,F=1,L=0
000860  0000142C 00000002                    3869          DC    A(TRTOP10),A(002)           Source - Op 1 & length
000868  0000312C 00000200                    3870          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             3871 *                                          Target -
000870  00320000 00420000                    3872          DC    A(3*MB+(2*K64)),A(4*MB+(2*K64)),A(0)  FC, Op1, Op1L
000878  00000000
00087C  AABBCCDD                             3873          DC    A(REG2PATT)
000880  00000007                             3874          DC    A(7) CC0
000884  00420002 00000000                    3875          DC    A(4*MB+(2*K64)+002),A(000),A(0)
00088C  00000000


000890                                       3877 M4T3     DS    0F
000890  43                                   3878          DC    X'43'                       Test Num
000891  0000                                 3879          DC    X'00',X'00'
000893  40                                   3880          DC    X'40'                       M3: A=0,F=1,L=0
000894  0000142C 00000004                    3881          DC    A(TRTOP10),A(004)           Source - Op 1 & length
00089C  0000312C 00000200                    3882          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             3883 *                                          Target -
0008A4  00340000 00440000                    3884          DC    A(3*MB+(4*K64)),A(4*MB+(4*K64)),A(0)  FC, Op1, Op1L
0008AC  00000000
0008B0  AABBCCDD                             3885          DC    A(REG2PATT)
0008B4  00000007                             3886          DC    A(7) CC0
0008B8  00440004 00000000                    3887          DC    A(4*MB+(4*K64)+004),A(000),A(0)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0008C0  00000000


0008C4                                    3889 M4T4     DS    0F
0008C4  44                                3890          DC    X'44'                       Test Num
0008C5  0000                              3891          DC    X'00',X'00'
0008C7  40                                3892          DC    X'40'                       M3: A=0,F=1,L=0
0008C8  0000142C 00000008                 3893          DC    A(TRTOP10),A(008)           Source - Op 1 & length
0008D0  0000312C 00000200                 3894          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                          3895 *                                          Target -
0008D8  00340000 00440000                 3896          DC    A(3*MB+(4*K64)),A(4*MB+(4*K64)),A(0)  FC, Op1, Op1L
0008E0  00000000
0008E4  AABBCCDD                          3897          DC    A(REG2PATT)
0008E8  00000007                          3898          DC    A(7) CC0
0008EC  00440008 00000000                 3899          DC    A(4*MB+(4*K64)+008),A(000),A(0)
0008F4  00000000


0008F8                                    3901 M4T5     DS    0F
0008F8  45                                3902          DC    X'45'                       Test Num
0008F9  0000                              3903          DC    X'00',X'00'
0008FB  40                                3904          DC    X'40'                       M3: A=0,F=1,L=0
0008FC  0000142C 00000100                 3905          DC    A(TRTOP10),A(256)           Source - Op 1 & length
000904  0000312C 00000200                 3906          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                          3907 *                                          Target -
00090C  00350000 00450000                 3908          DC    A(3*MB+(5*K64)),A(4*MB+(5*K64)),A(0)  FC, Op1, Op1L
000914  00000000
000918  AABBCCDD                          3909          DC    A(REG2PATT)
00091C  00000007                          3910          DC    A(7) CC0
000920  00450100 00000000                 3911          DC    A(4*MB+(5*K64)+256),A(000),A(0)
000928  00000000


00092C                                    3913 M4T6     DS    0F
00092C  46                                3914          DC    X'46'                       Test Num
00092D  0000                              3915          DC    X'00',X'00'
00092F  40                                3916          DC    X'40'                       M3: A=0,F=1,L=0
000930  0000152C 00000100                 3917          DC    A(TRTOP111),A(256)          Source - Op 1 & length
000938  0002342C 00000200                 3918          DC    A(TRTOP411),A(512)          Source - FC Table & length
                                          3919 *                                          Target -
000940  0035FFE0 0045FFF4                 3920          DC    A(3*MB+(6*K64)-32),A(4*MB+(6*K64)-12),A(0) FC, Op1, Op1L
000948  00000000
00094C  AABBCCDD                          3921          DC    A(REG2PATT)
000950  0000000A                          3922          DC    A(10) CC1 or CC3
000954  00460005 000000EF                 3923          DC    A(4*MB+(6*K64)-12+X'11'),A(256-X'11'),XL4'11'
00095C  00000011
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000960                                3925 M4T7     DS    0F
000960  47                            3926          DC    X'47'                       Test Num
000961  0000                          3927          DC    X'00',X'00'
000963  40                            3928          DC    X'40'                       M3: A=0,F=1,L=0
000964  0000162C 00000100             3929          DC    A(TRTOP1F0),A(256)          Source - Op 1 & length
00096C  0002362C 00000200             3930          DC    A(TRTOP4F0),A(512)          Source - FC Table & length
                                      3931 *                                          Target -
000974  00370000 0046FFF4             3932          DC    A(3*MB+(7*K64)),A(4*MB+(7*K64)-12),A(0) FC, Op1, Op1L
00097C  00000000
000980  AABBCCDD                      3933          DC    A(REG2PATT)
000984  0000000A                      3934          DC    A(10) CC1 or CC3
000988  004700F3 00000001             3935          DC    A(4*MB+(7*K64)-12+255),A(256-255),XL4'F0'
000990  000000F0



000994                                3937 M4T8     DS    0F
000994  48                            3938          DC    X'48'                       Test Num
000995  0000                          3939          DC    X'00',X'00'
000997  40                            3940          DC    X'40'                       M3: A=0,F=1,L=0
000998  0000152C 00000100             3941          DC    A(TRTOP111),A(256)          Source - Op 1 & length
0009A0  0002342C 00000200             3942          DC    A(TRTOP411),A(512)          Source - FC Table & length
                                      3943 *                                          Target -
0009A8  0037FFE0 00480000             3944          DC    A(3*MB+(8*K64)-32),A(4*MB+(8*K64)),A(0) FC, Op1, Op1L
0009B0  00000000
0009B4  AABBCCDD                      3945          DC    A(REG2PATT)
0009B8  0000000B                      3946          DC    A(11) CC1
0009BC  00480011 000000EF             3947          DC    A(4*MB+(8*K64)+X'11'),A(256-X'11'),XL4'11'
0009C4  00000011



0009C8                                3949 M4T9     DS    0F
0009C8  49                            3950          DC    X'49'                       Test Num
0009C9  0000                          3951          DC    X'00',X'00'
0009CB  40                            3952          DC    X'40'                       M3: A=0,F=1,L=0
0009CC  0000192C 00000800             3953          DC    A(TRTO1L0),A(2048)          Source - Op 1 & length
0009D4  0000312C 00000200             3954          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                      3955 *                                          Target -
0009DC  00390000 00490000             3956          DC    A(3*MB+(9*K64)),A(4*MB+(9*K64)),A(0) FC, Op1, Op1L
0009E4  00000000
0009E8  AABBCCDD                      3957          DC    A(REG2PATT)
0009EC  00000007                      3958          DC    A(7) CC0
0009F0  00490800 00000000             3959          DC    A(4*MB+(9*K64)+2048),A(000),A(0)
0009F8  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0009FC                                     3961 M4T10    DS    0F
0009FC  4A                                 3962          DC    X'4A'                        Test Num
0009FD  0000                               3963          DC    X'00',X'00'
0009FF  40                                 3964          DC    X'40'                        M3: A=0,F=1,L=0
000A00  0000212C 00000800                  3965          DC    A(TRTO1L11),A(2048)          Source - Op 1 & length
000A08  0002342C 00000200                  3966          DC    A(TRTOP411),A(512)           Source - FC Table & length
                                           3967 *                                           Target -
000A10  003A0000 0049FF38                  3968          DC    A(3*MB+(10*K64)),A(4*MB+(10*K64)-200),A(0) FC, Op1, Op1L
000A18  00000000
000A1C  AABBCCDD                           3969          DC    A(REG2PATT)
000A20  0000000A                           3970          DC    A(10) CC1 or CC3
000A24  004A0339 000003FF                  3971          DC    A(4*MB+(10*K64)-200+(4*256)+1),A(1023),XL4'11'
000A2C  00000011



000A30                                     3973 M4T11    DS    0F
000A30  4B                                 3974          DC    X'4B'                        Test Num
000A31  0000                               3975          DC    X'00',X'00'
000A33  40                                 3976          DC    X'40'                        M3: A=0,F=1,L=0
000A34  0000292C 00000800                  3977          DC    A(TRTO1LF0),A(2048)          Source - Op 1 & length
000A3C  0002362C 00000200                  3978          DC    A(TRTOP4F0),A(512)           Source - FC Table & length
                                           3979 *                                           Target -
000A44  003AFFC0 004B0000                  3980          DC    A(3*MB+(11*K64)-64),A(4*MB+(11*K64)),A(0) FC, Op1, Op1L
000A4C  00000000
000A50  AABBCCDD                           3981          DC    A(REG2PATT)
000A54  0000000B                           3982          DC    A(11) CC1
000A58  004B07FF 00000001                  3983          DC    A(4*MB+(11*K64)+2048-1),A(1),XL4'F0'
000A60  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             3985 ********************************************************************
                                             3986 *        tests with   M3: A=1,F=0,L=0, reserved=0    (8)
                                             3987 *                     FC Table : SIZE: 65,536 (2 BYTE ARGUMENT)
                                             3988 *
                                             3989 *              Note: Op1 length must be a multiple of 2
                                             3990 ********************************************************************



000A64                                       3992 M8T1     DS    0F
000A64  81                                   3993          DC    X'81'                          Test Num
000A65  0000                                 3994          DC    X'00',X'00'
000A67  80                                   3995          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000A68  0000142C 00000002                    3996          DC    A(TRTOP10),A(002)              Source - Op 1 & length
000A70  0000312C 00010000                    3997          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                             3998 *                                             Target -
000A78  00510000 00610000                    3999          DC    A(5*MB+(1*K64)),A(6*MB+(1*K64)),A(0)  FC, Op1, Op1L
000A80  00000000
000A84  AABBCCDD                             4000          DC    A(REG2PATT)
000A88  00000007                             4001          DC    A(7) CC0
000A8C  00610002 00000000                    4002          DC    A(6*MB+(1*K64)+002),A(000),A(0)
000A94  00000000
                                             4003
```

```
000A98                                       4005 M8T2     DS    0F
000A98  82                                   4006          DC    X'82'                          Test Num
000A99  0000                                 4007          DC    X'00',X'00'
000A9B  80                                   4008          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000A9C  0000142C 00000004                    4009          DC    A(TRTOP10),A(004)              Source - Op 1 & length
000AA4  0000312C 00010000                    4010          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                             4011 *                                             Target -
000AAC  00520000 00620000                    4012          DC    A(5*MB+(2*K64)),A(6*MB+(2*K64)),A(0)  FC, Op1, Op1L
000AB4  00000000
000AB8  AABBCCDD                             4013          DC    A(REG2PATT)
000ABC  00000007                             4014          DC    A(7) CC0
000AC0  00620004 00000000                    4015          DC    A(6*MB+(2*K64)+004),A(000),A(0)
000AC8  00000000



000ACC                                       4017 M8T3     DS    0F
000ACC  83                                   4018          DC    X'83'                          Test Num
000ACD  0000                                 4019          DC    X'00',X'00'
000ACF  80                                   4020          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000AD0  0000142C 00000008                    4021          DC    A(TRTOP10),A(008)              Source - Op 1 & length
000AD8  0000312C 00010000                    4022          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                             4023 *                                             Target -
000AE0  00530000 00630000                    4024          DC    A(5*MB+(3*K64)),A(6*MB+(3*K64)),A(0)  FC, Op1, Op1L
000AE8  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000AEC  AABBCCDD                           4025          DC    A(REG2PATT)
000AF0  00000007                           4026          DC    A(7) CC0
000AF4  00630008 00000000                  4027          DC    A(6*MB+(3*K64)+008),A(000),A(0)
000AFC  00000000



000B00                                     4029 M8T4     DS    0F
000B00  84                                 4030          DC    X'84'                       Test Num
000B01  0000                               4031          DC    X'00',X'00'
000B03  80                                 4032          DC    X'80'                       M3: A=1,F=0,L=0,--=0
000B04  0000142C 00000100                  4033          DC    A(TRTOP10),A(256)           Source - Op 1 & length
000B0C  0000312C 00010000                  4034          DC    A(TRTOP20),A(K64)           Source - FC Table & length
                                           4035 *                                    Target -
000B14  00540000 00640000                  4036          DC    A(5*MB+(4*K64)),A(6*MB+(4*K64)),A(0)  FC, Op1, Op1L
000B1C  00000000
000B20  AABBCCDD                           4037          DC    A(REG2PATT)
000B24  00000007                           4038          DC    A(7) CC0
000B28  00640100 00000000                  4039          DC    A(6*MB+(4*K64)+256),A(000),A(0)
000B30  00000000



000B34                                     4041 M8T5     DS    0F
000B34  85                                 4042          DC    X'85'                       Test Num
000B35  0000                               4043          DC    X'00',X'00'
000B37  80                                 4044          DC    X'80'                       M3: A=1,F=0,L=0,--=0
000B38  0000152C 00000100                  4045          DC    A(TRTOP111),A(256)          Source - Op 1 & length
000B40  0002382C 00010000                  4046          DC    A(TRTOP811),A(K64)          Source - FC Table & length
                                           4047 *                                    Target -
000B48  00550000 0064FFF4                  4048          DC    A(5*MB+(5*K64)),A(6*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
000B50  00000000
000B54  AABBCCDD                           4049          DC    A(REG2PATT)
000B58  0000000A                           4050          DC    A(10) CC1 or CC3
000B5C  00650004 000000F0                  4051          DC    A(6*MB+(5*K64)-12+X'10'),A(256-X'10'),XL4'11'
000B64  00000011



000B68                                     4053 M8T6     DS    0F
000B68  86                                 4054          DC    X'86'                       Test Num
000B69  0000                               4055          DC    X'00',X'00'
000B6B  80                                 4056          DC    X'80'                       M3: A=1,F=0,L=0,--=0
000B6C  0000162C 00000100                  4057          DC    A(TRTOP1F0),A(256)          Source - Op 1 & length
000B74  0004392C 00010000                  4058          DC    A(TRTOP8F0),A(K64)          Source - FC Table & length
                                           4059 *                                    Target -
000B7C  00560000 0065FFF4                  4060          DC    A(5*MB+(6*K64)),A(6*MB+(6*K64)-12),A(0)  FC, Op1, Op1L
000B84  00000000
000B88  AABBCCDD                           4061          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000B8C  0000000A                           4062          DC    A(10) CC1 or CC3
000B90  006600F2 00000002                  4063          DC    A(6*MB+(6*K64)-12+(256-2)),A(2),XL4'F0'
000B98  000000F0



000B9C                                     4065 M8T7     DS    0F
000B9C  87                                 4066          DC    X'87'                        Test Num
000B9D  0000                               4067          DC    X'00',X'00'
000B9F  80                                 4068          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000BA0  0000152C 00000100                  4069          DC    A(TRTOP111),A(256)           Source - Op 1 & length
000BA8  0002382C 00010000                  4070          DC    A(TRTOP811),A(K64)           Source - FC Table & length
                                           4071 *                                           Target -
000BB0  0057FFE0 00680000                  4072          DC    A(5*MB+(8*K64)-32),A(6*MB+(8*K64)),A(0)  FC, Op1, Op1L
000BB8  00000000
000BBC  AABBCCDD                           4073          DC    A(REG2PATT)
000BC0  0000000B                           4074          DC    A(11) CC1
000BC4  00680010 000000F0                  4075          DC    A(6*MB+(8*K64)+X'10'),A(256-X'10'),XL4'11'
000BCC  00000011



000BD0                                     4077 M8T8     DS    0F
000BD0  88                                 4078          DC    X'88'                        Test Num
000BD1  0000                               4079          DC    X'00',X'00'
000BD3  80                                 4080          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000BD4  0000172C 00000200                  4081          DC    A(TRTOP1F1),A(512)           Source - Op 1 & length
000BDC  00063A2C 00010000                  4082          DC    A(TRTOP8F1),A(K64)           Source - FC Table & length
                                           4083 *                                           Target -
000BE4  0058FFE0 00690000                  4084          DC    A(5*MB+(9*K64)-32),A(6*MB+(9*K64)),A(0)  FC, Op1, Op1L
000BEC  00000000
000BF0  AABBCCDD                           4085          DC    A(REG2PATT)
000BF4  0000000B                           4086          DC    A(11) CC1
000BF8  006901FE 00000002                  4087          DC    A(6*MB+(9*K64)+510),A(2),XL4'F1'
000C00  000000F1



000C04                                     4089 M8T9     DS    0F
000C04  89                                 4090          DC    X'89'                        Test Num
000C05  0000                               4091          DC    X'00',X'00'
000C07  80                                 4092          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000C08  0000192C 00000800                  4093          DC    A(TRTO1L0),A(2048)           Source - Op 1 & length
000C10  0000312C 00010000                  4094          DC    A(TRTOP20),A(K64)            Source - FC Table & length
                                           4095 *                                           Target -
000C18  005A0000 006A0000                  4096          DC    A(5*MB+(10*K64)),A(6*MB+(10*K64)),A(0)  FC, Op1, Op1L
000C20  00000000
000C24  AABBCCDD                           4097          DC    A(REG2PATT)
000C28  00000007                           4098          DC    A(7) CC0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000C2C  006A0800 00000000                  4099          DC    A(6*MB+(10*K64)+2048),A(0),XL4'00'
000C34  00000000



000C38                                     4101 M8T10    DS    0F
000C38  8A                                 4102          DC    X'8A'                        Test Num
000C39  0000                               4103          DC    X'00',X'00'
000C3B  80                                 4104          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000C3C  0000212C 00000800                  4105          DC    A(TRTO1L11),A(2048)          Source - Op 1 & length
000C44  0002382C 00010000                  4106          DC    A(TRTOP811),A(K64)           Source - FC Table & length
                                           4107 *                                           Target -
000C4C  005C0000 006BFF39                  4108          DC    A(5*MB+(12*K64)),A(6*MB+(12*K64)-199),A(0) FC, Op1, Op1L
000C54  00000000
000C58  AABBCCDD                           4109          DC    A(REG2PATT)
000C5C  0000000A                           4110          DC    A(10) CC1 or CC3
000C60  006C0339 00000400                  4111          DC    A(6*MB+(12*K64)-199+(4*256)),A(1024),XL4'11'
000C68  00000011



000C6C                                     4113 M8T11    DS    0F
000C6C  8B                                 4114          DC    X'8B'                        Test Num
000C6D  0000                               4115          DC    X'00',X'00'
000C6F  80                                 4116          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000C70  0000292C 00000800                  4117          DC    A(TRTO1LF0),A(2048)          Source - Op 1 & length
000C78  0004392C 00010000                  4118          DC    A(TRTOP8F0),A(K64)           Source - FC Table & length
                                           4119 *                                           Target -
000C80  005DFFC1 006E0000                  4120          DC    A(5*MB+(14*K64)-63),A(6*MB+(14*K64)),A(0) FC, Op1, Op1L
000C88  00000000
000C8C  AABBCCDD                           4121          DC    A(REG2PATT)
000C90  0000000B                           4122          DC    A(11) CC1
000C94  006E07FE 00000002                  4123          DC    A(6*MB+(14*K64)+2048-2),A(2),XL4'F0'
000C9C  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4125 ********************************************************************
                                             4126 *        tests with   M3: A=1,F=0,L=1, reserved=0    (10)
                                             4127 *                     FC Table : SIZE: 256 (2 BYTE ARGUMENT)
                                             4128 *                                Function Code is 1 byte
                                             4129 *                                Limit arg to 255
                                             4130 *
                                             4131 *               Note: Op1 length must be a multiple of 2
                                             4132 ********************************************************************


000CA0                                       4134 M10T1    DS    0F
000CA0  A1                                   4135          DC    X'A1'                       Test Num
000CA1  0000                                 4136          DC    X'00',X'00'
000CA3  A0                                   4137          DC    X'A0'                       M3: A=1,F=0,L=1,--=0
000CA4  0000142C 00000002                    4138          DC    A(TRTOP10),A(002)           Source - Op 1 & length
000CAC  0000312C 00000100                    4139          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                             4140 *                                          Target -
000CB4  00A00000 00B00000                    4141          DC    A(10*MB+(0*K64)),A(11*MB+(0*K64)),A(0)  FC, Op1, Op1L
000CBC  00000000
000CC0  AABBCCDD                             4142          DC    A(REG2PATT)
000CC4  00000007                             4143          DC    A(7) CC0
000CC8  00B00002 00000000                    4144          DC    A(11*MB+(0*K64)+002),A(000),A(0)
000CD0  00000000


000CD4                                       4146 M10T2    DS    0F
000CD4  A2                                   4147          DC    X'A2'                       Test Num
000CD5  0000                                 4148          DC    X'00',X'00'
000CD7  A0                                   4149          DC    X'A0'                       M3: A=1,F=0,L=1,--=0
000CD8  0000142C 00000004                    4150          DC    A(TRTOP10),A(004)           Source - Op 1 & length
000CE0  0000312C 00000100                    4151          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                             4152 *                                          Target -
000CE8  00A10000 00B10000                    4153          DC    A(10*MB+(1*K64)),A(11*MB+(1*K64)),A(0)  FC, Op1, Op1L
000CF0  00000000
000CF4  AABBCCDD                             4154          DC    A(REG2PATT)
000CF8  00000007                             4155          DC    A(7) CC0
000CFC  00B10004 00000000                    4156          DC    A(11*MB+(1*K64)+004),A(000),A(0)
000D04  00000000


000D08                                       4158 M10T3    DS    0F
000D08  A3                                   4159          DC    X'A3'                       Test Num
000D09  0000                                 4160          DC    X'00',X'00'
000D0B  A0                                   4161          DC    X'A0'                       M3: A=1,F=0,L=1,--=0
000D0C  0000142C 00000008                    4162          DC    A(TRTOP10),A(008)           Source - Op 1 & length
000D14  0000312C 00000100                    4163          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                             4164 *                                          Target -
000D1C  00A20000 00B20000                    4165          DC    A(10*MB+(2*K64)),A(11*MB+(2*K64)),A(0)  FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000D24  00000000
000D28  AABBCCDD                           4166          DC    A(REG2PATT)
000D2C  00000007                           4167          DC    A(7) CC0
000D30  00B20008 00000000                  4168          DC    A(11*MB+(2*K64)+008),A(000),A(0)
000D38  00000000




000D3C                                     4170 M10T4    DS    0F
000D3C  A4                                 4171          DC    X'A4'                       Test Num
000D3D  0000                               4172          DC    X'00',X'00'
000D3F  A0                                 4173          DC    X'A0'                       M3: A=1,F=0,L=1,--=0
000D40  0000142C 00000100                  4174          DC    A(TRTOP10),A(256)           Source - Op 1 & length
000D48  0000312C 00000100                  4175          DC    A(TRTOP20),A(256)           Source - FC Table & length
                                           4176 *                                          Target -
000D50  00A30000 00B30000                  4177          DC    A(10*MB+(3*K64)),A(11*MB+(3*K64)),A(0)  FC, Op1, Op1L
000D58  00000000
000D5C  AABBCCDD                           4178          DC    A(REG2PATT)
000D60  00000007                           4179          DC    A(7) CC0
000D64  00B30100 00000000                  4180          DC    A(11*MB+(3*K64)+256),A(000),A(0)
000D6C  00000000

000D70                                     4182 M10T5    DS    0F
000D70  A5                                 4183          DC    X'A5'                       Test Num
000D71  0000                               4184          DC    X'00',X'00'
000D73  A0                                 4185          DC    X'A0'                       M3: A=1,F=0,L=1,--=0
000D74  0000152C 00000100                  4186          DC    A(TRTOP111),A(256)          Source - Op 1 & length
000D7C  0002322C 00000100                  4187          DC    A(TRTOP211),A(256)          Source - FC Table & length
                                           4188 *                                          Target -
000D84  00A40000 00B3FFF4                  4189          DC    A(10*MB+(4*K64)),A(11*MB+(4*K64)-12),A(0)  FC, Op1, Op1L
000D8C  00000000
000D90  AABBCCDD                           4190          DC    A(REG2PATT)
000D94  0000000A                           4191          DC    A(10) CC1 or CC3
000D98  00B40004 000000F0                  4192          DC    A(11*MB+(4*K64)-12+X'10'),A(256-X'10'),XL4'11'
000DA0  00000011




000DA4                                     4194 M10T6    DS    0F
000DA4  A6                                 4195          DC    X'A6'                       Test Num
000DA5  0000                               4196          DC    X'00',X'00'
000DA7  A0                                 4197          DC    X'A0'                       M3: A=1,F=0,L=1,--=0
000DA8  0000162C 00000100                  4198          DC    A(TRTOP1F0),A(256)          Source - Op 1 & length
000DB0  0002332C 00000100                  4199          DC    A(TRTOP2F0),A(256)          Source - FC Table & length
                                           4200 *                                          Target -
000DB8  00A50000 00B4FFF4                  4201          DC    A(10*MB+(5*K64)),A(11*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
000DC0  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000DC4  AABBCCDD                           4202          DC    A(REG2PATT)
000DC8  0000000A                           4203          DC    A(10) CC1 or CC3
000DCC  00B500F2 00000002                  4204          DC    A(11*MB+(5*K64)-12+(256-2)),A(2),XL4'F0'
000DD4  000000F0

000DD8                                     4206 M10T7    DS    0F
000DD8  A7                                 4207          DC    X'A7'                          Test Num
000DD9  0000                               4208          DC    X'00',X'00'
000DDB  A0                                 4209          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000DDC  0000152C 00000100                  4210          DC    A(TRTOP111),A(256)             Source - Op 1 & length
000DE4  0002322C 00000100                  4211          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                           4212 *                                             Target -
000DEC  00A5FFE0 00B60000                  4213          DC    A(10*MB+(6*K64-32)),A(11*MB+(6*K64)),A(0)  FC, Op1, Op1L
000DF4  00000000
000DF8  AABBCCDD                           4214          DC    A(REG2PATT)
000DFC  0000000B                           4215          DC    A(11) CC1
000E00  00B60010 000000F0                  4216          DC    A(11*MB+(6*K64)+X'10'),A(256-X'10'),XL4'11'
000E08  00000011

000E0C                                     4218 M10T8    DS    0F
000E0C  A8                                 4219          DC    X'A8'                          Test Num
000E0D  0000                               4220          DC    X'00',X'00'
000E0F  A0                                 4221          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000E10  0000172C 00000200                  4222          DC    A(TRTOP1F1),A(512)             Source - Op 1 & length
000E18  00063A2C 00000100                  4223          DC    A(TRTOP8F1),A(256)             Source - FC Table & length
                                           4224 *                                             Target -
000E20  00A70000 00B70000                  4225          DC    A(10*MB+(7*K64)),A(11*MB+(7*K64)),A(0)  FC, Op1, Op1L
000E28  00000000
000E2C  AABBCCDD                           4226          DC    A(REG2PATT)
000E30  0000000B                           4227          DC    A(11) CC1
000E34  00B701FE 00000002                  4228          DC    A(11*MB+(7*K64)+510),A(2),XL4'F1'
000E3C  000000F1

000E40                                     4230 M10T9    DS    0F
000E40  A9                                 4231          DC    X'A9'                          Test Num
000E41  0000                               4232          DC    X'00',X'00'
000E43  A0                                 4233          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000E44  0000192C 00000800                  4234          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
000E4C  0000312C 00000100                  4235          DC    A(TRTOP20),A(256)             Source - FC Table & length
                                           4236 *                                             Target -
000E54  00A80000 00B80000                  4237          DC    A(10*MB+(8*K64)),A(11*MB+(8*K64)),A(0)  FC, Op1, Op1L
000E5C  00000000
000E60  AABBCCDD                           4238          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000E64  00000007                          4239          DC    A(7) CC0
000E68  00B80800 00000000                 4240          DC    A(11*MB+(8*K64)+2048),A(0),XL4'00'
000E70  00000000

000E74                                    4242 M10T10   DS    0F
000E74  AA                                4243          DC    X'AA'                          Test Num
000E75  0000                              4244          DC    X'00',X'00'
000E77  A0                                4245          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000E78  0000212C 00000800                 4246          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
000E80  0002322C 00000100                 4247          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                          4248 *                                             Target -
000E88  00A90000 00B8FF39                 4249          DC    A(10*MB+(9*K64)),A(11*MB+(9*K64)-199),A(0) FC, Op1, Op1L
000E90  00000000
000E94  AABBCCDD                          4250          DC    A(REG2PATT)
000E98  0000000A                          4251          DC    A(10) CC1 or CC3
000E9C  00B90339 00000400                 4252          DC    A(11*MB+(9*K64)-199+(4*256)),A(1024),XL4'11'
000EA4  00000011

000EA8                                    4254 M10T11   DS    0F
000EA8  AB                                4255          DC    X'AB'                          Test Num
000EA9  0000                              4256          DC    X'00',X'00'
000EAB  A0                                4257          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000EAC  0000292C 00000800                 4258          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
000EB4  0002332C 00000100                 4259          DC    A(TRTOP2F0),A(256)             Source - FC Table & length
                                          4260 *                                             Target - FC, Op1, Op1L
000EBC  00A9FE1F 00BA0000                 4261          DC    A(10*MB+(10*K64)-481),A(11*MB+(10*K64)),A(0)
000EC4  00000000
000EC8  AABBCCDD                          4262          DC    A(REG2PATT)
000ECC  0000000B                          4263          DC    A(11) CC1
000ED0  00BA07FE 00000002                 4264          DC    A(11*MB+(10*K64)+2048-2),A(2),XL4'F0'
000ED8  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4266 ********************************************************************
                                             4267 *        tests with   M3: A=1,F=1,L=0, reserved=0    (12)
                                             4268 *                     FC Table : SIZE: 131,072 (2 BYTE ARGUMENT)
                                             4269 *                                Function Code is 2 bytes
                                             4270 *
                                             4271 *              Note: Op1 length must be a multiple of 2
                                             4272 ********************************************************************


000EDC                                       4274 M12T1    DS    0F
000EDC  C1                                   4275          DC    X'C1'                       Test Num
000EDD  0000                                 4276          DC    X'00',X'00'
000EDF  C0                                   4277          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
000EE0  0000142C 00000002                    4278          DC    A(TRTOP10),A(002)           Source - Op 1 & length
000EE8  0000312C 00020000                    4279          DC    A(TRTOP20),A(2*K64)         Source - FC Table & length
                                             4280 *                                          Target -
000EF0  00700000 00900000                    4281          DC    A(7*MB+(0*K64)),A(9*MB+(0*K64)),A(0)  FC, Op1, Op1L
000EF8  00000000
000EFC  AABBCCDD                             4282          DC    A(REG2PATT)
000F00  00000007                             4283          DC    A(7) CC0
000F04  00900002 00000000                    4284          DC    A(9*MB+(0*K64)+002),A(000),A(0)
000F0C  00000000
                                             4285


000F10                                       4287 M12T2    DS    0F
000F10  C2                                   4288          DC    X'C2'                       Test Num
000F11  0000                                 4289          DC    X'00',X'00'
000F13  C0                                   4290          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
000F14  0000142C 00000004                    4291          DC    A(TRTOP10),A(004)           Source - Op 1 & length
000F1C  0000312C 00020000                    4292          DC    A(TRTOP20),A(2*K64)         Source - FC Table & length
                                             4293 *                                          Target -
000F24  00720000 00910000                    4294          DC    A(7*MB+(2*K64)),A(9*MB+(1*K64)),A(0)  FC, Op1, Op1L
000F2C  00000000
000F30  AABBCCDD                             4295          DC    A(REG2PATT)
000F34  00000007                             4296          DC    A(7) CC0
000F38  00910004 00000000                    4297          DC    A(9*MB+(1*K64)+004),A(000),A(0)
000F40  00000000


000F44                                       4299 M12T3    DS    0F
000F44  C3                                   4300          DC    X'C3'                       Test Num
000F45  0000                                 4301          DC    X'00',X'00'
000F47  C0                                   4302          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
000F48  0000142C 00000008                    4303          DC    A(TRTOP10),A(008)           Source - Op 1 & length
000F50  0000312C 00020000                    4304          DC    A(TRTOP20),A(2*K64)         Source - FC Table & length
                                             4305 *                                          Target -
000F58  00740000 00920000                    4306          DC    A(7*MB+(4*K64)),A(9*MB+(2*K64)),A(0)  FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000F60  00000000
000F64  AABBCCDD                          4307          DC    A(REG2PATT)
000F68  00000007                          4308          DC    A(7) CC0
000F6C  00920008 00000000                 4309          DC    A(9*MB+(2*K64)+008),A(000),A(0)
000F74  00000000



000F78                                    4311 M12T4    DS    0F
000F78  C4                                4312          DC    X'C4'                       Test Num
000F79  0000                              4313          DC    X'00',X'00'
000F7B  C0                                4314          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
000F7C  0000142C 00000100                 4315          DC    A(TRTOP10),A(256)           Source - Op 1 & length
000F84  0000312C 00020000                 4316          DC    A(TRTOP20),A(2*K64)         Source - FC Table & length
                                          4317 *                                          Target -
000F8C  00760000 00930000                 4318          DC    A(7*MB+(6*K64)),A(9*MB+(3*K64)),A(0)  FC, Op1, Op1L
000F94  00000000
000F98  AABBCCDD                          4319          DC    A(REG2PATT)
000F9C  00000007                          4320          DC    A(7) CC0
000FA0  00930100 00000000                 4321          DC    A(9*MB+(3*K64)+256),A(000),A(0)
000FA8  00000000

000FAC                                    4323 M12T5    DS    0F
000FAC  C5                                4324          DC    X'C5'                       Test Num
000FAD  0000                              4325          DC    X'00',X'00'
000FAF  C0                                4326          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
000FB0  0000152C 00000100                 4327          DC    A(TRTOP111),A(256)          Source - Op 1 & length
000FB8  00083B2C 00020000                 4328          DC    A(TRTOPC11),A(2*K64)        Source - FC Table & length
                                          4329 *                                          Target -
000FC0  00780000 0093FFF4                 4330          DC    A(7*MB+(8*K64)),A(9*MB+(4*K64)-12),A(0)  FC, Op1, Op1L
000FC8  00000000
000FCC  AABBCCDD                          4331          DC    A(REG2PATT)
000FD0  0000000A                          4332          DC    A(10) CC1 or CC3
000FD4  00940004 000000F0                 4333          DC    A(9*MB+(4*K64)-12+X'10'),A(256-X'10'),XL4'11'
000FDC  00000011



000FE0                                    4335 M12T6    DS    0F
000FE0  C6                                4336          DC    X'C6'                       Test Num
000FE1  0000                              4337          DC    X'00',X'00'
000FE3  C0                                4338          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
000FE4  0000162C 00000100                 4339          DC    A(TRTOP1F0),A(256)          Source - Op 1 & length
000FEC  000A3B50 00020000                 4340          DC    A(TRTOPCF0),A(2*K64)        Source - FC Table & length
                                          4341 *                                          Target -
000FF4  007A0000 0094FFF4                 4342          DC    A(7*MB+(10*K64)),A(9*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
000FFC  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001000  AABBCCDD                          4343          DC    A(REG2PATT)
001004  0000000A                          4344          DC    A(10) CC1 or CC3
001008  009500F2 00000002                 4345          DC    A(9*MB+(5*K64)-12+(256-2)),A(2),XL4'F0'
001010  000000F0


001014                                    4347 M12T7    DS    0F
001014  C7                                4348          DC    X'C7'                       Test Num
001015  0000                              4349          DC    X'00',X'00'
001017  C0                                4350          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
001018  0000152C 00000100                 4351          DC    A(TRTOP111),A(256)          Source - Op 1 & length
001020  00083B2C 00020000                 4352          DC    A(TRTOPC11),A(2*K64)        Source - FC Table & length
                                          4353 *                                          Target -
001028  007CFFE0 00960000                 4354          DC    A(7*MB+(13*K64)-32),A(9*MB+(6*K64)),A(0)  FC, Op1, Op1L
001030  00000000
001034  AABBCCDD                          4355          DC    A(REG2PATT)
001038  0000000B                          4356          DC    A(11) CC1
00103C  00960010 000000F0                 4357          DC    A(9*MB+(6*K64)+X'10'),A(256-X'10'),XL4'11'
001044  00000011


001048                                    4359 M12T8    DS    0F
001048  C8                                4360          DC    X'C8'                       Test Num
001049  0000                              4361          DC    X'00',X'00'
00104B  C0                                4362          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
00104C  0000172C 00000200                 4363          DC    A(TRTOP1F1),A(512)          Source - Op 1 & length
001054  000C3D4E 00020000                 4364          DC    A(TRTOPCF1),A(2*K64)        Source - FC Table & length
                                          4365 *                                          Target -
00105C  007F0000 00970000                 4366          DC    A(7*MB+(15*K64)),A(9*MB+(7*K64)),A(0)  FC, Op1, Op1L
001064  00000000
001068  AABBCCDD                          4367          DC    A(REG2PATT)
00106C  0000000B                          4368          DC    A(11) CC1
001070  009701FE 00000002                 4369          DC    A(9*MB+(7*K64)+510),A(2),XL4'F1'
001078  000000F1


00107C                                    4371 M12T9    DS    0F
00107C  C9                                4372          DC    X'C9'                       Test Num
00107D  0000                              4373          DC    X'00',X'00'
00107F  C0                                4374          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
001080  0000192C 00000800                 4375          DC    A(TRTO1L0),A(2048)          Source - Op 1 & length
001088  0000312C 00020000                 4376          DC    A(TRTOP20),A(2*K64)         Source - FC Table & length
                                          4377 *                                          Target -
001090  00810000 00980000                 4378          DC    A(7*MB+(17*K64)),A(9*MB+(8*K64)),A(0)  FC, Op1, Op1L
001098  00000000
00109C  AABBCCDD                          4379          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0010A0  00000007                          4380          DC    A(7) CC0
0010A4  00980800 00000000                 4381          DC    A(9*MB+(8*K64)+2048),A(0),XL4'00'
0010AC  00000000

0010B0                                    4383 M12T10   DS    0F
0010B0  CA                                4384          DC    X'CA'                        Test Num
0010B1  0000                              4385          DC    X'00',X'00'
0010B3  C0                                4386          DC    X'C0'                        M3: A=1,F=1,L=0,--=0
0010B4  0000212C 00000800                 4387          DC    A(TRTO1L11),A(2048)          Source - Op 1 & length
0010BC  00083B2C 00020000                 4388          DC    A(TRTOPC11),A(2*K64)         Source - FC Table & length
                                          4389 *                                           Target -
0010C4  00830000 0098FF39                 4390          DC    A(7*MB+(19*K64)),A(9*MB+(9*K64)-199),A(0)  FC, Op1, Op1L
0010CC  00000000
0010D0  AABBCCDD                          4391          DC    A(REG2PATT)
0010D4  0000000A                          4392          DC    A(10) CC1 or CC3
0010D8  00990339 00000400                 4393          DC    A(9*MB+(9*K64)-199+(4*256)),A(1024),XL4'11'
0010E0  00000011

0010E4                                    4395 M12T11   DS    0F
0010E4  CB                                4396          DC    X'CB'                        Test Num
0010E5  0000                              4397          DC    X'00',X'00'
0010E7  C0                                4398          DC    X'C0'                        M3: A=1,F=1,L=0,--=0
0010E8  0000292C 00000800                 4399          DC    A(TRTO1LF0),A(2048)          Source - Op 1 & length
0010F0  000A3B50 00020000                 4400          DC    A(TRTOPCF0),A(2*K64)         Source - FC Table & length
                                          4401 *                                           Target -
0010F8  0085FE1F 009A0000                 4402          DC    A(7*MB+(22*K64)-481),A(9*MB+(10*K64)),A(0) FC, Op1, Op1L
001100  00000000
001104  AABBCCDD                          4403          DC    A(REG2PATT)
001108  0000000B                          4404          DC    A(11) CC1
00110C  009A07FE 00000002                 4405          DC    A(9*MB+(10*K64)+2048-2),A(2),XL4'F0'
001114  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4407 ********************************************************************
                                             4408 *        tests with   M3: A=1,F=1,L=1, reserved=0    (14)
                                             4409 *                     FC Table : SIZE: 512 (2 BYTE ARGUMENT)
                                             4410 *                                Function Code is 2 byte
                                             4411 *                                Limit arg to 255
                                             4412 *
                                             4413 *               Note: Op1 length must be a multiple of 2
                                             4414 ********************************************************************


001118                                       4416 M14T1    DS    0F
001118  E1                                  4417          DC    X'E1'                       Test Num
001119  0000                                 4418          DC    X'00',X'00'
00111B  E0                                   4419          DC    X'E0'                       M3: A=1,F=1,L=1,--=0
00111C  0000142C 00000002                    4420          DC    A(TRTOP10),A(002)           Source - Op 1 & length
001124  0000312C 00000200                    4421          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             4422 *                                        Target -
00112C  00B00000 00C00000                    4423          DC    A(11*MB+(0*K64)),A(12*MB+(0*K64)),A(0)  FC, Op1, Op1L
001134  00000000
001138  AABBCCDD                            4424          DC    A(REG2PATT)
00113C  00000007                             4425          DC    A(7) CC0
001140  00C00002 00000000                    4426          DC    A(12*MB+(0*K64)+002),A(000),A(0)
001148  00000000


00114C                                       4428 M14T2    DS    0F
00114C  E2                                  4429          DC    X'E2'                       Test Num
00114D  0000                                 4430          DC    X'00',X'00'
00114F  E0                                   4431          DC    X'E0'                       M3: A=1,F=1,L=1,--=0
001150  0000142C 00000004                    4432          DC    A(TRTOP10),A(004)           Source - Op 1 & length
001158  0000312C 00000200                    4433          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             4434 *                                        Target -
001160  00B10000 00C10000                    4435          DC    A(11*MB+(1*K64)),A(12*MB+(1*K64)),A(0)  FC, Op1, Op1L
001168  00000000
00116C  AABBCCDD                            4436          DC    A(REG2PATT)
001170  00000007                             4437          DC    A(7) CC0
001174  00C10004 00000000                    4438          DC    A(12*MB+(1*K64)+004),A(000),A(0)
00117C  00000000


001180                                       4440 M14T3    DS    0F
001180  E3                                  4441          DC    X'E3'                       Test Num
001181  0000                                 4442          DC    X'00',X'00'
001183  E0                                   4443          DC    X'E0'                       M3: A=1,F=1,L=1,--=0
001184  0000142C 00000008                    4444          DC    A(TRTOP10),A(008)           Source - Op 1 & length
00118C  0000312C 00000200                    4445          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             4446 *                                        Target -
001194  00B20000 00C20000                    4447          DC    A(11*MB+(2*K64)),A(12*MB+(2*K64)),A(0)  FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00119C  00000000
0011A0  AABBCCDD                          4448          DC    A(REG2PATT)
0011A4  00000007                          4449          DC    A(7) CC0
0011A8  00C20008 00000000                 4450          DC    A(12*MB+(2*K64)+008),A(000),A(0)
0011B0  00000000



0011B4                                    4452 M14T4    DS    0F
0011B4  E4                                4453          DC    X'E4'                       Test Num
0011B5  0000                              4454          DC    X'00',X'00'
0011B7  E0                                4455          DC    X'E0'                       M3: A=1,F=1,L=1,--=0
0011B8  0000142C 00000100                 4456          DC    A(TRTOP10),A(256)           Source - Op 1 & length
0011C0  0000312C 00000200                 4457          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                          4458 *                                          Target -
0011C8  00B30000 00C30000                 4459          DC    A(11*MB+(3*K64)),A(12*MB+(3*K64)),A(0)  FC, Op1, Op1L
0011D0  00000000
0011D4  AABBCCDD                          4460          DC    A(REG2PATT)
0011D8  00000007                          4461          DC    A(7) CC0
0011DC  00C30100 00000000                 4462          DC    A(12*MB+(3*K64)+256),A(000),A(0)
0011E4  00000000

0011E8                                    4464 M14T5    DS    0F
0011E8  E5                                4465          DC    X'E5'                       Test Num
0011E9  0000                              4466          DC    X'00',X'00'
0011EB  E0                                4467          DC    X'E0'                       M3: A=1,F=1,L=1,--=0
0011EC  0000152C 00000100                 4468          DC    A(TRTOP111),A(256)          Source - Op 1 & length
0011F4  0002342C 00000200                 4469          DC    A(TRTOP411),A(512)          Source - FC Table & length
                                          4470 *                                          Target -
0011FC  00B40000 00C3FFF4                 4471          DC    A(11*MB+(4*K64)),A(12*MB+(4*K64)-12),A(0)  FC, Op1, Op1L
001204  00000000
001208  AABBCCDD                          4472          DC    A(REG2PATT)
00120C  0000000A                          4473          DC    A(10) CC1 or CC3
001210  00C40004 000000F0                 4474          DC    A(12*MB+(4*K64)-12+X'10'),A(256-X'10'),XL4'11'
001218  00000011



00121C                                    4476 M14T6    DS    0F
00121C  E6                                4477          DC    X'E6'                       Test Num
00121D  0000                              4478          DC    X'00',X'00'
00121F  E0                                4479          DC    X'E0'                       M3: A=1,F=1,L=1,--=0
001220  0000162C 00000100                 4480          DC    A(TRTOP1F0),A(256)          Source - Op 1 & length
001228  0002362C 00000200                 4481          DC    A(TRTOP4F0),A(512)          Source - FC Table & length
                                          4482 *                                          Target -
001230  00B50000 00C4FFF4                 4483          DC    A(11*MB+(5*K64)),A(12*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
001238  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00123C  AABBCCDD                           4484          DC    A(REG2PATT)
001240  0000000A                           4485          DC    A(10) CC1 or CC3
001244  00C500F2 00000002                  4486          DC    A(12*MB+(5*K64)-12+254),A(2),XL4'F0'
00124C  000000F0



001250                                     4488 M14T7    DS    0F
001250  E7                                 4489          DC    X'E7'                        Test Num
001251  0000                               4490          DC    X'00',X'00'
001253  E0                                 4491          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
001254  0000152C 00000100                  4492          DC    A(TRTOP111),A(256)           Source - Op 1 & length
00125C  0002342C 00000200                  4493          DC    A(TRTOP411),A(512)           Source - FC Table & length
                                           4494 *                                           Target -
001264  00B5FFE0 00C60000                  4495          DC    A(11*MB+(6*K64)-32),A(12*MB+(6*K64)),A(0)  FC, Op1, Op1L
00126C  00000000
001270  AABBCCDD                           4496          DC    A(REG2PATT)
001274  0000000B                           4497          DC    A(11) CC1
001278  00C60010 000000F0                  4498          DC    A(12*MB+(6*K64)+X'10'),A(256-X'10'),XL4'11'
001280  00000011



001284                                     4500 M14T8    DS    0F
001284  E8                                 4501          DC    X'E8'                        Test Num
001285  0000                               4502          DC    X'00',X'00'
001287  E0                                 4503          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
001288  0000172C 00000200                  4504          DC    A(TRTOP1F1),A(512)           Source - Op 1 & length
001290  000C3D4E 00000200                  4505          DC    A(TRTOPCF1),A(512)           Source - FC Table & length
                                           4506 *                                           Target -
001298  00B70000 00C70000                  4507          DC    A(11*MB+(7*K64)),A(12*MB+(7*K64)),A(0)  FC, Op1, Op1L
0012A0  00000000
0012A4  AABBCCDD                           4508          DC    A(REG2PATT)
0012A8  0000000B                           4509          DC    A(11) CC1
0012AC  00C701FE 00000002                  4510          DC    A(12*MB+(7*K64)+510),A(2),XL4'F1'
0012B4  000000F1



0012B8                                     4512 M14T9    DS    0F
0012B8  E9                                 4513          DC    X'E9'                        Test Num
0012B9  0000                               4514          DC    X'00',X'00'
0012BB  E0                                 4515          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
0012BC  0000192C 00000800                  4516          DC    A(TRTO1L0),A(2048)           Source - Op 1 & length
0012C4  0000312C 00000200                  4517          DC    A(TRTOP20),A(512)            Source - FC Table & length
                                           4518 *                                           Target -
0012CC  00B80000 00C80000                  4519          DC    A(11*MB+(8*K64)),A(12*MB+(8*K64)),A(0)  FC, Op1, Op1L
0012D4  00000000
0012D8  AABBCCDD                           4520          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0012DC  00000007                          4521          DC    A(7) CC0
0012E0  00C80800 00000000                 4522          DC    A(12*MB+(8*K64)+2048),A(000),A(0)
0012E8  00000000



0012EC                                    4524 M14T10   DS    0F
0012EC  EA                                4525          DC    X'EA'                        Test Num
0012ED  0000                              4526          DC    X'00',X'00'
0012EF  E0                                4527          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
0012F0  0000212C 00000800                 4528          DC    A(TRTO1L11),A(2048)          Source - Op 1 & length
0012F8  0002342C 00000200                 4529          DC    A(TRTOP411),A(512)           Source - FC Table & length
                                          4530 *                                           Target -
001300  00B90000 00C8FF38                 4531          DC    A(11*MB+(9*K64)),A(12*MB+(9*K64)-200),A(0) FC, Op1, Op1L
001308  00000000
00130C  AABBCCDD                          4532          DC    A(REG2PATT)
001310  0000000A                          4533          DC    A(10) CC1 or CC3
001314  00C90338 00000400                 4534          DC    A(12*MB+(9*K64)-200+(4*256)),A(1024),XL4'11'
00131C  00000011



001320                                    4536 M14T11   DS    0F
001320  EB                                4537          DC    X'EB'                        Test Num
001321  0000                              4538          DC    X'00',X'00'
001323  E0                                4539          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
001324  0000292C 00000800                 4540          DC    A(TRTO1LF0),A(2048)          Source - Op 1 & length
00132C  0002362C 00000200                 4541          DC    A(TRTOP4F0),A(512)           Source - FC Table & length
                                          4542 *                                           Target -  FC, Op1, Op1L
001334  00B9FFC0 00CA0000                 4543          DC    A(11*MB+(10*K64)-64),A(12*MB+(10*K64)),A(0)
00133C  00000000
001340  AABBCCDD                          4544          DC    A(REG2PATT)
001344  0000000B                          4545          DC    A(11) CC1
001348  00CA07FE 00000002                 4546          DC    A(12*MB+(10*K64)+2048-2),A(2),XL4'F0'
001350  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4548 ************************************************************************
                                             4549 *        Check performance tests are valid.
                                             4550 *        tests with   M3: A=1,F=1,L=0, reserved=0    (12)
                                             4551 *                     FC Table : SIZE: 131,072 (2 BYTE ARGUMENT)
                                             4552 *                                Function Code is 2 bytes
                                             4553 *
                                             4554 *              Note: Op1 length must be a multiple of 2
                                             4555 ************************************************************************


001354                                       4557 F12T8    DS    0F
001354  F8                                   4558          DC    X'F8'                       Test Num
001355  0000                                 4559          DC    X'00',X'00'
001357  C0                                   4560          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
001358  0000172C 00000200                    4561          DC    A(TRTOP1F1),A(512)          Source - Op 1 & length
001360  000C3D4E 00020000                    4562          DC    A(TRTOPCF1),A(2*K64)        Source - FC Table & length
                                             4563 *                                        Target -
001368  00710000 00910000                    4564          DC    A(7*MB+(1*K64)),A(9*MB+(1*K64)),A(0)  FC, Op1, Op1L
001370  00000000
001374  AABBCCDD                             4565          DC    A(REG2PATT)
001378  0000000B                             4566          DC    A(11) CC1
00137C  009101FE 00000002                    4567          DC    A(9*MB+(1*K64)+510),A(2),XL4'F1'
001384  000000F1


001388                                       4569 F12T8A   DS    0F
001388  F9                                   4570          DC    X'F9'                       Test Num
001389  0000                                 4571          DC    X'00',X'00'
00138B  C0                                   4572          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
00138C  0000172C 00000200                    4573          DC    A(TRTOP1F1),A(512)          Source - Op 1 & length
001394  000C3D4E 00020000                    4574          DC    A(TRTOPCF1),A(2*K64)        Source - FC Table & length
                                             4575 *                                        Target - FC, Op1, Op1L
00139C  0072FF81 0092FF81                    4576          DC    A(7*MB+(3*K64)-127),A(9*MB+(3*K64)-127),A(0)
0013A4  00000000
0013A8  AABBCCDD                             4577          DC    A(REG2PATT)
0013AC  0000000A                             4578          DC    A(10) CC1 or CC3
0013B0  0093017F 00000002                    4579          DC    A(9*MB+(3*K64)-127+510),A(2),XL4'F1'
0013B8  000000F1


0013BC                                       4581 F12T11   DS    0F
0013BC  FB                                   4582          DC    X'FB'                       Test Num
0013BD  0000                                 4583          DC    X'00',X'00'
0013BF  C0                                   4584          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
0013C0  0000292C 00000800                    4585          DC    A(TRTO1LF0),A(2048)         Source - Op 1 & length
0013C8  000A3B50 00020000                    4586          DC    A(TRTOPCF0),A(2*K64)        Source - FC Table & length
                                             4587 *                                        Target -
0013D0  00760000 00960000                    4588          DC    A(7*MB+(6*K64)),A(9*MB+(6*K64)),A(0) FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0013D8  00000000
0013DC  AABBCCDD                           4589          DC    A(REG2PATT)
0013E0  0000000B                           4590          DC    A(11) CC1
0013E4  009607FE 00000002                  4591          DC    A(9*MB+(6*K64)+2048-2),A(2),XL4'F0'
0013EC  000000F0




0013F0                                     4593 F12T11A  DS    0F
0013F0  FC                                 4594          DC    X'FC'                        Test Num
0013F1  0000                               4595          DC    X'00',X'00'
0013F3  C0                                 4596          DC    X'C0'                        M3: A=1,F=1,L=0,--=0
0013F4  0000292C 00000800                  4597          DC    A(TRTO1LF0),A(2048)          Source - Op 1 & length
0013FC  000A3B50 00020000                  4598          DC    A(TRTOPCF0),A(2*K64)         Source - FC Table & length
                                           4599 *                                     Target - FC, Op1, Op1L
001404  0078FE1F 0098FE1F                  4600          DC    A(7*MB+(9*K64)-481),A(9*MB+(9*K64)-481),A(0)
00140C  00000000
001410  AABBCCDD                           4601          DC    A(REG2PATT)
001414  0000000A                           4602          DC    A(10) CC1 or CC3
001418  0099061D 00000002                  4603          DC    A(9*MB+(9*K64)-481+2048-2),A(2),XL4'F0'
001420  000000F0
                                           4604
                                           4605
                                           4606
001424  00000000                           4607          DC    A(0)     end of table
001428  00000000                           4608          DC    A(0)     end of table
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     4610 ************************************************************************
                                     4611 *        TRTE op1 scan data...
                                     4612 ************************************************************************

00142C  78125634 78125634                   4614 TRTOP10  DC    64XL4'78125634'    (CC0)
001434  78125634 78125634
00143C  78125634 78125634
001444  78125634 78125634
00144C  78125634 78125634
001454  78125634 78125634
00145C  78125634 78125634
001464  78125634 78125634
00146C  78125634 78125634
001474  78125634 78125634
00147C  78125634 78125634
001484  78125634 78125634
00148C  78125634 78125634
001494  78125634 78125634
00149C  78125634 78125634
0014A4  78125634 78125634
0014AC  78125634 78125634
0014B4  78125634 78125634
0014BC  78125634 78125634
0014C4  78125634 78125634
0014CC  78125634 78125634
0014D4  78125634 78125634
0014DC  78125634 78125634
0014E4  78125634 78125634
0014EC  78125634 78125634
0014F4  78125634 78125634
0014FC  78125634 78125634
001504  78125634 78125634
00150C  78125634 78125634
001514  78125634 78125634
00151C  78125634 78125634
001524  78125634 78125634

00152C  78125634 78125634                   4616 TRTOP111 DC    04XL4'78125634',X'00110000',59XL4'78125634'    (CC1)
001534  78125634 78125634
00153C  00110000 78125634
001544  78125634 78125634
00154C  78125634 78125634
001554  78125634 78125634
00155C  78125634 78125634
001564  78125634 78125634
00156C  78125634 78125634
001574  78125634 78125634
00157C  78125634 78125634
001584  78125634 78125634
00158C  78125634 78125634
001594  78125634 78125634
00159C  78125634 78125634
```

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 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0015A4  78125634 78125634
0015AC  78125634 78125634
0015B4  78125634 78125634
0015BC  78125634 78125634
0015C4  78125634 78125634
0015CC  78125634 78125634
0015D4  78125634 78125634
0015DC  78125634 78125634
0015E4  78125634 78125634
0015EC  78125634 78125634
0015F4  78125634 78125634
0015FC  78125634 78125634
001604  78125634 78125634
00160C  78125634 78125634
001614  78125634 78125634
00161C  78125634 78125634
001624  78125634 78125634

00162C  78125634 78125634                4618 TRTOP1F0 DC    63XL4'78125634',X'000000F0'    (CC1)
001634  78125634 78125634
00163C  78125634 78125634
001644  78125634 78125634
00164C  78125634 78125634
001654  78125634 78125634
00165C  78125634 78125634
001664  78125634 78125634
00166C  78125634 78125634
001674  78125634 78125634
00167C  78125634 78125634
001684  78125634 78125634
00168C  78125634 78125634
001694  78125634 78125634
00169C  78125634 78125634
0016A4  78125634 78125634
0016AC  78125634 78125634
0016B4  78125634 78125634
0016BC  78125634 78125634
0016C4  78125634 78125634
0016CC  78125634 78125634
0016D4  78125634 78125634
0016DC  78125634 78125634
0016E4  78125634 78125634
0016EC  78125634 78125634
0016F4  78125634 78125634
0016FC  78125634 78125634
001704  78125634 78125634
00170C  78125634 78125634
001714  78125634 78125634
00171C  78125634 78125634
001724  78125634 000000F0

00172C  78125634 78125634                4620 TRTOP1F1 DC    127XL4'78125634',X'000000F1'    (CC1)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001734  78125634 78125634
00173C  78125634 78125634
001744  78125634 78125634
00174C  78125634 78125634
001754  78125634 78125634
00175C  78125634 78125634
001764  78125634 78125634
00176C  78125634 78125634
001774  78125634 78125634
00177C  78125634 78125634
001784  78125634 78125634
00178C  78125634 78125634
001794  78125634 78125634
00179C  78125634 78125634
0017A4  78125634 78125634
0017AC  78125634 78125634
0017B4  78125634 78125634
0017BC  78125634 78125634
0017C4  78125634 78125634
0017CC  78125634 78125634
0017D4  78125634 78125634
0017DC  78125634 78125634
0017E4  78125634 78125634
0017EC  78125634 78125634
0017F4  78125634 78125634
0017FC  78125634 78125634
001804  78125634 78125634
00180C  78125634 78125634
001814  78125634 78125634
00181C  78125634 78125634
001824  78125634 78125634
00182C  78125634 78125634
001834  78125634 78125634
00183C  78125634 78125634
001844  78125634 78125634
00184C  78125634 78125634
001854  78125634 78125634
00185C  78125634 78125634
001864  78125634 78125634
00186C  78125634 78125634
001874  78125634 78125634
00187C  78125634 78125634
001884  78125634 78125634
00188C  78125634 78125634
001894  78125634 78125634
00189C  78125634 78125634
0018A4  78125634 78125634
0018AC  78125634 78125634
0018B4  78125634 78125634
0018BC  78125634 78125634
0018C4  78125634 78125634
0018CC  78125634 78125634
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 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0018D4  78125634 78125634
0018DC  78125634 78125634
0018E4  78125634 78125634
0018EC  78125634 78125634
0018F4  78125634 78125634
0018FC  78125634 78125634
001904  78125634 78125634
00190C  78125634 78125634
001914  78125634 78125634
00191C  78125634 78125634
001924  78125634 000000F1

00192C  98765432 98765432                4622 TRTO1L0  DC    512XL4'98765432'    (CC0)
001934  98765432 98765432
00193C  98765432 98765432
001944  98765432 98765432
00194C  98765432 98765432
001954  98765432 98765432
00195C  98765432 98765432
001964  98765432 98765432
00196C  98765432 98765432
001974  98765432 98765432
00197C  98765432 98765432
001984  98765432 98765432
00198C  98765432 98765432
001994  98765432 98765432
00199C  98765432 98765432
0019A4  98765432 98765432
0019AC  98765432 98765432
0019B4  98765432 98765432
0019BC  98765432 98765432
0019C4  98765432 98765432
0019CC  98765432 98765432
0019D4  98765432 98765432
0019DC  98765432 98765432
0019E4  98765432 98765432
0019EC  98765432 98765432
0019F4  98765432 98765432
0019FC  98765432 98765432
001A04  98765432 98765432
001A0C  98765432 98765432
001A14  98765432 98765432
001A1C  98765432 98765432
001A24  98765432 98765432
001A2C  98765432 98765432
001A34  98765432 98765432
001A3C  98765432 98765432
001A44  98765432 98765432
001A4C  98765432 98765432
001A54  98765432 98765432
001A5C  98765432 98765432
001A64  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001A6C  98765432 98765432
001A74  98765432 98765432
001A7C  98765432 98765432
001A84  98765432 98765432
001A8C  98765432 98765432
001A94  98765432 98765432
001A9C  98765432 98765432
001AA4  98765432 98765432
001AAC  98765432 98765432
001AB4  98765432 98765432
001ABC  98765432 98765432
001AC4  98765432 98765432
001ACC  98765432 98765432
001AD4  98765432 98765432
001ADC  98765432 98765432
001AE4  98765432 98765432
001AEC  98765432 98765432
001AF4  98765432 98765432
001AFC  98765432 98765432
001B04  98765432 98765432
001B0C  98765432 98765432
001B14  98765432 98765432
001B1C  98765432 98765432
001B24  98765432 98765432
001B2C  98765432 98765432
001B34  98765432 98765432
001B3C  98765432 98765432
001B44  98765432 98765432
001B4C  98765432 98765432
001B54  98765432 98765432
001B5C  98765432 98765432
001B64  98765432 98765432
001B6C  98765432 98765432
001B74  98765432 98765432
001B7C  98765432 98765432
001B84  98765432 98765432
001B8C  98765432 98765432
001B94  98765432 98765432
001B9C  98765432 98765432
001BA4  98765432 98765432
001BAC  98765432 98765432
001BB4  98765432 98765432
001BBC  98765432 98765432
001BC4  98765432 98765432
001BCC  98765432 98765432
001BD4  98765432 98765432
001BDC  98765432 98765432
001BE4  98765432 98765432
001BEC  98765432 98765432
001BF4  98765432 98765432
001BFC  98765432 98765432
001C04  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001C0C  98765432 98765432
001C14  98765432 98765432
001C1C  98765432 98765432
001C24  98765432 98765432
001C2C  98765432 98765432
001C34  98765432 98765432
001C3C  98765432 98765432
001C44  98765432 98765432
001C4C  98765432 98765432
001C54  98765432 98765432
001C5C  98765432 98765432
001C64  98765432 98765432
001C6C  98765432 98765432
001C74  98765432 98765432
001C7C  98765432 98765432
001C84  98765432 98765432
001C8C  98765432 98765432
001C94  98765432 98765432
001C9C  98765432 98765432
001CA4  98765432 98765432
001CAC  98765432 98765432
001CB4  98765432 98765432
001CBC  98765432 98765432
001CC4  98765432 98765432
001CCC  98765432 98765432
001CD4  98765432 98765432
001CDC  98765432 98765432
001CE4  98765432 98765432
001CEC  98765432 98765432
001CF4  98765432 98765432
001CFC  98765432 98765432
001D04  98765432 98765432
001D0C  98765432 98765432
001D14  98765432 98765432
001D1C  98765432 98765432
001D24  98765432 98765432
001D2C  98765432 98765432
001D34  98765432 98765432
001D3C  98765432 98765432
001D44  98765432 98765432
001D4C  98765432 98765432
001D54  98765432 98765432
001D5C  98765432 98765432
001D64  98765432 98765432
001D6C  98765432 98765432
001D74  98765432 98765432
001D7C  98765432 98765432
001D84  98765432 98765432
001D8C  98765432 98765432
001D94  98765432 98765432
001D9C  98765432 98765432
001DA4  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001DAC  98765432 98765432
001DB4  98765432 98765432
001DBC  98765432 98765432
001DC4  98765432 98765432
001DCC  98765432 98765432
001DD4  98765432 98765432
001DDC  98765432 98765432
001DE4  98765432 98765432
001DEC  98765432 98765432
001DF4  98765432 98765432
001DFC  98765432 98765432
001E04  98765432 98765432
001E0C  98765432 98765432
001E14  98765432 98765432
001E1C  98765432 98765432
001E24  98765432 98765432
001E2C  98765432 98765432
001E34  98765432 98765432
001E3C  98765432 98765432
001E44  98765432 98765432
001E4C  98765432 98765432
001E54  98765432 98765432
001E5C  98765432 98765432
001E64  98765432 98765432
001E6C  98765432 98765432
001E74  98765432 98765432
001E7C  98765432 98765432
001E84  98765432 98765432
001E8C  98765432 98765432
001E94  98765432 98765432
001E9C  98765432 98765432
001EA4  98765432 98765432
001EAC  98765432 98765432
001EB4  98765432 98765432
001EBC  98765432 98765432
001EC4  98765432 98765432
001ECC  98765432 98765432
001ED4  98765432 98765432
001EDC  98765432 98765432
001EE4  98765432 98765432
001EEC  98765432 98765432
001EF4  98765432 98765432
001EFC  98765432 98765432
001F04  98765432 98765432
001F0C  98765432 98765432
001F14  98765432 98765432
001F1C  98765432 98765432
001F24  98765432 98765432
001F2C  98765432 98765432
001F34  98765432 98765432
001F3C  98765432 98765432
001F44  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001F4C  98765432 98765432
001F54  98765432 98765432
001F5C  98765432 98765432
001F64  98765432 98765432
001F6C  98765432 98765432
001F74  98765432 98765432
001F7C  98765432 98765432
001F84  98765432 98765432
001F8C  98765432 98765432
001F94  98765432 98765432
001F9C  98765432 98765432
001FA4  98765432 98765432
001FAC  98765432 98765432
001FB4  98765432 98765432
001FBC  98765432 98765432
001FC4  98765432 98765432
001FCC  98765432 98765432
001FD4  98765432 98765432
001FDC  98765432 98765432
001FE4  98765432 98765432
001FEC  98765432 98765432
001FF4  98765432 98765432
001FFC  98765432 98765432
002004  98765432 98765432
00200C  98765432 98765432
002014  98765432 98765432
00201C  98765432 98765432
002024  98765432 98765432
00202C  98765432 98765432
002034  98765432 98765432
00203C  98765432 98765432
002044  98765432 98765432
00204C  98765432 98765432
002054  98765432 98765432
00205C  98765432 98765432
002064  98765432 98765432
00206C  98765432 98765432
002074  98765432 98765432
00207C  98765432 98765432
002084  98765432 98765432
00208C  98765432 98765432
002094  98765432 98765432
00209C  98765432 98765432
0020A4  98765432 98765432
0020AC  98765432 98765432
0020B4  98765432 98765432
0020BC  98765432 98765432
0020C4  98765432 98765432
0020CC  98765432 98765432
0020D4  98765432 98765432
0020DC  98765432 98765432
0020E4  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0020EC  98765432 98765432
0020F4  98765432 98765432
0020FC  98765432 98765432
002104  98765432 98765432
00210C  98765432 98765432
002114  98765432 98765432
00211C  98765432 98765432
002124  98765432 98765432

00212C  98765432 98765432                4624 TRTO1L11 DC    256XL4'98765432',X'00110000',255XL4'98765432'    (CC1)
002134  98765432 98765432
00213C  98765432 98765432
002144  98765432 98765432
00214C  98765432 98765432
002154  98765432 98765432
00215C  98765432 98765432
002164  98765432 98765432
00216C  98765432 98765432
002174  98765432 98765432
00217C  98765432 98765432
002184  98765432 98765432
00218C  98765432 98765432
002194  98765432 98765432
00219C  98765432 98765432
0021A4  98765432 98765432
0021AC  98765432 98765432
0021B4  98765432 98765432
0021BC  98765432 98765432
0021C4  98765432 98765432
0021CC  98765432 98765432
0021D4  98765432 98765432
0021DC  98765432 98765432
0021E4  98765432 98765432
0021EC  98765432 98765432
0021F4  98765432 98765432
0021FC  98765432 98765432
002204  98765432 98765432
00220C  98765432 98765432
002214  98765432 98765432
00221C  98765432 98765432
002224  98765432 98765432
00222C  98765432 98765432
002234  98765432 98765432
00223C  98765432 98765432
002244  98765432 98765432
00224C  98765432 98765432
002254  98765432 98765432
00225C  98765432 98765432
002264  98765432 98765432
00226C  98765432 98765432
002274  98765432 98765432
00227C  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002284  98765432 98765432
00228C  98765432 98765432
002294  98765432 98765432
00229C  98765432 98765432
0022A4  98765432 98765432
0022AC  98765432 98765432
0022B4  98765432 98765432
0022BC  98765432 98765432
0022C4  98765432 98765432
0022CC  98765432 98765432
0022D4  98765432 98765432
0022DC  98765432 98765432
0022E4  98765432 98765432
0022EC  98765432 98765432
0022F4  98765432 98765432
0022FC  98765432 98765432
002304  98765432 98765432
00230C  98765432 98765432
002314  98765432 98765432
00231C  98765432 98765432
002324  98765432 98765432
00232C  98765432 98765432
002334  98765432 98765432
00233C  98765432 98765432
002344  98765432 98765432
00234C  98765432 98765432
002354  98765432 98765432
00235C  98765432 98765432
002364  98765432 98765432
00236C  98765432 98765432
002374  98765432 98765432
00237C  98765432 98765432
002384  98765432 98765432
00238C  98765432 98765432
002394  98765432 98765432
00239C  98765432 98765432
0023A4  98765432 98765432
0023AC  98765432 98765432
0023B4  98765432 98765432
0023BC  98765432 98765432
0023C4  98765432 98765432
0023CC  98765432 98765432
0023D4  98765432 98765432
0023DC  98765432 98765432
0023E4  98765432 98765432
0023EC  98765432 98765432
0023F4  98765432 98765432
0023FC  98765432 98765432
002404  98765432 98765432
00240C  98765432 98765432
002414  98765432 98765432
00241C  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002424  98765432 98765432
00242C  98765432 98765432
002434  98765432 98765432
00243C  98765432 98765432
002444  98765432 98765432
00244C  98765432 98765432
002454  98765432 98765432
00245C  98765432 98765432
002464  98765432 98765432
00246C  98765432 98765432
002474  98765432 98765432
00247C  98765432 98765432
002484  98765432 98765432
00248C  98765432 98765432
002494  98765432 98765432
00249C  98765432 98765432
0024A4  98765432 98765432
0024AC  98765432 98765432
0024B4  98765432 98765432
0024BC  98765432 98765432
0024C4  98765432 98765432
0024CC  98765432 98765432
0024D4  98765432 98765432
0024DC  98765432 98765432
0024E4  98765432 98765432
0024EC  98765432 98765432
0024F4  98765432 98765432
0024FC  98765432 98765432
002504  98765432 98765432
00250C  98765432 98765432
002514  98765432 98765432
00251C  98765432 98765432
002524  98765432 98765432
00252C  00110000 98765432
002534  98765432 98765432
00253C  98765432 98765432
002544  98765432 98765432
00254C  98765432 98765432
002554  98765432 98765432
00255C  98765432 98765432
002564  98765432 98765432
00256C  98765432 98765432
002574  98765432 98765432
00257C  98765432 98765432
002584  98765432 98765432
00258C  98765432 98765432
002594  98765432 98765432
00259C  98765432 98765432
0025A4  98765432 98765432
0025AC  98765432 98765432
0025B4  98765432 98765432
0025BC  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0025C4  98765432 98765432
0025CC  98765432 98765432
0025D4  98765432 98765432
0025DC  98765432 98765432
0025E4  98765432 98765432
0025EC  98765432 98765432
0025F4  98765432 98765432
0025FC  98765432 98765432
002604  98765432 98765432
00260C  98765432 98765432
002614  98765432 98765432
00261C  98765432 98765432
002624  98765432 98765432
00262C  98765432 98765432
002634  98765432 98765432
00263C  98765432 98765432
002644  98765432 98765432
00264C  98765432 98765432
002654  98765432 98765432
00265C  98765432 98765432
002664  98765432 98765432
00266C  98765432 98765432
002674  98765432 98765432
00267C  98765432 98765432
002684  98765432 98765432
00268C  98765432 98765432
002694  98765432 98765432
00269C  98765432 98765432
0026A4  98765432 98765432
0026AC  98765432 98765432
0026B4  98765432 98765432
0026BC  98765432 98765432
0026C4  98765432 98765432
0026CC  98765432 98765432
0026D4  98765432 98765432
0026DC  98765432 98765432
0026E4  98765432 98765432
0026EC  98765432 98765432
0026F4  98765432 98765432
0026FC  98765432 98765432
002704  98765432 98765432
00270C  98765432 98765432
002714  98765432 98765432
00271C  98765432 98765432
002724  98765432 98765432
00272C  98765432 98765432
002734  98765432 98765432
00273C  98765432 98765432
002744  98765432 98765432
00274C  98765432 98765432
002754  98765432 98765432
00275C  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002764  98765432 98765432
00276C  98765432 98765432
002774  98765432 98765432
00277C  98765432 98765432
002784  98765432 98765432
00278C  98765432 98765432
002794  98765432 98765432
00279C  98765432 98765432
0027A4  98765432 98765432
0027AC  98765432 98765432
0027B4  98765432 98765432
0027BC  98765432 98765432
0027C4  98765432 98765432
0027CC  98765432 98765432
0027D4  98765432 98765432
0027DC  98765432 98765432
0027E4  98765432 98765432
0027EC  98765432 98765432
0027F4  98765432 98765432
0027FC  98765432 98765432
002804  98765432 98765432
00280C  98765432 98765432
002814  98765432 98765432
00281C  98765432 98765432
002824  98765432 98765432
00282C  98765432 98765432
002834  98765432 98765432
00283C  98765432 98765432
002844  98765432 98765432
00284C  98765432 98765432
002854  98765432 98765432
00285C  98765432 98765432
002864  98765432 98765432
00286C  98765432 98765432
002874  98765432 98765432
00287C  98765432 98765432
002884  98765432 98765432
00288C  98765432 98765432
002894  98765432 98765432
00289C  98765432 98765432
0028A4  98765432 98765432
0028AC  98765432 98765432
0028B4  98765432 98765432
0028BC  98765432 98765432
0028C4  98765432 98765432
0028CC  98765432 98765432
0028D4  98765432 98765432
0028DC  98765432 98765432
0028E4  98765432 98765432
0028EC  98765432 98765432
0028F4  98765432 98765432
0028FC  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002904  98765432 98765432
00290C  98765432 98765432
002914  98765432 98765432
00291C  98765432 98765432
002924  98765432 98765432

00292C  98765432 98765432                  4626 TRTO1LF0 DC    511XL4'98765432',X'000000F0'    (CC1)
002934  98765432 98765432
00293C  98765432 98765432
002944  98765432 98765432
00294C  98765432 98765432
002954  98765432 98765432
00295C  98765432 98765432
002964  98765432 98765432
00296C  98765432 98765432
002974  98765432 98765432
00297C  98765432 98765432
002984  98765432 98765432
00298C  98765432 98765432
002994  98765432 98765432
00299C  98765432 98765432
0029A4  98765432 98765432
0029AC  98765432 98765432
0029B4  98765432 98765432
0029BC  98765432 98765432
0029C4  98765432 98765432
0029CC  98765432 98765432
0029D4  98765432 98765432
0029DC  98765432 98765432
0029E4  98765432 98765432
0029EC  98765432 98765432
0029F4  98765432 98765432
0029FC  98765432 98765432
002A04  98765432 98765432
002A0C  98765432 98765432
002A14  98765432 98765432
002A1C  98765432 98765432
002A24  98765432 98765432
002A2C  98765432 98765432
002A34  98765432 98765432
002A3C  98765432 98765432
002A44  98765432 98765432
002A4C  98765432 98765432
002A54  98765432 98765432
002A5C  98765432 98765432
002A64  98765432 98765432
002A6C  98765432 98765432
002A74  98765432 98765432
002A7C  98765432 98765432
002A84  98765432 98765432
002A8C  98765432 98765432
002A94  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002A9C  98765432 98765432
002AA4  98765432 98765432
002AAC  98765432 98765432
002AB4  98765432 98765432
002ABC  98765432 98765432
002AC4  98765432 98765432
002ACC  98765432 98765432
002AD4  98765432 98765432
002ADC  98765432 98765432
002AE4  98765432 98765432
002AEC  98765432 98765432
002AF4  98765432 98765432
002AFC  98765432 98765432
002B04  98765432 98765432
002B0C  98765432 98765432
002B14  98765432 98765432
002B1C  98765432 98765432
002B24  98765432 98765432
002B2C  98765432 98765432
002B34  98765432 98765432
002B3C  98765432 98765432
002B44  98765432 98765432
002B4C  98765432 98765432
002B54  98765432 98765432
002B5C  98765432 98765432
002B64  98765432 98765432
002B6C  98765432 98765432
002B74  98765432 98765432
002B7C  98765432 98765432
002B84  98765432 98765432
002B8C  98765432 98765432
002B94  98765432 98765432
002B9C  98765432 98765432
002BA4  98765432 98765432
002BAC  98765432 98765432
002BB4  98765432 98765432
002BBC  98765432 98765432
002BC4  98765432 98765432
002BCC  98765432 98765432
002BD4  98765432 98765432
002BDC  98765432 98765432
002BE4  98765432 98765432
002BEC  98765432 98765432
002BF4  98765432 98765432
002BFC  98765432 98765432
002C04  98765432 98765432
002C0C  98765432 98765432
002C14  98765432 98765432
002C1C  98765432 98765432
002C24  98765432 98765432
002C2C  98765432 98765432
002C34  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002C3C  98765432 98765432
002C44  98765432 98765432
002C4C  98765432 98765432
002C54  98765432 98765432
002C5C  98765432 98765432
002C64  98765432 98765432
002C6C  98765432 98765432
002C74  98765432 98765432
002C7C  98765432 98765432
002C84  98765432 98765432
002C8C  98765432 98765432
002C94  98765432 98765432
002C9C  98765432 98765432
002CA4  98765432 98765432
002CAC  98765432 98765432
002CB4  98765432 98765432
002CBC  98765432 98765432
002CC4  98765432 98765432
002CCC  98765432 98765432
002CD4  98765432 98765432
002CDC  98765432 98765432
002CE4  98765432 98765432
002CEC  98765432 98765432
002CF4  98765432 98765432
002CFC  98765432 98765432
002D04  98765432 98765432
002D0C  98765432 98765432
002D14  98765432 98765432
002D1C  98765432 98765432
002D24  98765432 98765432
002D2C  98765432 98765432
002D34  98765432 98765432
002D3C  98765432 98765432
002D44  98765432 98765432
002D4C  98765432 98765432
002D54  98765432 98765432
002D5C  98765432 98765432
002D64  98765432 98765432
002D6C  98765432 98765432
002D74  98765432 98765432
002D7C  98765432 98765432
002D84  98765432 98765432
002D8C  98765432 98765432
002D94  98765432 98765432
002D9C  98765432 98765432
002DA4  98765432 98765432
002DAC  98765432 98765432
002DB4  98765432 98765432
002DBC  98765432 98765432
002DC4  98765432 98765432
002DCC  98765432 98765432
002DD4  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002DDC  98765432 98765432
002DE4  98765432 98765432
002DEC  98765432 98765432
002DF4  98765432 98765432
002DFC  98765432 98765432
002E04  98765432 98765432
002E0C  98765432 98765432
002E14  98765432 98765432
002E1C  98765432 98765432
002E24  98765432 98765432
002E2C  98765432 98765432
002E34  98765432 98765432
002E3C  98765432 98765432
002E44  98765432 98765432
002E4C  98765432 98765432
002E54  98765432 98765432
002E5C  98765432 98765432
002E64  98765432 98765432
002E6C  98765432 98765432
002E74  98765432 98765432
002E7C  98765432 98765432
002E84  98765432 98765432
002E8C  98765432 98765432
002E94  98765432 98765432
002E9C  98765432 98765432
002EA4  98765432 98765432
002EAC  98765432 98765432
002EB4  98765432 98765432
002EBC  98765432 98765432
002EC4  98765432 98765432
002ECC  98765432 98765432
002ED4  98765432 98765432
002EDC  98765432 98765432
002EE4  98765432 98765432
002EEC  98765432 98765432
002EF4  98765432 98765432
002EFC  98765432 98765432
002F04  98765432 98765432
002F0C  98765432 98765432
002F14  98765432 98765432
002F1C  98765432 98765432
002F24  98765432 98765432
002F2C  98765432 98765432
002F34  98765432 98765432
002F3C  98765432 98765432
002F44  98765432 98765432
002F4C  98765432 98765432
002F54  98765432 98765432
002F5C  98765432 98765432
002F64  98765432 98765432
002F6C  98765432 98765432
002F74  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002F7C  98765432 98765432
002F84  98765432 98765432
002F8C  98765432 98765432
002F94  98765432 98765432
002F9C  98765432 98765432
002FA4  98765432 98765432
002FAC  98765432 98765432
002FB4  98765432 98765432
002FBC  98765432 98765432
002FC4  98765432 98765432
002FCC  98765432 98765432
002FD4  98765432 98765432
002FDC  98765432 98765432
002FE4  98765432 98765432
002FEC  98765432 98765432
002FF4  98765432 98765432
002FFC  98765432 98765432
003004  98765432 98765432
00300C  98765432 98765432
003014  98765432 98765432
00301C  98765432 98765432
003024  98765432 98765432
00302C  98765432 98765432
003034  98765432 98765432
00303C  98765432 98765432
003044  98765432 98765432
00304C  98765432 98765432
003054  98765432 98765432
00305C  98765432 98765432
003064  98765432 98765432
00306C  98765432 98765432
003074  98765432 98765432
00307C  98765432 98765432
003084  98765432 98765432
00308C  98765432 98765432
003094  98765432 98765432
00309C  98765432 98765432
0030A4  98765432 98765432
0030AC  98765432 98765432
0030B4  98765432 98765432
0030BC  98765432 98765432
0030C4  98765432 98765432
0030CC  98765432 98765432
0030D4  98765432 98765432
0030DC  98765432 98765432
0030E4  98765432 98765432
0030EC  98765432 98765432
0030F4  98765432 98765432
0030FC  98765432 98765432
003104  98765432 98765432
00310C  98765432 98765432
003114  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00311C  98765432 98765432
003124  98765432 000000F0
                                          4627
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     4629 ************************************************************************
                                     4630 *        Function Code (FC) Tables (GR1)
                                     4631 ************************************************************************

00312C  00000000 00000000            4633 TRTOP20  DC    256X'00'            no stop
003134  00000000 00000000
00313C  00000000 00000000
003144  00000000 00000000
00314C  00000000 00000000
003154  00000000 00000000
00315C  00000000 00000000
003164  00000000 00000000
00316C  00000000 00000000
003174  00000000 00000000
00317C  00000000 00000000
003184  00000000 00000000
00318C  00000000 00000000
003194  00000000 00000000
00319C  00000000 00000000
0031A4  00000000 00000000
0031AC  00000000 00000000
0031B4  00000000 00000000
0031BC  00000000 00000000
0031C4  00000000 00000000
0031CC  00000000 00000000
0031D4  00000000 00000000
0031DC  00000000 00000000
0031E4  00000000 00000000
0031EC  00000000 00000000
0031F4  00000000 00000000
0031FC  00000000 00000000
003204  00000000 00000000
00320C  00000000 00000000
003214  00000000 00000000
00321C  00000000 00000000
003224  00000000 00000000
00322C                       00322C  02322C  4634          ORG   *+2*K64

02322C  00000000 00000000            4636 TRTOP211 DC    17X'00',X'11',238X'00'     stop on X'11'
023234  00000000 00000000
02323C  00110000 00000000
023244  00000000 00000000
02324C  00000000 00000000
023254  00000000 00000000
02325C  00000000 00000000
023264  00000000 00000000
02326C  00000000 00000000
023274  00000000 00000000
02327C  00000000 00000000
023284  00000000 00000000
02328C  00000000 00000000
023294  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

02329C  00000000 00000000
0232A4  00000000 00000000
0232AC  00000000 00000000
0232B4  00000000 00000000
0232BC  00000000 00000000
0232C4  00000000 00000000
0232CC  00000000 00000000
0232D4  00000000 00000000
0232DC  00000000 00000000
0232E4  00000000 00000000
0232EC  00000000 00000000
0232F4  00000000 00000000
0232FC  00000000 00000000
023304  00000000 00000000
02330C  00000000 00000000
023314  00000000 00000000
02331C  00000000 00000000
023324  00000000 00000000

02332C  00000000 00000000                4638 TRTOP2F0 DC    240X'00',X'F0',15X'00'     stop on X'F0'
023334  00000000 00000000
02333C  00000000 00000000
023344  00000000 00000000
02334C  00000000 00000000
023354  00000000 00000000
02335C  00000000 00000000
023364  00000000 00000000
02336C  00000000 00000000
023374  00000000 00000000
02337C  00000000 00000000
023384  00000000 00000000
02338C  00000000 00000000
023394  00000000 00000000
02339C  00000000 00000000
0233A4  00000000 00000000
0233AC  00000000 00000000
0233B4  00000000 00000000
0233BC  00000000 00000000
0233C4  00000000 00000000
0233CC  00000000 00000000
0233D4  00000000 00000000
0233DC  00000000 00000000
0233E4  00000000 00000000
0233EC  00000000 00000000
0233F4  00000000 00000000
0233FC  00000000 00000000
023404  00000000 00000000
02340C  00000000 00000000
023414  00000000 00000000
02341C  F0000000 00000000
023424  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

02342C  00000000 00000000                  4640 TRTOP411 DC    34X'00',X'0011',476X'00'   stop on X'11'
023434  00000000 00000000
02343C  00000000 00000000
023444  00000000 00000000
02344C  00000011 00000000
023454  00000000 00000000
02345C  00000000 00000000
023464  00000000 00000000
02346C  00000000 00000000
023474  00000000 00000000
02347C  00000000 00000000
023484  00000000 00000000
02348C  00000000 00000000
023494  00000000 00000000
02349C  00000000 00000000
0234A4  00000000 00000000
0234AC  00000000 00000000
0234B4  00000000 00000000
0234BC  00000000 00000000
0234C4  00000000 00000000
0234CC  00000000 00000000
0234D4  00000000 00000000
0234DC  00000000 00000000
0234E4  00000000 00000000
0234EC  00000000 00000000
0234F4  00000000 00000000
0234FC  00000000 00000000
023504  00000000 00000000
02350C  00000000 00000000
023514  00000000 00000000
02351C  00000000 00000000
023524  00000000 00000000
02352C  00000000 00000000
023534  00000000 00000000
02353C  00000000 00000000
023544  00000000 00000000
02354C  00000000 00000000
023554  00000000 00000000
02355C  00000000 00000000
023564  00000000 00000000
02356C  00000000 00000000
023574  00000000 00000000
02357C  00000000 00000000
023584  00000000 00000000
02358C  00000000 00000000
023594  00000000 00000000
02359C  00000000 00000000
0235A4  00000000 00000000
0235AC  00000000 00000000
0235B4  00000000 00000000
0235BC  00000000 00000000
0235C4  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0235CC  00000000 00000000
0235D4  00000000 00000000
0235DC  00000000 00000000
0235E4  00000000 00000000
0235EC  00000000 00000000
0235F4  00000000 00000000
0235FC  00000000 00000000
023604  00000000 00000000
02360C  00000000 00000000
023614  00000000 00000000
02361C  00000000 00000000
023624  00000000 00000000

02362C  00000000 00000000                  4642 TRTOP4F0 DC    480X'00',X'00F0',30X'00'     stop on X'F0'
023634  00000000 00000000
02363C  00000000 00000000
023644  00000000 00000000
02364C  00000000 00000000
023654  00000000 00000000
02365C  00000000 00000000
023664  00000000 00000000
02366C  00000000 00000000
023674  00000000 00000000
02367C  00000000 00000000
023684  00000000 00000000
02368C  00000000 00000000
023694  00000000 00000000
02369C  00000000 00000000
0236A4  00000000 00000000
0236AC  00000000 00000000
0236B4  00000000 00000000
0236BC  00000000 00000000
0236C4  00000000 00000000
0236CC  00000000 00000000
0236D4  00000000 00000000
0236DC  00000000 00000000
0236E4  00000000 00000000
0236EC  00000000 00000000
0236F4  00000000 00000000
0236FC  00000000 00000000
023704  00000000 00000000
02370C  00000000 00000000
023714  00000000 00000000
02371C  00000000 00000000
023724  00000000 00000000
02372C  00000000 00000000
023734  00000000 00000000
02373C  00000000 00000000
023744  00000000 00000000
02374C  00000000 00000000
023754  00000000 00000000
02375C  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

023764  00000000 00000000
02376C  00000000 00000000
023774  00000000 00000000
02377C  00000000 00000000
023784  00000000 00000000
02378C  00000000 00000000
023794  00000000 00000000
02379C  00000000 00000000
0237A4  00000000 00000000
0237AC  00000000 00000000
0237B4  00000000 00000000
0237BC  00000000 00000000
0237C4  00000000 00000000
0237CC  00000000 00000000
0237D4  00000000 00000000
0237DC  00000000 00000000
0237E4  00000000 00000000
0237EC  00000000 00000000
0237F4  00000000 00000000
0237FC  00000000 00000000
023804  00000000 00000000
02380C  00F00000 00000000
023814  00000000 00000000
02381C  00000000 00000000
023824  00000000 00000000
                                      4643
02382C  00000000 00000000                    4644 TRTOP811 DC    17X'00',X'11',238X'00'       stop on X'11'
023834  00000000 00000000
02383C  00110000 00000000
023844  00000000 00000000
02384C  00000000 00000000
023854  00000000 00000000
02385C  00000000 00000000
023864  00000000 00000000
02386C  00000000 00000000
023874  00000000 00000000
02387C  00000000 00000000
023884  00000000 00000000
02388C  00000000 00000000
023894  00000000 00000000
02389C  00000000 00000000
0238A4  00000000 00000000
0238AC  00000000 00000000
0238B4  00000000 00000000
0238BC  00000000 00000000
0238C4  00000000 00000000
0238CC  00000000 00000000
0238D4  00000000 00000000
0238DC  00000000 00000000
0238E4  00000000 00000000
0238EC  00000000 00000000
0238F4  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0238FC  00000000 00000000
023904  00000000 00000000
02390C  00000000 00000000
023914  00000000 00000000
02391C  00000000 00000000
023924  00000000 00000000
02392C                   02392C  04392C  4645          ORG   *+2*K64
                                         4646
04392C  00000000 00000000                4647 TRTOP8F0 DC    240X'00',X'F0',15X'00'     stop on X'F0'
043934  00000000 00000000
04393C  00000000 00000000
043944  00000000 00000000
04394C  00000000 00000000
043954  00000000 00000000
04395C  00000000 00000000
043964  00000000 00000000
04396C  00000000 00000000
043974  00000000 00000000
04397C  00000000 00000000
043984  00000000 00000000
04398C  00000000 00000000
043994  00000000 00000000
04399C  00000000 00000000
0439A4  00000000 00000000
0439AC  00000000 00000000
0439B4  00000000 00000000
0439BC  00000000 00000000
0439C4  00000000 00000000
0439CC  00000000 00000000
0439D4  00000000 00000000
0439DC  00000000 00000000
0439E4  00000000 00000000
0439EC  00000000 00000000
0439F4  00000000 00000000
0439FC  00000000 00000000
043A04  00000000 00000000
043A0C  00000000 00000000
043A14  00000000 00000000
043A1C  F0000000 00000000
043A24  00000000 00000000
043A2C                   043A2C  063A2C  4648          ORG   *+2*K64
                                         4649
063A2C  00000000 00000000                4650 TRTOP8F1 DC    240X'00',X'00',X'F1',14X'00'     stop on X'F1'
063A34  00000000 00000000
063A3C  00000000 00000000
063A44  00000000 00000000
063A4C  00000000 00000000
063A54  00000000 00000000
063A5C  00000000 00000000
063A64  00000000 00000000
063A6C  00000000 00000000
063A74  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

063A7C  00000000 00000000
063A84  00000000 00000000
063A8C  00000000 00000000
063A94  00000000 00000000
063A9C  00000000 00000000
063AA4  00000000 00000000
063AAC  00000000 00000000
063AB4  00000000 00000000
063ABC  00000000 00000000
063AC4  00000000 00000000
063ACC  00000000 00000000
063AD4  00000000 00000000
063ADC  00000000 00000000
063AE4  00000000 00000000
063AEC  00000000 00000000
063AF4  00000000 00000000
063AFC  00000000 00000000
063B04  00000000 00000000
063B0C  00000000 00000000
063B14  00000000 00000000
063B1C  00F10000 00000000
063B24  00000000 00000000
063B2C                   063B2C  083B2C  4651          ORG   *+2*K64
                                         4652
083B2C  00000000 00000000                4653 TRTOPC11 DC    34X'00',X'0011'        stop on X'11'
083B34  00000000 00000000
083B3C  00000000 00000000
083B44  00000000 00000000
083B4C  00000011
083B50                   083B50  0A3B50  4654          ORG   *+2*K64
                                         4655
                                         4656
0A3B50  00000000 00000000                4657 TRTOPCF0 DC    480X'00',X'00F0',28X'00'     stop on X'F0'
0A3B58  00000000 00000000
0A3B60  00000000 00000000
0A3B68  00000000 00000000
0A3B70  00000000 00000000
0A3B78  00000000 00000000
0A3B80  00000000 00000000
0A3B88  00000000 00000000
0A3B90  00000000 00000000
0A3B98  00000000 00000000
0A3BA0  00000000 00000000
0A3BA8  00000000 00000000
0A3BB0  00000000 00000000
0A3BB8  00000000 00000000
0A3BC0  00000000 00000000
0A3BC8  00000000 00000000
0A3BD0  00000000 00000000
0A3BD8  00000000 00000000
0A3BE0  00000000 00000000
0A3BE8  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0A3BF0  00000000 00000000
0A3BF8  00000000 00000000
0A3C00  00000000 00000000
0A3C08  00000000 00000000
0A3C10  00000000 00000000
0A3C18  00000000 00000000
0A3C20  00000000 00000000
0A3C28  00000000 00000000
0A3C30  00000000 00000000
0A3C38  00000000 00000000
0A3C40  00000000 00000000
0A3C48  00000000 00000000
0A3C50  00000000 00000000
0A3C58  00000000 00000000
0A3C60  00000000 00000000
0A3C68  00000000 00000000
0A3C70  00000000 00000000
0A3C78  00000000 00000000
0A3C80  00000000 00000000
0A3C88  00000000 00000000
0A3C90  00000000 00000000
0A3C98  00000000 00000000
0A3CA0  00000000 00000000
0A3CA8  00000000 00000000
0A3CB0  00000000 00000000
0A3CB8  00000000 00000000
0A3CC0  00000000 00000000
0A3CC8  00000000 00000000
0A3CD0  00000000 00000000
0A3CD8  00000000 00000000
0A3CE0  00000000 00000000
0A3CE8  00000000 00000000
0A3CF0  00000000 00000000
0A3CF8  00000000 00000000
0A3D00  00000000 00000000
0A3D08  00000000 00000000
0A3D10  00000000 00000000
0A3D18  00000000 00000000
0A3D20  00000000 00000000
0A3D28  00000000 00000000
0A3D30  00F00000 00000000
0A3D38  00000000 00000000
0A3D40  00000000 00000000
0A3D48  00000000 0000
0A3D4E                   0A3D4E  0C3D4E  4658          ORG    *+2*K64
                                         4659
0C3D4E  00000000 00000000                4660 TRTOPCF1 DC     480X'00',X'0000',X'00F1',28X'00'     stop on X'F1'
0C3D56  00000000 00000000
0C3D5E  00000000 00000000
0C3D66  00000000 00000000
0C3D6E  00000000 00000000
0C3D76  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0C3D7E  00000000 00000000
0C3D86  00000000 00000000
0C3D8E  00000000 00000000
0C3D96  00000000 00000000
0C3D9E  00000000 00000000
0C3DA6  00000000 00000000
0C3DAE  00000000 00000000
0C3DB6  00000000 00000000
0C3DBE  00000000 00000000
0C3DC6  00000000 00000000
0C3DCE  00000000 00000000
0C3DD6  00000000 00000000
0C3DDE  00000000 00000000
0C3DE6  00000000 00000000
0C3DEE  00000000 00000000
0C3DF6  00000000 00000000
0C3DFE  00000000 00000000
0C3E06  00000000 00000000
0C3E0E  00000000 00000000
0C3E16  00000000 00000000
0C3E1E  00000000 00000000
0C3E26  00000000 00000000
0C3E2E  00000000 00000000
0C3E36  00000000 00000000
0C3E3E  00000000 00000000
0C3E46  00000000 00000000
0C3E4E  00000000 00000000
0C3E56  00000000 00000000
0C3E5E  00000000 00000000
0C3E66  00000000 00000000
0C3E6E  00000000 00000000
0C3E76  00000000 00000000
0C3E7E  00000000 00000000
0C3E86  00000000 00000000
0C3E8E  00000000 00000000
0C3E96  00000000 00000000
0C3E9E  00000000 00000000
0C3EA6  00000000 00000000
0C3EAE  00000000 00000000
0C3EB6  00000000 00000000
0C3EBE  00000000 00000000
0C3EC6  00000000 00000000
0C3ECE  00000000 00000000
0C3ED6  00000000 00000000
0C3EDE  00000000 00000000
0C3EE6  00000000 00000000
0C3EEE  00000000 00000000
0C3EF6  00000000 00000000
0C3EFE  00000000 00000000
0C3F06  00000000 00000000
0C3F0E  00000000 00000000
0C3F16  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0C3F1E  00000000 00000000
0C3F26  00000000 00000000
0C3F2E  000000F1 00000000
0C3F36  00000000 00000000
0C3F3E  00000000 00000000
0C3F46  00000000 00000000
0C3F4E                   0C3F4E  0E3F4E  4661          ORG   *+2*K64
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                      4663 ***********************************************************************
                                      4664 *        (other DSECTS needed by SATK)
                                      4665 ***********************************************************************

                                      4667          DSECTS PRINT=OFF,NAME=(ASA)
                                      4830          PRINT ON

                                      4832 ***********************************************************************
                                      4833 *        Register equates
                                      4834 ***********************************************************************


                            000000  000001  4836 R0       EQU   0
                            000001  000001  4837 R1       EQU   1
                            000002  000001  4838 R2       EQU   2
                            000003  000001  4839 R3       EQU   3
                            000004  000001  4840 R4       EQU   4
                            000005  000001  4841 R5       EQU   5
                            000006  000001  4842 R6       EQU   6
                            000007  000001  4843 R7       EQU   7
                            000008  000001  4844 R8       EQU   8
                            000009  000001  4845 R9       EQU   9
                            00000A  000001  4846 R10      EQU   10
                            00000B  000001  4847 R11      EQU   11
                            00000C  000001  4848 R12      EQU   12
                            00000D  000001  4849 R13      EQU   13
                            00000E  000001  4850 R14      EQU   14
                            00000F  000001  4851 R15      EQU   15

                                      4853          END
```

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
ASA	4	00000000	512	4671	3522
ASBEGIN	U	00000000	1	4672	4677 4719 4755 4764 4782 4789 4795 4799 4803 4809 4826
ASEND	U	00000200	1	4825	4826
ASLENGTH	U	00000200	1	4826	
BCEXTCOD	H	0000001A	2	4689	
BCIOCOD	H	0000003A	2	4697	
BCMCKCOD	H	00000032	2	4695	
BCPGMCOD	H	0000002A	2	4693	
BCSVCCOD	H	00000022	2	4691	
BEGIN	I	00000200	2	3526	3554 3496 3523 3524 3638
CAW	F	00000048	4	4701	
CAWADDR	R	00000049	3	4704	
CAWKEY	X	00000048	1	4702	
CAWSUSP	U	00000008	1	4703	
CHANID	F	000000A8	4	4756	
CODE	2	00000000	933710	3477	
CPUID	U	0000031B	1	4828	
CSW	F	00000040	8	4700	
DWAT0008	3	000005D0	8	3648	3647
DWAT0009	3	000005E0	8	3653	3652
ENDREGS	A	00000028	4	3693	3610
EOJ	H	000005C8	2	3646	3548
EXTCPUAD	H	00000084	2	4721	
EXTICODE	H	00000086	2	4722	
EXTIPARM	F	00000080	4	4720	
EXTNPSW	F	00000058	8	4710	
EXTOPSW	F	00000018	8	4682	4688
F12T11	F	000013BC	4	4581	
F12T11A	F	000013F0	4	4593	
F12T8	F	00001354	4	4557	
F12T8A	F	00001388	4	4569	
FAILMASK	A	00000024	4	3690	3598
FAILTEST	H	000005D8	2	3651	3543 3546 3629
IMAGE	1	00000000	933710	0	
IOELADDR	F	000000AC	4	4757	
IOICODE	H	000000BA	2	4762	
IOIID	F	000000C0	4	4767	
IOIPARM	F	000000BC	4	4766	
IONPSW	F	00000078	8	4714	
IOOPSW	F	00000038	8	4686	4696
IOSSID	F	000000B8	4	4765	
IPLCCW1	F	00000008	8	4674	
IPLCCW2	F	00000010	8	4675	
IPLPSW	F	00000000	8	4673	
K	U	00000400	1	3662	3663 3664 3665
K64	U	00010000	1	3664	4634 4645 4648 4651 4654 4658 4661 3722 3725 3734 3737 3746 3749
					3758 3761 3770 3773 3782 3785 3794 3797 3807 3810 3819 3822 3831
					3834 3843 3846 3860 3863 3872 3875 3884 3887 3896 3899 3908 3911
					3920 3923 3932 3935 3944 3947 3956 3959 3968 3971 3980 3983 3997
					3999 4002 4010 4012 4015 4022 4024 4027 4034 4036 4039 4046 4048
					4051 4058 4060 4063 4070 4072 4075 4082 4084 4087 4094 4096 4099
					4106 4108 4111 4118 4120 4123 4141 4144 4153 4156 4165 4168 4177

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
					4180 4189 4192 4201 4204 4213 4216 4225 4228 4237 4240 4249 4252
					4261 4264 4279 4281 4284 4292 4294 4297 4304 4306 4309 4316 4318
					4321 4328 4330 4333 4340 4342 4345 4352 4354 4357 4364 4366 4369
					4376 4378 4381 4388 4390 4393 4400 4402 4405 4423 4426 4435 4438
					4447 4450 4459 4462 4471 4474 4483 4486 4495 4498 4507 4510 4519
					4522 4531 4534 4543 4546 4562 4564 4567 4574 4576 4579 4586 4588
					4591 4598 4600 4603
LCHANLOG	F	000000B0	4	4758	
M0T1	F	000005EC	4	3715	
M0T10	F	000007C0	4	3824	
M0T11	F	000007F4	4	3836	
M0T2	F	00000620	4	3727	
M0T3	F	00000654	4	3739	
M0T4	F	00000688	4	3751	
M0T5	F	000006BC	4	3763	
M0T6	F	000006F0	4	3775	
M0T7	F	00000724	4	3787	
M0T8	F	00000758	4	3800	
M0T9	F	0000078C	4	3812	
M10T1	F	00000CA0	4	4134	
M10T10	F	00000E74	4	4242	
M10T11	F	00000EA8	4	4254	
M10T2	F	00000CD4	4	4146	
M10T3	F	00000D08	4	4158	
M10T4	F	00000D3C	4	4170	
M10T5	F	00000D70	4	4182	
M10T6	F	00000DA4	4	4194	
M10T7	F	00000DD8	4	4206	
M10T8	F	00000E0C	4	4218	
M10T9	F	00000E40	4	4230	
M12T1	F	00000EDC	4	4274	
M12T10	F	000010B0	4	4383	
M12T11	F	000010E4	4	4395	
M12T2	F	00000F10	4	4287	
M12T3	F	00000F44	4	4299	
M12T4	F	00000F78	4	4311	
M12T5	F	00000FAC	4	4323	
M12T6	F	00000FE0	4	4335	
M12T7	F	00001014	4	4347	
M12T8	F	00001048	4	4359	
M12T9	F	0000107C	4	4371	
M14T1	F	00001118	4	4416	
M14T10	F	000012EC	4	4524	
M14T11	F	00001320	4	4536	
M14T2	F	0000114C	4	4428	
M14T3	F	00001180	4	4440	
M14T4	F	000011B4	4	4452	
M14T5	F	000011E8	4	4464	
M14T6	F	0000121C	4	4476	
M14T7	F	00001250	4	4488	
M14T8	F	00001284	4	4500	
M14T9	F	000012B8	4	4512	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
M3	X	00000003	1	3677	3595
M4T1	F	00000828	4	3853	
M4T10	F	000009FC	4	3961	
M4T11	F	00000A30	4	3973	
M4T2	F	0000085C	4	3865	
M4T3	F	00000890	4	3877	
M4T4	F	000008C4	4	3889	
M4T5	F	000008F8	4	3901	
M4T6	F	0000092C	4	3913	
M4T7	F	00000960	4	3925	
M4T8	F	00000994	4	3937	
M4T9	F	000009C8	4	3949	
M8T1	F	00000A64	4	3992	
M8T10	F	00000C38	4	4101	
M8T11	F	00000C6C	4	4113	
M8T2	F	00000A98	4	4005	
M8T3	F	00000ACC	4	4017	
M8T4	F	00000B00	4	4029	
M8T5	F	00000B34	4	4041	
M8T6	F	00000B68	4	4053	
M8T7	F	00000B9C	4	4065	
M8T8	F	00000BD0	4	4077	
M8T9	F	00000C04	4	4089	
MB	U	00100000	1	3665	3722 3725 3734 3737 3746 3749 3758 3761 3770 3773 3782 3785 3794 3797 3807 3810 3819 3822 3831 3834 3843 3846 3860 3863 3872 3875 3884 3887 3896 3899 3908 3911 3920 3923 3932 3935 3944 3947 3956 3959 3968 3971 3980 3983 3999 4002 4012 4015 4024 4027 4036 4039 4048 4051 4060 4063 4072 4075 4084 4087 4096 4099 4108 4111 4120 4123 4141 4144 4153 4156 4165 4168 4177 4180 4189 4192 4201 4204 4213 4216 4225 4228 4237 4240 4249 4252 4261 4264 4281 4284 4294 4297 4306 4309 4318 4321 4330 4333 4342 4345 4354 4357 4366 4369 4378 4381 4390 4393 4402 4405 4423 4426 4435 4438 4447 4450 4459 4462 4471 4474 4483 4486 4495 4498 4507 4510 4519 4522 4531 4534 4543 4546 4564 4567 4576 4579 4588 4591 4600 4603
MCKLOG	F	00000100	4	4790	
MCKNPSW	F	00000070	8	4713	
MCKOPSW	F	00000030	8	4685	4694
MEASUREB	X	000000B9	1	4761	
MKARCHMD	X	000000A3	1	4749	
MKARS	F	00000120	4	4788	
MKCLKCMP	F	000000E0	8	4774	
MKCPUTIM	F	000000D8	8	4773	
MKCRS	F	000001C0	4	4793	
MKDMGCOD	F	000000F4	4	4777	
MKFAILA	F	000000F8	4	4779	
MKFPRS	D	00000160	8	4791	
MKICODE	F	000000E8	4	4775	
MKLOGOUT	F	00000100	4	4781	
MKMODEL	F	000000FC	4	4780	
MKXSAA	F	000000D4	4	4772	
MONCLS	H	00000094	2	4737	
MONCODE	F	0000009C	4	4744	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
MONNUMBR	X	00000095	1	4739	
MPGACCID	X	000000A2	1	4747	
NKGRS	F	00000180	4	4792	
OP1DATA	A	00000004	4	3679	3580
OP1LEN	F	00000008	4	3680	3578 3581
OP1WHERE	A	00000018	4	3686	3577
OP1WLEN	F	0000001C	4	3687	3579
OP2DATA	A	0000000C	4	3681	3586
OP2LEN	F	00000010	4	3682	3585 3587
OP2WHERE	A	00000014	4	3685	3584
OPSWHERE	U	00000014	1	3684	3592
PAGE	U	00001000	1	3663	
PCFETO	A	000000C4	4	4768	
PERACCID	X	000000A1	1	4746	
PERADDR	F	00000098	4	4743	
PERCODE	X	00000096	1	4740	
PERCODMK	U	000000F0	1	4741	
PGMACCID	X	000000A0	1	4745	
PGMDXC	F	00000090	4	4735	
PGMICODE	H	0000008E	2	4734	
PGMIID	F	0000008C	4	4730	
PGMIILC	X	0000008D	1	4732	
PGMIILCM	U	0000000C	1	4733	
PGMNPSW	F	00000068	8	4712	
PGMOPSW	F	00000028	8	4684	4692
PGMTRX	F	00000090	4	4736	
R0	U	00000000	1	4836	3522
R1	U	00000001	1	4837	3592 3604
R10	U	0000000A	1	4846	3577 3582 3584 3588 3610 3613
R11	U	0000000B	1	4847	3578 3579 3585 3598 3599 3605 3617
R12	U	0000000C	1	4848	3610 3621
R13	U	0000000D	1	4849	
R14	U	0000000E	1	4850	3535 3629 3630
R15	U	0000000F	1	4851	3637
R2	U	00000002	1	4838	3602 3613
R3	U	00000003	1	4839	3617
R4	U	00000004	1	4840	3592 3602 3604 3621
R5	U	00000005	1	4841	3568 3569 3624 3625 3636
R6	U	00000006	1	4842	3572 3573 3580 3582 3586 3588
R7	U	00000007	1	4843	3581 3587 3594 3595 3596
R8	U	00000008	1	4844	3523 3526 3527 3528 3530 3638
R9	U	00000009	1	4845	3524 3530 3531
REG2LOW	U	000000DD	1	3700	
REG2PATT	U	AABBCCDD	1	3699	3723 3735 3747 3759 3771 3783 3795 3808 3820 3832 3844 3861 3873
					3885 3897 3909 3921 3933 3945 3957 3969 3981 4000 4013 4025 4037
					4049 4061 4073 4085 4097 4109 4121 4142 4154 4166 4178 4190 4202
					4214 4226 4238 4250 4262 4282 4295 4307 4319 4331 4343 4355 4367
					4379 4391 4403 4424 4436 4448 4460 4472 4484 4496 4508 4520 4532
					4544 4565 4577 4589 4601
RSTNPSW	F	00000000	8	4678	
RSTOPSW	F	00000008	8	4679	
SAVETRT	D	000005A8	8	3634	3604

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     SYMBOL         TYPE    VALUE      LENGTH    DEFN  REFERENCES

SCANOUT              X     00000080          1  4716  4717
SCANOUTL             U     00000000          1  4717
SSARCHMD             X     000000A3          1  4748
SSARS                F     00000120          4  4804
SSCLKCMP             F     000000E0          8  4798
SSCPUTIM             F     000000D8          8  4797
SSCRS                F     000001C0          4  4807
SSFPRS               D     00000160          8  4805
SSGRS                F     00000180          4  4806
SSMODEL              F     0000010C          4  4802
SSPREFIX             F     00000108          4  4801
SSPSW                F     00000100          8  4800
SSXSAA               A     000000D4          4  4796
STFLDATA             F     000000C8          4  4769
SUBTEST              X     00000401          1  3558  3545  3601  3612  3616  3620
SVCICODE             H     0000008A          2  4728
SVCIID               F     00000088          4  4724
SVCIILC              X     00000089          1  4726
SVCIILCM             U     0000000C          1  4727
SVCNPSW              F     00000060          8  4711
SVCOPSW              F     00000020          8  4683  4690
TEST01               I     00000502          4  3566  3535
TESTADDR             D     00000400          8  3556
TESTNUM              X     00000400          1  3557  3542  3566  3573
TIMER                F     00000050          4  4707
TNUM                 X     00000000          1  3674  3572
TRTE1TST             J     00000000     933710  3477  3480  3487  3495  3497
TRTEBC               I     0000059E          4  3632  3605
TRTECTL              A     000005EC          4  3708  3568
TRTEDONE             I     0000059C          2  3630  3627
TRTEFAIL             I     00000598          4  3629  3614  3618  3622  3632
TRTEMOD              I     00000554          4  3602  3596  3606
TRTENEXT             U     00000034          1  3697  3624
TRTETEST             4     00000000         52  3673  3569
TRTO1L0              X     0000192C          4  4622  3816  3953  4093  4234  4375  4516
TRTO1L11             X     0000212C          4  4624  3828  3965  4105  4246  4387  4528
TRTO1LF0             X     0000292C          4  4626  3840  3977  4117  4258  4399  4540  4585  4597
TRTOP10              X     0000142C          4  4614  3719  3731  3743  3755  3767  3857  3869  3881  3893  3905  3996  4009  4021
                                                 4033  4138  4150  4162  4174  4278  4291  4303  4315  4420  4432  4444  4456
TRTOP111             X     0000152C          4  4616  3779  3804  3917  3941  4045  4069  4186  4210  4327  4351  4468  4492
TRTOP1F0             X     0000162C          4  4618  3791  3929  4057  4198  4339  4480
TRTOP1F1             X     0000172C          4  4620  4081  4222  4363  4504  4561  4573
TRTOP20              X     0000312C          1  4633  3720  3732  3744  3756  3768  3817  3858  3870  3882  3894  3906  3954  3997
                                                 4010  4022  4034  4094  4139  4151  4163  4175  4235  4279  4292  4304  4316
                                                 4376  4421  4433  4445  4457  4517
TRTOP211             X     0002322C          1  4636  3780  3805  3829  4187  4211  4247
TRTOP2F0             X     0002332C          1  4638  3792  3841  4199  4259
TRTOP411             X     0002342C          1  4640  3918  3942  3966  4469  4493  4529
TRTOP4F0             X     0002362C          1  4642  3930  3978  4481  4541
TRTOP811             X     0002382C          1  4644  4046  4070  4106
TRTOP8F0             X     0004392C          1  4647  4058  4118
TRTOP8F1             X     00063A2C          1  4650  4082  4223
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SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
TRTOPC11	X	00083B2C	1	4653	4328 4352 4388
TRTOPCF0	X	000A3B50	1	4657	4340 4400 4586 4598
TRTOPCF1	X	000C3D4E	1	4660	4364 4505 4562 4574
TST1LOOP	U	0000050A	1	3571	3626
TTDES	F	00000054	4	4708	
UA0	F	00000010	8	4680	
UA1	F	0000004C	4	4705	
UA2	F	000000A4	4	4750	
UA3	F	000000B4	4	4759	
UA4	X	000000B8	1	4760	
UA5	X	000000CC	8	4770	
UA6	X	000000EC	8	4776	
UA7	F	00000118	8	4787	
UA8	X	00000180	32	4816	
ZBRKADDR	A	00000110	8	4786	
ZEMONCNT	F	0000010C	4	4785	
ZEMONCTR	A	00000100	8	4783	
ZEMONSIZ	F	00000108	4	4784	
ZEXTNPSW	X	000001B0	16	4819	
ZEXTOPSW	X	00000130	16	4811	
ZIONPSW	X	000001F0	16	4823	
ZIOOPSW	X	00000170	16	4815	
ZMCKNPSW	X	000001E0	16	4822	
ZMCKOPSW	X	00000160	16	4814	
ZMKFAILA	F	000000F8	8	4778	
ZMONCODE	F	000000B0	8	4753	
ZPGMNPSW	X	000001D0	16	4821	
ZPGMOPSW	X	00000150	16	4813	
ZPGMTRX	F	000000A8	8	4752	
ZRSTNPSW	X	000001A0	16	4818	
ZRSTOPSW	X	00000120	16	4810	
ZSASDISP	U	000011C0	1	4824	
ZSVCNPSW	X	000001C0	16	4820	
ZSVCOPSW	X	00000140	16	4812	
=F'0'	F	000005E8	4	3660	3625

MACRO	DEFN	REFERENCES		
ANTR	111			
APROB	243			
ARCHIND	403	3433		
ARCHLVL	544	3432		
ASAIPL	670	3493		
ASALOAD	750	3476		
ASAREA	805	4670		
ASAZAREA	990			
CPUWAIT	1073			
DSECTS	1399	4667		
DWAIT	1602	3645	3650	
DWAITEND	1659	3644		
ENADEV	1667			
ESA390	1767			
IOCB	1778			
IOCBDS	1954			
IOFMT	1988			
IOINIT	2326			
IOTRFR	2367			
ORB	2415			
POINTER	2604			
PSWFMT	2632			
RAWAIT	2766			
RAWIO	2862			
SIGCPU	3020			
SMMGR	3078			
SMMGRB	3178			
TRAP128	3227			
TRAP64	3204	3478	3481	
TRAPS	3240			
ZARCH	3314			
ZEROH	3326			
ZEROL	3354			
ZEROLH	3382			
ZEROLL	3405			

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   DESC      SYMBOL     SIZE        POS           ADDR

Entry: 0

Image       IMAGE      933710  00000-E3F4D  00000-E3F4D
  Region    CODE       933710  00000-E3F4D  00000-E3F4D
    CSECT   TRTE1TST   933710  00000-E3F4D  00000-E3F4D
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   STMT                     FILE NAME

1     /devstor/dev/satk/samples/tests/TRTE-01-basic.asm
2     /home/tn529/dev/satk/srcasm/satk.mac


** NO ERRORS FOUND **
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