

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				2 *****
				3 *
				4 * Zvector E6 instruction tests for VRI-g encoded:
				5 *
				6 * E659 VSRP - VECTOR SHIFT AND ROUND DECIMAL
				7 * E65B VPSOP - VECTOR PERFORM SIGN OPERATION DECIMAL
				8 *
				9 * James Wekel June 2024
				10 *****
				11
				12 *****
				13 *
				14 * basic instruction tests
				15 *
				16 *****
				17 * This program tests proper functioning of the z/arch E6 VRI-g vector
				18 * shift and round decimal, and perform sign operation decimal
				19 * instructions. Exceptions are not tested.
				20 *
				21 * PLEASE NOTE that the tests are very SIMPLE TESTS designed to catch
				22 * obvious coding errors. None of the tests are thorough. They are
				23 * NOT designed to test all aspects of any of the instructions.
				24 *
				25 *****
				26 *
				27 * *Testcase zvector-e6-16-VSRP-VPSOP: VECTOR E6 VRI-g instructions
				28 * *
				29 * * Zvector E6 tests for VRI-g encoded instruction:
				30 * *
				31 * * E659 VSRP - VECTOR SHIFT AND ROUND DECIMAL
				32 * * E65B VPSOP - VECTOR PERFORM SIGN OPERATION DECIMAL
				33 * *
				34 * * # -----
				35 * * # This tests only the basic function of the instruction.
				36 * * # Exceptions are NOT tested.
				37 * * # -----
				38 * *
				39 * main size 2
				40 * numcpu 1
				41 * sysclear
				42 * archlvl z/Arch
				43 *
				44 * diag8cmd enable # (needed for messages to Hercules console)
				45 * loadcore "\$(testpath)/zvector-e6-16-VSRP-VPSOP.core" 0x0
				46 * diag8cmd disable # (reset back to default)
				47 *
				48 * *Done
				49 *
				50 *****
00000000		00000000	0000376B	52 ZVE6TST START 0
				53 USING ZVE6TST, R0
				54 Low core addressability
		00000140	00000000	55 SV0LDPSW EQU ZVE6TST+X' 140'
				z/Arch Supervisor call old PSW

LOC	OBJECT CODE			ADDR1	ADDR2	STMT	
						140 *****	
						141 * cc was not as expected	
						142 *****	
0000026E	E310	0001	0082	0000026E	00000001	143 CCMG EQU *	
00000274	E310	5009	0076		00000001	144 XG R1, R1	
0000027A	5410	82C8			00000009	145 LB R1, M5	M5 has CS bit
0000027E	4780	8052			000004C8	146 N R1, =F' 1'	get CS (CC set) bit
					00000252	147 BZ TESTREST	ignore if not set
						148 *	
						149 * extract CC extracted PSW	
						150 *	
00000282	5810	8EF0			000010F0	151 L R1, CCPSW	
00000286	8810	000C			0000000C	152 SRL R1, 12	
0000028A	5410	82CC			000004CC	153 N R1, =XL4' 3'	
0000028E	4210	8EF8			000010F8	154 STC R1, CCFOUND	save cc
						155 *	
						156 * FILL IN MESSAGE	
						157 *	
00000292	4820	5004			00000004	158 LH R2, TNUM	get test number and convert
00000296	4E20	8EDE			000010DE	159 CVD R2, DECNUM	
0000029A	D211	8EC8	8EB2	000010C8	000010B2	160 MVC PRT3, EDIT	
000002A0	DE11	8EC8	8EDE	000010C8	000010DE	161 ED PRT3, DECNUM	
000002A6	D202	8E6D	8ED5	0000106D	000010D5	162 MVC CCPRTNUM(3), PRT3+13	fill in message with test #
						163	
000002AC	D207	8E8A	5010	0000108A	00000010	164 MVC CCPRTNAME, OPNAME	fill in message with instruction
						165	
000002B2	B982	0022				166 XGR R2, R2	get CC as U8
000002B6	4320	500A			0000000A	167 IC R2, CC	
000002BA	4E20	8EDE			000010DE	168 CVD R2, DECNUM	and convert
000002BE	D211	8EC8	8EB2	000010C8	000010B2	169 MVC PRT3, EDIT	
000002C4	DE11	8EC8	8EDE	000010C8	000010DE	170 ED PRT3, DECNUM	
000002CA	D200	8EA0	8ED7	000010A0	000010D7	171 MVC CCPRTEXP(1), PRT3+15	fill in message with CC field
						172	
000002D0	B982	0022				173 XGR R2, R2	get CCFOUND as U8
000002D4	4320	8EF8			000010F8	174 IC R2, CCFOUND	
000002D8	4E20	8EDE			000010DE	175 CVD R2, DECNUM	and convert
000002DC	D211	8EC8	8EB2	000010C8	000010B2	176 MVC PRT3, EDIT	
000002E2	DE11	8EC8	8EDE	000010C8	000010DE	177 ED PRT3, DECNUM	
000002E8	D200	8EB0	8ED7	000010B0	000010D7	178 MVC CCPRTGOT(1), PRT3+15	fill in message with ccfound
						179	
000002EE	4100	0055			00000055	180 LA R0, CCPRTLNG	message length
000002F2	4110	8E5D			0000105D	181 LA R1, CCPRTLNE	messagfe address
000002F6	45F0	81A2			000003A2	182 BAL R15, RPTERROR	
						183	
000002FA	47F0	8184			00000384	184 B FAILCONT	

LOC	OBJECT CODE			ADDR1	ADDR2	STMT	
						186	*****
						187	* result not as expected:
						188	* issue message with test number, instruction under test
						189	* and instruction m3
						190	*****
				000002FE	00000001	191	FAILMSG EQU *
000002FE	4820	5004			00000004	192	LH R2, TNUM get test number and convert
00000302	4E20	8EDE			000010DE	193	CVD R2, DECNUM
00000306	D211	8EC8 8EB2		000010C8	000010B2	194	MWC PRT3, EDIT
0000030C	DE11	8EC8 8EDE		000010C8	000010DE	195	ED PRT3, DECNUM
00000312	D202	8E14 8ED5		00001014	000010D5	196	MWC PRTNUM(3), PRT3+13 fill in message with test #
						197	
00000318	D207	8E2F 5010		0000102F	00000010	198	MWC PRTNAME, OPNAME fill in message with instruction
						199	
0000031E	B982	0022				200	XGR R2, R2 get i3 as U8
00000322	4320	5007			00000007	201	IC R2, I3
00000326	4E20	8EDE			000010DE	202	CVD R2, DECNUM and convert
0000032A	D211	8EC8 8EB2		000010C8	000010B2	203	MWC PRT3, EDIT
00000330	DE11	8EC8 8EDE		000010C8	000010DE	204	ED PRT3, DECNUM
00000336	D202	8E40 8ED5		00001040	000010D5	205	MWC PRTI3(3), PRT3+13 fill in message with i3 field
						206	
0000033C	B982	0022				207	XGR R2, R2 get i4 as U8
00000340	4320	5008			00000008	208	IC R2, I4
00000344	4E20	8EDE			000010DE	209	CVD R2, DECNUM and convert
00000348	D211	8EC8 8EB2		000010C8	000010B2	210	MWC PRT3, EDIT
0000034E	DE11	8EC8 8EDE		000010C8	000010DE	211	ED PRT3, DECNUM
00000354	D202	8E4D 8ED5		0000104D	000010D5	212	MWC PRTI4(3), PRT3+13 fill in message with i4 field
						213	
0000035A	B982	0022				214	XGR R2, R2 get m5 as U8
0000035E	4320	5009			00000009	215	IC R2, M5 and convert
00000362	4E20	8EDE			000010DE	216	CVD R2, DECNUM
00000366	D211	8EC8 8EB2		000010C8	000010B2	217	MWC PRT3, EDIT
0000036C	DE11	8EC8 8EDE		000010C8	000010DE	218	ED PRT3, DECNUM
00000372	D201	8E5A 8ED6		0000105A	000010D6	219	MWC PRTM5(2), PRT3+14 fill in message with m5 field
						220	
00000378	4100	0059			00000059	221	LA R0, PRTLNG message length
0000037C	4110	8E04			00001004	222	LA R1, PRTLNE messagfe address
00000380	45F0	81A2			000003A2	223	BAL R15, RPTERROR
						224	
						225	*****
						226	* continue after a failed test
						227	*****
				00000384	00000001	228	FAILCONT EQU *
00000384	5800	82C8			000004C8	229	L R0, =F' 1' set GLOBAL failed test indicator
00000388	5000	8E00			00001000	230	ST R0, FAILED
						231	
0000038C	41C0	C004			00000004	232	LA R12, 4(0, R12) next test address
00000390	47F0	802A			0000022A	233	B NEXTE6
						234	
						235	*****
						236	* end of testing; set ending psw
						237	*****
				00000394	00000001	238	ENDTEST EQU *
00000394	5810	8E00			00001000	239	L R1, FAILED did a test fail?

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT				
					269	*****			
					270	*	Issue HERCULES MESSAGE pointed to by R1, length in R0		
					271	*	R2 = return address		
					272	*****			
					273				
000003D8	4900	82D0		000004D0	274	MSG	CH	R0, =H' 0'	Do we even HAVE a message?
000003DC	07D2				275		BNHR	R2	No, ignore
					276				
000003DE	9002	8210		00000410	277		STM	R0, R2, MSGSAVE	Save registers
					278				
000003E2	4900	82D2		000004D2	279		CH	R0, =AL2(L' MSGMSG)	Message length within limits?
000003E6	47D0	81EE		000003EE	280		BNH	MSGOK	Yes, continue
000003EA	4100	005F		0000005F	281		LA	R0, L' MSGMSG	No, set to maximum
					282				
000003EE	1820				283	MSGOK	LR	R2, R0	Copy length to work register
000003F0	0620				284		BCTR	R2, 0	Minus-1 for execute
000003F2	4420	821C		0000041C	285		EX	R2, MSGMVC	Copy message to O/P buffer
					286				
000003F6	4120	200A		0000000A	287		LA	R2, 1+L' MSGCMD(, R2)	Calculate true command length
000003FA	4110	8222		00000422	288		LA	R1, MSGCMD	Point to true command
					289				
000003FE	8312	0008			290		DC	X' 83' , X' 12' , X' 0008'	Issue Hercules Diagnose X' 008'
00000402	4780	8208		00000408	291		BZ	MSGRET	Return if successful
00000406	0000				292		DC	H' 0'	CRASH for debugging purposes
					293				
00000408	9802	8210		00000410	294	MSGRET	LM	R0, R2, MSGSAVE	Restore registers
0000040C	07F2				295		BR	R2	Return to caller
00000410	00000000	00000000			297	MSGSAVE	DC	3F' 0'	Registers save area
0000041C	D200	822B	1000	0000042B	298	MSGMVC	MVC	MSGMSG(0) , 0(R1)	Executed instruction
00000422	D4E2C7D5	D6C8405C			300	MSGCMD	DC	C' MSGNOH * '	*** HERCULES MESSAGE COMMAND ***
0000042B	40404040	40404040			301	MSGMSG	DC	CL95' '	The message text to be displayed
					302				

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				304	*****
				305	* Normal completion or Abnormal termination PSWs
				306	*****
00000490	00020001 80000000			308	E0JPSW DC 0D' 0' , X' 0002000180000000' , AD(0)
000004A0	B2B2 8290		00000490	310	E0J LPSWE E0JPSW Normal completion
000004A8	00020001 80000000			312	FAILPSW DC 0D' 0' , X' 0002000180000000' , AD(X' BAD')
000004B8	B2B2 82A8		000004A8	314	FAILTEST LPSWE FAILPSW Abnormal termination
				316	*****
				317	* Working Storage
				318	*****
000004BC	00000000			320	CTLRO DS F CRO
000004C0	00000000			321	DS F
				322	
000004C4	000035D8			323	E6TADR DC A(E6TESTS) address of E6 test table
000004C8				325	LTORG , Literals pool
000004C8	00000001			326	=F' 1'
000004CC	00000003			327	=XL4' 3'
000004D0	0000			328	=H' 0'
000004D2	005F			329	=AL2(L' MSGMSG)
				330	
				331	* some constants
				332	
	00000400	00000001		333	K EQU 1024 One KB
	00001000	00000001		334	PAGE EQU (4*K) Size of one page
	00010000	00000001		335	K64 EQU (64*K) 64 KB
	00100000	00000001		336	MB EQU (K*K) 1 MB
				337	
	AABBCCDD	00000001		338	REG2PATT EQU X' AABBCCDD' Polluted Register pattern
	000000DD	00000001		339	REG2LOW EQU X' DD' (last byte above)

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				341 *=====
				342 *
				343 * NOTE: start data on an address that is easy to display
				344 * within Hercules
				345 *
				346 *=====
				347
000004D4		000004D4	00001000	348 ORG ZVE6TST+X' 1000'
00001000	00000000			349 FAILED DC F' 0' some test failed?
				351 *****
				352 * TEST failed : result messgae
				353 *****
				354 *
				355 * failed message and associated editting
				356 *
00001004	40404040	40404040		357 PRTLNE DC C' Test # '
00001014	A7A7A7			358 PRTNUM DC C' xxx'
00001017	40868189	93858440		359 DC C' failed for instruction '
0000102F	A7A7A7A7	A7A7A7A7		360 PRTNAME DC CL8' xxxxxxxx'
00001037	40A689A3	884089F3		361 DC C' with i3='
00001040	A7A7A7			362 PRTI3 DC C' xxx'
00001043	6B			363 DC C' ,'
00001044	40A689A3	884089F4		364 DC C' with i4='
0000104D	A7A7A7			365 PRTI4 DC C' xxx'
00001050	6B			366 DC C' ,'
00001051	40A689A3	884094F5		367 DC C' with m5='
0000105A	A7A7			368 PRTM5 DC C' xx'
0000105C	4B			369 DC C' .'
		00000059	00000001	370 PRTLNG EQU *- PRTLNE
				372 *****
				373 * TEST failed : CC message
				374 *****
				375 *
				376 * failed message and associated editting
				377 *
0000105D	40404040	40404040		378 CCPRTLNE DC C' Test # '
0000106D	A7A7A7			379 CCPRTNUM DC C' xxx'
00001070	40A69996	95874083		380 DC c' wrong cc for instruction '
0000108A	A7A7A7A7	A7A7A7A7		381 CCPRTNAME DC CL8' xxxxxxxx'
00001092	4085A797	8583A385		382 DC C' expected: cc='
000010A0	A7			383 CCPRTEXP DC C' x'
000010A1	6B			384 DC C' ,'
000010A2	40998583	8589A585		385 DC C' received: cc='
000010B0	A7			386 CCPRTGOT DC C' x'
000010B1	4B			387 DC C' .'
		00000055	00000001	388 CCPRTLNG EQU *- CCPRTLNE

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				443 *****
				444 * Macros to help build test tables
				445 *-----
				446 * VRI_G Macro to help build test tables
				447 *****
				448 MACRO
				449 VRI_G &INST, &I3, &I4, &M5, &CC
				450 . * &INST - VRI-g instruction under test
				451 . * &i3 - i3 field
				452 . * &i4 - i4 field
				453 . * &m5 - m5 field
				454 . * &CC - expected CC
				455 . *
				456 LCLA &XCC(4) &CC has mask values for FAILED condition codes
				457 &XCC(1) SETA 7 CC != 0
				458 &XCC(2) SETA 11 CC != 1
				459 &XCC(3) SETA 13 CC != 2
				460 &XCC(4) SETA 14 CC != 3
				461 . *
				462 GBLA &TNUM
				463 &TNUM SETA &TNUM+1
				464
				465 DS 0FD
				466 USING *, R5 base for test data and test routine
				467
				468 T&TNUM DC A(X&TNUM) address of test routine
				469 DC H' &TNUM test number
				470 DC X' 00'
				471 DC HL1' &I3' i3
				472 DC HL1' &I4' i4
				473 DC HL1' &M5' m5
				474 DC HL1' &CC' cc
				475 DC HL1' &XCC(&CC+1)' cc failed mask
				476 V2_&TNUM DC A(RE&TNUM+16) address of v2: 16-byte packed decimal
				477 DC CL8' &INST' instruction name
				478 DC A(16) result length
				479 REA&TNUM DC A(RE&TNUM) result address
				480 . *
				481 * INSTRUCTION UNDER TEST ROUTINE
				482
				483 X&TNUM DS 0F
				484 L R2, V2_&TNUM get v2
				485 VL V2, 0(R2)
				486
				487 &INST V1, V2, &I3, &I4, &M5 test instruction
				488
				489 VST V1, V10OUTPUT save result
				490 EPSW R2, R0 exptrect psw
				491 ST R2, CCPSW to save CC
				492 BR R11 return
				493
				494 RE&TNUM DC 0F
				495 DROP R5
				496
				497 MEND

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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499	*****		
500	* PTTABLE Macro to generate table of pointers to individual tests		
501	*****		
502			
503		MACRO	
504		PTTABLE	
505		GBLA	&TNUM
506		LCLA	&CUR
507	&CUR	SETA	1
508	. *		
509	TTABLE	DS	OF
510	. LOOP	ANOP	
511	. *		
512		DC	A(T&CUR) address of test
513	. *		
514	&CUR	SETA	&CUR+1
515		AIF	(&CUR LE &TNUM) . LOOP
516	*		
517		DC	A(0) END OF TABLE
518		DC	A(0)
519	. *		
520		MEND	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				522 *****
				523 * E6 VRI_G tests
				524 *****
				525
00001170		00000000	0000376B	526 ZVE6TST CSECT ,
				527 DS 0F
				529 PRINT DATA
				530 *
				531 * E659 VSRP - VECTOR SHIFT AND ROUND DECIMAL
				532 * E65B VPSOP - VECTOR PERFORM SIGN OPERATION DECIMAL
				533 *
				534 * VRI_G instr,i3,i4,m5,cc
				535 * followed by
				536 * v1 - 16 byte expected result
				537 * v2 - 16 byte zoned decimal (operand)
				538
				539 * -----
				540 * VSRP - VECTOR SHIFT AND ROUND DECIMAL REGISTER
				541 * -----
				542 * VSRP with some I3, I4 and M5's
				543 * I3 tests
				544 * i4=129 (iom=1, rdc=1)
				545 * i4=132 (iom=1, rdc=4)
				546 * i4=135 (iom=1, rdc=7)
				547 * i4=142 (iom=1, rdc=14)
				548 * i4=159 (iom=1, rdc=31)
				549 * I4
				550 * i4= 0 (drd=0, shamt=0) shi ft left
				551 * i4= 1 (drd=0, shamt=1) shi ft left
				552 * i4= 4 (drd=0, shamt=4) shi ft left
				553 * i4= 7 (drd=0, shamt=7) shi ft left
				554 * i4= 14 (drd=0, shamt=14) shi ft left
				555 * i4= 30 (drd=0, shamt=30) shi ft left
				556 * i4= 31 (drd=0, shamt=31) shi ft left
				557
				558 * i4=96 (drd=0, shamt=- 32) shi ft right
				559 * i4=114 (drd=0, shamt=- 14) shi ft right
				560 * i4=121 (drd=0, shamt=- 7) shi ft right
				561 * i4=124 (drd=0, shamt=- 4) shi ft right
				562 * i4=127 (drd=0, shamt=- 1) shi ft right
				563
				564 * i4=129 (drd=1, shamt=1) shi ft left
				565 * i4=132 (drd=1, shamt=4) shi ft left
				566 * i4=135 (drd=1, shamt=7) shi ft left
				567 * i4=142 (drd=1, shamt=14) shi ft left
				568 * i4=159 (drd=1, shamt=31) shi ft left
				569
				570 * i4=224 (drd=1, shamt=- 32) shi ft right
				571 * i4=225 (drd=1, shamt=- 31) shi ft right
				572 * i4=242 (drd=1, shamt=- 14) shi ft right
				573 * i4=249 (drd=1, shamt=- 7) shi ft right
				574 * i4=252 (drd=1, shamt=- 4) shi ft right
				575 * i4=255 (drd=1, shamt=- 1) shi ft right
				576

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
				577 *		
				578 *	m5 tests (note: cs is always 1)	
				579 *	m5=1 (p2=0, p1=0, cs=1)	
				580 *	m5=3 (p2=0, p1=1, cs=1)	
				581 *	m5=9 (p2=1, p1=0, cs=1)	
				582 *	m5=11 (p2=1, p1=1, cs=1)	
				583		
				584	VRI_G VSRP, 159, 0, 1, 2	shamt=0
00001170				585+	DS	OFD
00001170		00001170		586+	USING	*, R5
00001170	00001190			587+T1	DC	A(X1)
00001174	0001			588+	DC	H' 1'
00001176	00			589+	DC	X' 00'
00001177	9F			590+	DC	HL1' 159'
00001178	00			591+	DC	HL1' 0'
00001179	01			592+	DC	HL1' 1'
0000117A	02			593+	DC	HL1' 2'
0000117B	0D			594+	DC	HL1' 13'
0000117C	000011C0			595+V2_1	DC	A(RE1+16)
00001180	E5E2D9D7 40404040			596+	DC	CL8' VSRP'
00001188	00000010			597+	DC	A(16)
0000118C	000011B0			598+REA1	DC	A(RE1)
				599+*		INSTRUCTION UNDER TEST ROUTINE
00001190				600+X1	DS	OF
00001190	5820 500C		0000117C	601+	L	R2, V2_1
00001194	E722 0000 0006		00000000	602+	VL	V2, 0(R2)
0000119A	E612 0019 F059			603+	VSRP	V1, V2, 159, 0, 1
000011A0	E710 8F10 000E		00001110	604+	VST	V1, V10UTPUT
000011A6	B98D 0020			605+	EPSW	R2, R0
000011AA	5020 8EF0		000010F0	606+	ST	R2, CCPSW
000011AE	07FB			607+	BR	R11
000011B0				608+RE1	DC	OF
000011B0				609+	DROP	R5
000011B0	00000000 00000000			610	DC	XL16' 0000000000000000000000000000022C' V1
000011B8	00000000 0000022C					
000011C0	00000000 00000000			611	DC	XL16' 0000000000000000000000000000022C' V2
000011C8	00000000 0000022C					
				612		
				613	VRI_G VSRP, 159, 1, 1, 2	shamt=1 (left)
000011D0				614+	DS	OFD
000011D0		000011D0		615+	USING	*, R5
000011D0	000011F0			616+T2	DC	A(X2)
000011D4	0002			617+	DC	H' 2'
000011D6	00			618+	DC	X' 00'
000011D7	9F			619+	DC	HL1' 159'
000011D8	01			620+	DC	HL1' 1'
000011D9	01			621+	DC	HL1' 1'
000011DA	02			622+	DC	HL1' 2'
000011DB	0D			623+	DC	HL1' 13'
000011DC	00001220			624+V2_2	DC	A(RE2+16)
000011E0	E5E2D9D7 40404040			625+	DC	CL8' VSRP'
000011E8	00000010			626+	DC	A(16)
000011EC	00001210			627+REA2	DC	A(RE2)
				628+*		INSTRUCTION UNDER TEST ROUTINE
000011F0				629+X2	DS	OF
000011F0	5820 500C		000011DC	630+	L	R2, V2_2

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000011F4	E722 0000 0006		00000000	631+	VL	V2, 0(R2)	
000011FA	E612 0119 F059			632+	VSRP	V1, V2, 159, 1, 1	test instruction
00001200	E710 8F10 000E		00001110	633+	VST	V1, V10OUTPUT	save result
00001206	B98D 0020			634+	EPSW	R2, R0	exptract psw
0000120A	5020 8EF0		000010F0	635+	ST	R2, CCPSW	to save CC
0000120E	07FB			636+	BR	R11	return
00001210				637+RE2	DC	0F	
00001210				638+	DROP	R5	
00001210	00000000 00000000			639	DC	XL16' 0000000000000000000000000000220C'	V1
00001218	00000000 0000220C						
00001220	00000000 00000000			640	DC	XL16' 000000000000000000000000000022C'	V2
00001228	00000000 0000022C						
				641			
				642	VRI_G	VSRP, 159, 7, 1, 2	shamt=7 (left)
00001230				643+	DS	0FD	
00001230		00001230		644+	USING	*, R5	base for test data and test routine
00001230	00001250			645+T3	DC	A(X3)	address of test routine
00001234	0003			646+	DC	H' 3'	test number
00001236	00			647+	DC	X' 00'	
00001237	9F			648+	DC	HL1' 159'	i3
00001238	07			649+	DC	HL1' 7'	i4
00001239	01			650+	DC	HL1' 1'	m5
0000123A	02			651+	DC	HL1' 2'	cc
0000123B	0D			652+	DC	HL1' 13'	cc failed mask
0000123C	00001280			653+V2_3	DC	A(RE3+16)	address of v2: 16-byte packed decimal
00001240	E5E2D9D7 40404040			654+	DC	CL8' VSRP'	instruction name
00001248	00000010			655+	DC	A(16)	result length
0000124C	00001270			656+REA3	DC	A(RE3)	result address
				657+*			INSTRUCTION UNDER TEST ROUTINE
00001250				658+X3	DS	0F	
00001250	5820 500C		0000123C	659+	L	R2, V2_3	get v2
00001254	E722 0000 0006		00000000	660+	VL	V2, 0(R2)	
0000125A	E612 0719 F059			661+	VSRP	V1, V2, 159, 7, 1	test instruction
00001260	E710 8F10 000E		00001110	662+	VST	V1, V10OUTPUT	save result
00001266	B98D 0020			663+	EPSW	R2, R0	exptract psw
0000126A	5020 8EF0		000010F0	664+	ST	R2, CCPSW	to save CC
0000126E	07FB			665+	BR	R11	return
00001270				666+RE3	DC	0F	
00001270				667+	DROP	R5	
00001270	00000000 00000000			668	DC	XL16' 000000000000000000000000220000000C'	V1
00001278	00000022 0000000C						
00001280	00000000 00000000			669	DC	XL16' 000000000000000000000000000022C'	V2
00001288	00000000 0000022C						
				670			
				671	VRI_G	VSRP, 159, 30, 1, 3	shamt=30 (left) (overflow)
00001290				672+	DS	0FD	
00001290		00001290		673+	USING	*, R5	base for test data and test routine
00001290	000012B0			674+T4	DC	A(X4)	address of test routine
00001294	0004			675+	DC	H' 4'	test number
00001296	00			676+	DC	X' 00'	
00001297	9F			677+	DC	HL1' 159'	i3
00001298	1E			678+	DC	HL1' 30'	i4
00001299	01			679+	DC	HL1' 1'	m5
0000129A	03			680+	DC	HL1' 3'	cc
0000129B	0E			681+	DC	HL1' 14'	cc failed mask
0000129C	000012E0			682+V2_4	DC	A(RE4+16)	address of v2: 16-byte packed decimal

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000012A0	E5E2D9D7 40404040			683+	DC	CL8' VSRP'	instruction name
000012A8	00000010			684+	DC	A(16)	result length
000012AC	000012D0			685+REA4	DC	A(RE4)	result address
				686+*			INSTRUCTION UNDER TEST ROUTINE
000012B0				687+X4	DS	0F	
000012B0	5820 500C		0000129C	688+	L	R2, V2_4	get v2
000012B4	E722 0000 0006		00000000	689+	VL	V2, 0(R2)	
000012BA	E612 1E19 F059			690+	VSRP	V1, V2, 159, 30, 1	test instruction
000012C0	E710 8F10 000E		00001110	691+	VST	V1, V10UTPUT	save result
000012C6	B98D 0020			692+	EPSW	R2, R0	exptract psw
000012CA	5020 8EF0		000010F0	693+	ST	R2, CCPSW	to save CC
000012CE	07FB			694+	BR	R11	return
000012D0				695+RE4	DC	0F	
000012D0				696+	DROP	R5	
000012D0	20000000 00000000			697	DC	XL16' 20000000000000000000000000000000C'	V1
000012D8	00000000 0000000C						
000012E0	00000000 00000000			698	DC	XL16' 0000000000000000000000000000000022C'	V2
000012E8	00000000 0000022C						
				699			
				700	VRI_G	VSRP, 159, 31, 1, 3	shamt=31 (left) (overflow)
000012F0				701+	DS	0FD	
000012F0		000012F0		702+	USING	*, R5	base for test data and test routine
000012F0	00001310			703+T5	DC	A(X5)	address of test routine
000012F4	0005			704+	DC	H' 5'	test number
000012F6	00			705+	DC	X' 00'	
000012F7	9F			706+	DC	HL1' 159'	i3
000012F8	1F			707+	DC	HL1' 31'	i4
000012F9	01			708+	DC	HL1' 1'	m5
000012FA	03			709+	DC	HL1' 3'	cc
000012FB	0E			710+	DC	HL1' 14'	cc failed mask
000012FC	00001340			711+V2_5	DC	A(RE5+16)	address of v2: 16-byte packed decimal
00001300	E5E2D9D7 40404040			712+	DC	CL8' VSRP'	instruction name
00001308	00000010			713+	DC	A(16)	result length
0000130C	00001330			714+REA5	DC	A(RE5)	result address
				715+*			INSTRUCTION UNDER TEST ROUTINE
00001310				716+X5	DS	0F	
00001310	5820 500C		000012FC	717+	L	R2, V2_5	get v2
00001314	E722 0000 0006		00000000	718+	VL	V2, 0(R2)	
0000131A	E612 1F19 F059			719+	VSRP	V1, V2, 159, 31, 1	test instruction
00001320	E710 8F10 000E		00001110	720+	VST	V1, V10UTPUT	save result
00001326	B98D 0020			721+	EPSW	R2, R0	exptract psw
0000132A	5020 8EF0		000010F0	722+	ST	R2, CCPSW	to save CC
0000132E	07FB			723+	BR	R11	return
00001330				724+RE5	DC	0F	
00001330				725+	DROP	R5	
00001330	00000000 00000000			726	DC	XL16' 00000000000000000000000000000000C'	V1
00001338	00000000 0000000C						
00001340	00000000 00000000			727	DC	XL16' 0000000000000000000000000000000022D'	V2
00001348	00000000 0000022D						
				728			
				729	VRI_G	VSRP, 159, 127, 1, 2	shamt=-1 (right)
00001350				730+	DS	0FD	
00001350		00001350		731+	USING	*, R5	base for test data and test routine
00001350	00001370			732+T6	DC	A(X6)	address of test routine
00001354	0006			733+	DC	H' 6'	test number
00001356	00			734+	DC	X' 00'	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001357	9F			735+	DC	HL1' 159'	i3
00001358	7F			736+	DC	HL1' 127'	i4
00001359	01			737+	DC	HL1' 1'	m5
0000135A	02			738+	DC	HL1' 2'	cc
0000135B	0D			739+	DC	HL1' 13'	cc failed mask
0000135C	000013A0			740+V2_6	DC	A(RE6+16)	address of v2: 16-byte packed decimal
00001360	E5E2D9D7 40404040			741+	DC	CL8' VSRP'	instruction name
00001368	00000010			742+	DC	A(16)	result length
0000136C	00001390			743+REA6	DC	A(RE6)	result address
				744+*			INSTRUCTION UNDER TEST ROUTINE
00001370				745+X6	DS	0F	
00001370	5820 500C		0000135C	746+	L	R2, V2_6	get v2
00001374	E722 0000 0006		00000000	747+	VL	V2, 0(R2)	
0000137A	E612 7F19 F059			748+	VSRP	V1, V2, 159, 127, 1	test instruction
00001380	E710 8F10 000E		00001110	749+	VST	V1, V10OUTPUT	save result
00001386	B98D 0020			750+	EPSW	R2, R0	exptract psw
0000138A	5020 8EF0		000010F0	751+	ST	R2, CCPSW	to save CC
0000138E	07FB			752+	BR	R11	return
00001390				753+RE6	DC	0F	
00001390				754+	DROP	R5	
00001390	00000000 00000000			755	DC	XL16' 0000000000000000000000000000000002C'	V1
00001398	00000000 0000002C						
000013A0	00000000 00000000			756	DC	XL16' 0000000000000000000000000000000002C'	V2
000013A8	00000000 0000022C						
				757			
				758	VRI_G	VSRP, 159, 255, 1, 2	shamt=-1 (right) drd=1
000013B0				759+	DS	0FD	
000013B0		000013B0		760+	USING	*, R5	base for test data and test routine
000013B0	000013D0			761+T7	DC	A(X7)	address of test routine
000013B4	0007			762+	DC	H' 7'	test number
000013B6	00			763+	DC	X' 00'	
000013B7	9F			764+	DC	HL1' 159'	i3
000013B8	FF			765+	DC	HL1' 255'	i4
000013B9	01			766+	DC	HL1' 1'	m5
000013BA	02			767+	DC	HL1' 2'	cc
000013BB	0D			768+	DC	HL1' 13'	cc failed mask
000013BC	00001400			769+V2_7	DC	A(RE7+16)	address of v2: 16-byte packed decimal
000013C0	E5E2D9D7 40404040			770+	DC	CL8' VSRP'	instruction name
000013C8	00000010			771+	DC	A(16)	result length
000013CC	000013F0			772+REA7	DC	A(RE7)	result address
				773+*			INSTRUCTION UNDER TEST ROUTINE
000013D0				774+X7	DS	0F	
000013D0	5820 500C		000013BC	775+	L	R2, V2_7	get v2
000013D4	E722 0000 0006		00000000	776+	VL	V2, 0(R2)	
000013DA	E612 FF19 F059			777+	VSRP	V1, V2, 159, 255, 1	test instruction
000013E0	E710 8F10 000E		00001110	778+	VST	V1, V10OUTPUT	save result
000013E6	B98D 0020			779+	EPSW	R2, R0	exptract psw
000013EA	5020 8EF0		000010F0	780+	ST	R2, CCPSW	to save CC
000013EE	07FB			781+	BR	R11	return
000013F0				782+RE7	DC	0F	
000013F0				783+	DROP	R5	
000013F0	00000000 00000000			784	DC	XL16' 0000000000000000000000000000000003C'	V1
000013F8	00000000 0000003C						
00001400	00000000 00000000			785	DC	XL16' 00000000000000000000000000000000028C'	V2
00001408	00000000 0000028C						
				786			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
				787	VRI_G VSRP, 159, 252, 1, 1	shamt=- 4 (right) drd=1
00001410				788+	DS OFD	
00001410		00001410		789+	USING *, R5	base for test data and test routine
00001410	00001430			790+T8	DC A(X8)	address of test routine
00001414	0008			791+	DC H' 8'	test number
00001416	00			792+	DC X' 00'	
00001417	9F			793+	DC HL1' 159'	i3
00001418	FC			794+	DC HL1' 252'	i4
00001419	01			795+	DC HL1' 1'	m5
0000141A	01			796+	DC HL1' 1'	cc
0000141B	0B			797+	DC HL1' 11'	cc failed mask
0000141C	00001460			798+V2_8	DC A(RE8+16)	address of v2: 16-byte packed decimal
00001420	E5E2D9D7 40404040			799+	DC CL8' VSRP'	instruction name
00001428	00000010			800+	DC A(16)	result length
0000142C	00001450			801+REA8	DC A(RE8)	result address
				802+*		INSTRUCTION UNDER TEST ROUTINE
00001430				803+X8	DS OF	
00001430	5820 500C		0000141C	804+	L R2, V2_8	get v2
00001434	E722 0000 0006		00000000	805+	VL V2, 0(R2)	
0000143A	E612 FC19 F059			806+	VSRP V1, V2, 159, 252, 1	test instruction
00001440	E710 8F10 000E		00001110	807+	VST V1, V10UTPUT	save result
00001446	B98D 0020			808+	EPSW R2, R0	exptract psw
0000144A	5020 8EF0		000010F0	809+	ST R2, CCPSW	to save CC
0000144E	07FB			810+	BR R11	return
00001450				811+RE8	DC OF	
00001450				812+	DROP R5	
00001450	00000000 00000000			813	DC XL16' 000000000000000000000000000000002D'	V1
00001458	00000000 0000002D					
00001460	00000000 00000000			814	DC XL16' 0000000000000000000000000000000015028D'	V2
00001468	00000000 0015028D					
				815		
				816	VRI_G VSRP, 159, 249, 1, 0	shamt=- 7 (right) drd=1
00001470				817+	DS OFD	
00001470		00001470		818+	USING *, R5	base for test data and test routine
00001470	00001490			819+T9	DC A(X9)	address of test routine
00001474	0009			820+	DC H' 9'	test number
00001476	00			821+	DC X' 00'	
00001477	9F			822+	DC HL1' 159'	i3
00001478	F9			823+	DC HL1' 249'	i4
00001479	01			824+	DC HL1' 1'	m5
0000147A	00			825+	DC HL1' 0'	cc
0000147B	07			826+	DC HL1' 7'	cc failed mask
0000147C	000014C0			827+V2_9	DC A(RE9+16)	address of v2: 16-byte packed decimal
00001480	E5E2D9D7 40404040			828+	DC CL8' VSRP'	instruction name
00001488	00000010			829+	DC A(16)	result length
0000148C	000014B0			830+REA9	DC A(RE9)	result address
				831+*		INSTRUCTION UNDER TEST ROUTINE
00001490				832+X9	DS OF	
00001490	5820 500C		0000147C	833+	L R2, V2_9	get v2
00001494	E722 0000 0006		00000000	834+	VL V2, 0(R2)	
0000149A	E612 F919 F059			835+	VSRP V1, V2, 159, 249, 1	test instruction
000014A0	E710 8F10 000E		00001110	836+	VST V1, V10UTPUT	save result
000014A6	B98D 0020			837+	EPSW R2, R0	exptract psw
000014AA	5020 8EF0		000010F0	838+	ST R2, CCPSW	to save CC
000014AE	07FB			839+	BR R11	return
000014B0				840+RE9	DC OF	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000014B0				841+	DROP	R5	
000014B0	00000000 00000000			842	DC	XL16' 00000000000000000000000000000000C'	V1 (note: C)
000014B8	00000000 0000000C						
000014C0	00000000 00000000			843	DC	XL16' 0000000000000000000000000000028D'	V2
000014C8	00000000 0000028D						
				844			
				845	VRI_G	VSRP, 159, 225, 1, 1	shamt=- 31 (right) drd=1
000014D0				846+	DS	0FD	
000014D0		000014D0		847+	USING	*, R5	base for test data and test routine
000014D0	000014F0			848+T10	DC	A(X10)	address of test routine
000014D4	000A			849+	DC	H' 10'	test number
000014D6	00			850+	DC	X' 00'	
000014D7	9F			851+	DC	HL1' 159'	i3
000014D8	E1			852+	DC	HL1' 225'	i4
000014D9	01			853+	DC	HL1' 1'	m5
000014DA	01			854+	DC	HL1' 1'	cc
000014DB	0B			855+	DC	HL1' 11'	cc failed mask
000014DC	00001520			856+V2_10	DC	A(RE10+16)	address of v2: 16-byte packed decimal
000014E0	E5E2D9D7 40404040			857+	DC	CL8' VSRP'	instruction name
000014E8	00000010			858+	DC	A(16)	result length
000014EC	00001510			859+REA10	DC	A(RE10)	result address
				860+*			INSTRUCTION UNDER TEST ROUTINE
000014F0				861+X10	DS	0F	
000014F0	5820 500C		000014DC	862+	L	R2, V2_10	get v2
000014F4	E722 0000 0006		00000000	863+	VL	V2, 0(R2)	
000014FA	E612 E119 F059			864+	VSRP	V1, V2, 159, 225, 1	test instruction
00001500	E710 8F10 000E		00001110	865+	VST	V1, V10UTPUT	save result
00001506	B98D 0020			866+	EPSW	R2, R0	exptract psw
0000150A	5020 8EF0		000010F0	867+	ST	R2, CCPSW	to save CC
0000150E	07FB			868+	BR	R11	return
00001510				869+RE10	DC	0F	
00001510				870+	DROP	R5	
00001510	00000000 00000000			871	DC	XL16' 0000000000000000000000000000001D'	V1 (note: C)
00001518	00000000 0000001D						
00001520	99990000 00000000			872	DC	XL16' 9999000000000000000000000000028D'	V2
00001528	00000000 0000028D						
				873			
				874	VRI_G	VSRP, 159, 224, 1, 0	shamt=- 32 (right) drd=1
00001530				875+	DS	0FD	
00001530		00001530		876+	USING	*, R5	base for test data and test routine
00001530	00001550			877+T11	DC	A(X11)	address of test routine
00001534	000B			878+	DC	H' 11'	test number
00001536	00			879+	DC	X' 00'	
00001537	9F			880+	DC	HL1' 159'	i3
00001538	E0			881+	DC	HL1' 224'	i4
00001539	01			882+	DC	HL1' 1'	m5
0000153A	00			883+	DC	HL1' 0'	cc
0000153B	07			884+	DC	HL1' 7'	cc failed mask
0000153C	00001580			885+V2_11	DC	A(RE11+16)	address of v2: 16-byte packed decimal
00001540	E5E2D9D7 40404040			886+	DC	CL8' VSRP'	instruction name
00001548	00000010			887+	DC	A(16)	result length
0000154C	00001570			888+REA11	DC	A(RE11)	result address
				889+*			INSTRUCTION UNDER TEST ROUTINE
00001550				890+X11	DS	0F	
00001550	5820 500C		0000153C	891+	L	R2, V2_11	get v2
00001554	E722 0000 0006		00000000	892+	VL	V2, 0(R2)	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000155A	E612 E019 F059			893+	VSRP	V1, V2, 159, 224, 1	test instruction
00001560	E710 8F10 000E		00001110	894+	VST	V1, V10OUTPUT	save result
00001566	B98D 0020			895+	EPSW	R2, R0	exptract psw
0000156A	5020 8EF0		000010F0	896+	ST	R2, CCPSW	to save CC
0000156E	07FB			897+	BR	R11	return
00001570				898+RE11	DC	0F	
00001570				899+	DROP	R5	
00001570	00000000 00000000			900	DC	XL16' 00000000000000000000000000000000C'	V1 (note: C)
00001578	00000000 0000000C						
00001580	90000000 00000000			901	DC	XL16' 9000000000000000000000000000000028D'	V2
00001588	00000000 0000028D						
				902			
				903 *	m5 tests (note: cs is always 1)		
				904 *		m5=1 (p2=0, p1=0, cs=1)	
				905 *		m5=3 (p2=0, p1=1, cs=1)	
				906 *		m5=9 (p2=1, p1=0, cs=1)	
				907 *		m5=11 (p2=1, p1=1, cs=1)	
				908			
00001590				909	VRI_G	VSRP, 159, 255, 3, 2	shamt=-1 (right) drd=1 p1=1
00001590		00001590		910+	DS	0FD	
00001590	000015B0			911+	USING	*, R5	base for test data and test routine
00001594	000C			912+T12	DC	A(X12)	address of test routine
00001596	00			913+	DC	H' 12'	test number
00001597	9F			914+	DC	X' 00'	
00001598	FF			915+	DC	HL1' 159'	i3
00001598	FF			916+	DC	HL1' 255'	i4
00001599	03			917+	DC	HL1' 3'	m5
0000159A	02			918+	DC	HL1' 2'	cc
0000159B	0D			919+	DC	HL1' 13'	cc failed mask
0000159C	000015E0			920+V2_12	DC	A(RE12+16)	address of v2: 16-byte packed decimal
000015A0	E5E2D9D7 40404040			921+	DC	CL8' VSRP'	instruction name
000015A8	00000010			922+	DC	A(16)	result length
000015AC	000015D0			923+REA12	DC	A(RE12)	result address
				924+*			INSTRUCTION UNDER TEST ROUTINE
000015B0				925+X12	DS	0F	
000015B0	5820 500C		0000159C	926+	L	R2, V2_12	get v2
000015B4	E722 0000 0006		00000000	927+	VL	V2, 0(R2)	
000015BA	E612 FF39 F059			928+	VSRP	V1, V2, 159, 255, 3	test instruction
000015C0	E710 8F10 000E		00001110	929+	VST	V1, V10OUTPUT	save result
000015C6	B98D 0020			930+	EPSW	R2, R0	exptract psw
000015CA	5020 8EF0		000010F0	931+	ST	R2, CCPSW	to save CC
000015CE	07FB			932+	BR	R11	return
000015D0				933+RE12	DC	0F	
000015D0				934+	DROP	R5	
000015D0	00000000 00000000			935	DC	XL16' 000000000000000000000000000000003F'	V1
000015D8	00000000 0000003F						
000015E0	00000000 00000000			936	DC	XL16' 0000000000000000000000000000000028D'	V2
000015E8	00000000 0000028D						
				937			
000015F0				938	VRI_G	VSRP, 159, 255, 9, 2	shamt=-1 (right) drd=1 p2=1 p1=0
000015F0		000015F0		939+	DS	0FD	
000015F0	00001610			940+	USING	*, R5	base for test data and test routine
000015F4	000D			941+T13	DC	A(X13)	address of test routine
000015F6	00			942+	DC	H' 13'	test number
000015F6	00			943+	DC	X' 00'	
000015F7	9F			944+	DC	HL1' 159'	i3

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00001750	00000000 00000000			1051	DC	XL16' 0000000000000000000000000000220C'	V1	
00001758	00000000 0000220C							
00001760	00000000 00000000			1052	DC	XL16' 000000000000000000000000000022D'	V2	
00001768	00000000 0000022D							
				1053				
00001770				1054	VRI_G	VSRP, 159, 7, 11, 2	shamt=7 (left)	p2=1 p1=1
00001770		00001770		1055+	DS	OFD		
00001770	00001790			1056+	USING	*, R5	base for test data and test routine	
00001774	0011			1057+T17	DC	A(X17)	address of test routine	
00001776	00			1058+	DC	H' 17'	test number	
00001776	00			1059+	DC	X' 00'		
00001777	9F			1060+	DC	HL1' 159'	i3	
00001778	07			1061+	DC	HL1' 7'	i4	
00001779	0B			1062+	DC	HL1' 11'	m5	
0000177A	02			1063+	DC	HL1' 2'	cc	
0000177B	0D			1064+	DC	HL1' 13'	cc failed mask	
0000177C	000017C0			1065+V2_17	DC	A(RE17+16)	address of v2: 16-byte packed decimal	
00001780	E5E2D9D7 40404040			1066+	DC	CL8' VSRP'	instruction name	
00001788	00000010			1067+	DC	A(16)	result length	
0000178C	000017B0			1068+REA17	DC	A(RE17)	result address	
				1069+*			INSTRUCTION UNDER TEST ROUTINE	
00001790				1070+X17	DS	OF		
00001790	5820 500C		0000177C	1071+	L	R2, V2_17	get v2	
00001794	E722 0000 0006		00000000	1072+	VL	V2, 0(R2)		
0000179A	E612 07B9 F059			1073+	VSRP	V1, V2, 159, 7, 11	test instruction	
000017A0	E710 8F10 000E		00001110	1074+	VST	V1, V10OUTPUT	save result	
000017A6	B98D 0020			1075+	EPSW	R2, R0	exptract psw	
000017AA	5020 8EF0		000010F0	1076+	ST	R2, CCPSW	to save CC	
000017AE	07FB			1077+	BR	R11	return	
000017B0				1078+RE17	DC	OF		
000017B0				1079+	DROP	R5		
000017B0	00000000 00000000			1080	DC	XL16' 000000000000000000000000220000000F'	V1	
000017B8	00000022 0000000F							
000017C0	00000000 00000000			1081	DC	XL16' 000000000000000000000000000022D'	V2	
000017C8	00000000 0000022D							
				1082				
				1083	*	-----		
				1084	*	VPSOP - VECTOR PERFORM SIGN OPERATION DECIMAL		
				1085	*	-----		
				1086	*	VPSOP with some I3, I4 and M5's		
				1087	*	I3 tests		
				1088	*	i4=129 (iom=1, rdc=1)		
				1089	*	i4=132 (iom=1, rdc=4)		
				1090	*	i4=135 (iom=1, rdc=7)		
				1091	*	i4=142 (iom=1, rdc=14)		
				1092	*	i4=159 (iom=1, rdc=31)		
				1093	*	I4 : so=00		
				1094	*	i4=128 (nv=1, nz=0, //, so=00, pc=0, sv=0)		
				1095	*	i4=130 (nv=1, nz=0, //, so=00, pc=1, sv=0)		
				1096	*			
				1097	*	i4=192 (nv=1, nz=1, //, so=00, pc=0, sv=0)		
				1098	*	i4=194 (nv=1, nz=1, //, so=00, pc=1, sv=0)		
				1099				
				1100	*	I4 : so=01		
				1101	*	i4=132 (nv=1, nz=0, //, so=01, pc=0, sv=0)		
				1102	*	i4=134 (nv=1, nz=0, //, so=01, pc=1, sv=0)		

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1103 *
				1104 * i 4=196 (nv=1, nz=1, //, so=01, pc=0, sv=0)
				1105 * i 4=198 (nv=1, nz=1, //, so=01, pc=1, sv=0)
				1106
				1107
				1108 * I 4 : so=10
				1109 * i 4=136 (nv=1, nz=0, //, so=10, pc=0, sv=0)
				1110 * i 4=138 (nv=1, nz=0, //, so=10, pc=1, sv=0)
				1111 *
				1112 * i 4=200 (nv=1, nz=1, //, so=10, pc=0, sv=0)
				1113 * i 4=202 (nv=1, nz=1, //, so=10, pc=1, sv=0)
				1114
				1115 * I 4 : so=11
				1116 * i 4=140 (nv=1, nz=0, //, so=11, pc=0, sv=0)
				1117 * i 4=142 (nv=1, nz=0, //, so=11, pc=1, sv=0)
				1118 *
				1119 * i 4=204 (nv=1, nz=1, //, so=11, pc=0, sv=0)
				1120 * i 4=206 (nv=1, nz=1, //, so=11, pc=1, sv=0)
				1121
				1122 * m5 tests (note: cs is always 1)
				1123 * m5=1 (cs=1)
				1124 *
				1125 * z/Architecture Principles of Operation, SA22- 7832- 12.
				1126 * Figure 25- 4. Operation of VECTOR PERFORM SIGN OPERATION DECIMAL
				1127 * tests
				1128 *
				1129 *-----
				1130 * SC=00 (maintain): nv=1 to avoid data exceptions
				1131 *-----
				1132 * V1: nonzero V2: positive PC= ' 0' NZ= ' 0' V1_sign=C CC=2
				1133 VRI_G VPSOP, 159, 128, 1, 2 nz=0
				1134+ DS OFD
000017D0				1135+ USING *, R5 base for test data and test routine
000017D0	000017F0	000017D0		1136+T18 DC A(X18) address of test routine
000017D4	0012			1137+ DC H' 18' test number
000017D6	00			1138+ DC X' 00'
000017D7	9F			1139+ DC HL1' 159' i 3
000017D8	80			1140+ DC HL1' 128' i 4
000017D9	01			1141+ DC HL1' 1' m5
000017DA	02			1142+ DC HL1' 2' cc
000017DB	0D			1143+ DC HL1' 13' cc failed mask
000017DC	00001820			1144+V2_18 DC A(RE18+16) address of v2: 16-byte packed decimal
000017E0	E5D7E2D6 D7404040			1145+ DC CL8' VPSOP' instruction name
000017E8	00000010			1146+ DC A(16) result length
000017EC	00001810			1147+REA18 DC A(RE18) result address
				1148+* INSTRUCTION UNDER TEST ROUTINE
000017F0				1149+X18 DS OF
000017F0	5820 500C	000017DC		1150+ L R2, V2_18 get v2
000017F4	E722 0000 0006	00000000		1151+ VL V2, 0(R2)
000017FA	E612 8019 F05B			1152+ VPSOP V1, V2, 159, 128, 1 test instruction
00001800	E710 8F10 000E	00001110		1153+ VST V1, V10OUTPUT save result
00001806	B98D 0020			1154+ EPSW R2, R0 exptract psw
0000180A	5020 8EF0	000010F0		1155+ ST R2, CCPSW to save CC
0000180E	07FB			1156+ BR R11 return
00001810				1157+RE18 DC OF
00001810				1158+ DROP R5

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00001810	00000000 00000000			1159	DC	XL16' 000000000000000000000000220000000C'	V1	
00001818	00000022 0000000C							
00001820	00000000 00000000			1160	DC	XL16' 000000000000000000000000220000000F'	V2	
00001828	00000022 0000000F							
				1161				
				1162	VRI_G	VPSOP, 159, 192, 1, 2	nz=1	
00001830				1163+	DS	OFD		
00001830		00001830		1164+	USING	*, R5	base for test data and test routine	
00001830	00001850			1165+T19	DC	A(X19)	address of test routine	
00001834	0013			1166+	DC	H' 19'	test number	
00001836	00			1167+	DC	X' 00'		
00001837	9F			1168+	DC	HL1' 159'	i3	
00001838	C0			1169+	DC	HL1' 192'	i4	
00001839	01			1170+	DC	HL1' 1'	m5	
0000183A	02			1171+	DC	HL1' 2'	cc	
0000183B	0D			1172+	DC	HL1' 13'	cc failed mask	
0000183C	00001880			1173+V2_19	DC	A(RE19+16)	address of v2: 16-byte packed decimal	
00001840	E5D7E2D6 D7404040			1174+	DC	CL8' VPSOP'	instruction name	
00001848	00000010			1175+	DC	A(16)	result length	
0000184C	00001870			1176+REA19	DC	A(RE19)	result address	
				1177+*			INSTRUCTION UNDER TEST ROUTINE	
00001850				1178+X19	DS	0F		
00001850	5820 500C		0000183C	1179+	L	R2, V2_19	get v2	
00001854	E722 0000 0006		00000000	1180+	VL	V2, 0(R2)		
0000185A	E612 C019 F05B			1181+	VPSOP	V1, V2, 159, 192, 1	test instruction	
00001860	E710 8F10 000E		00001110	1182+	VST	V1, V10UTPUT	save result	
00001866	B98D 0020			1183+	EPSW	R2, R0	exptract psw	
0000186A	5020 8EF0		000010F0	1184+	ST	R2, CCPSW	to save CC	
0000186E	07FB			1185+	BR	R11	return	
00001870				1186+RE19	DC	0F		
00001870				1187+	DROP	R5		
00001870	00000000 00000000			1188	DC	XL16' 000000000000000000000000220000000C'	V1	
00001878	00000022 0000000C							
00001880	00000000 00000000			1189	DC	XL16' 000000000000000000000000220000000F'	V2	
00001888	00000022 0000000F							
				1190				
				1191	* V1: nonzero V2: positive PC=' 1' NZ=' â€‘'			V1_sign=F CC=2
				1192	VRI_G	VPSOP, 159, 130, 1, 2	nz=0	
00001890				1193+	DS	OFD		
00001890		00001890		1194+	USING	*, R5	base for test data and test routine	
00001890	000018B0			1195+T20	DC	A(X20)	address of test routine	
00001894	0014			1196+	DC	H' 20'	test number	
00001896	00			1197+	DC	X' 00'		
00001897	9F			1198+	DC	HL1' 159'	i3	
00001898	82			1199+	DC	HL1' 130'	i4	
00001899	01			1200+	DC	HL1' 1'	m5	
0000189A	02			1201+	DC	HL1' 2'	cc	
0000189B	0D			1202+	DC	HL1' 13'	cc failed mask	
0000189C	000018E0			1203+V2_20	DC	A(RE20+16)	address of v2: 16-byte packed decimal	
000018A0	E5D7E2D6 D7404040			1204+	DC	CL8' VPSOP'	instruction name	
000018A8	00000010			1205+	DC	A(16)	result length	
000018AC	000018D0			1206+REA20	DC	A(RE20)	result address	
				1207+*			INSTRUCTION UNDER TEST ROUTINE	
000018B0				1208+X20	DS	0F		
000018B0	5820 500C		0000189C	1209+	L	R2, V2_20	get v2	
000018B4	E722 0000 0006		00000000	1210+	VL	V2, 0(R2)		

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000018BA	E612 8219 F05B			1211+	VPSOP	V1, V2, 159, 130, 1	test instruction
000018C0	E710 8F10 000E		00001110	1212+	VST	V1, V10OUTPUT	save result
000018C6	B98D 0020			1213+	EPSW	R2, R0	exptract psw
000018CA	5020 8EF0		000010F0	1214+	ST	R2, CCPSW	to save CC
000018CE	07FB			1215+	BR	R11	return
000018D0				1216+RE20	DC	0F	
000018D0				1217+	DROP	R5	
000018D0	00000000 00000000			1218	DC	XL16' 000000000000000000000000220000000F'	V1
000018D8	00000022 0000000F						
000018E0	00000000 00000000			1219	DC	XL16' 000000000000000000000000220000000A'	V2
000018E8	00000022 0000000A						
				1220			
000018F0				1221	VRI_G	VPSOP, 159, 194, 1, 2	nz=1
000018F0		000018F0		1222+	DS	0FD	
000018F0	00001910			1223+	USING	*, R5	base for test data and test routine
000018F4	0015			1224+T21	DC	A(X21)	address of test routine
000018F6	00			1225+	DC	H' 21'	test number
000018F7	9F			1226+	DC	X' 00'	
000018F8	C2			1227+	DC	HL1' 159'	i3
000018F9	01			1228+	DC	HL1' 194'	i4
000018FA	02			1229+	DC	HL1' 1'	m5
000018FB	0D			1230+	DC	HL1' 2'	cc
000018FC	00001940			1231+	DC	HL1' 13'	cc failed mask
00001900	E5D7E2D6 D7404040			1232+V2_21	DC	A(RE21+16)	address of v2: 16-byte packed decimal
00001908	00000010			1233+	DC	CL8' VPSOP'	instruction name
0000190C	00001930			1234+	DC	A(16)	result length
				1235+REA21	DC	A(RE21)	result address
				1236+*			INSTRUCTION UNDER TEST ROUTINE
00001910				1237+X21	DS	0F	
00001910	5820 500C		000018FC	1238+	L	R2, V2_21	get v2
00001914	E722 0000 0006		00000000	1239+	VL	V2, 0(R2)	
0000191A	E612 C219 F05B			1240+	VPSOP	V1, V2, 159, 194, 1	test instruction
00001920	E710 8F10 000E		00001110	1241+	VST	V1, V10OUTPUT	save result
00001926	B98D 0020			1242+	EPSW	R2, R0	exptract psw
0000192A	5020 8EF0		000010F0	1243+	ST	R2, CCPSW	to save CC
0000192E	07FB			1244+	BR	R11	return
00001930				1245+RE21	DC	0F	
00001930				1246+	DROP	R5	
00001930	00000000 00000000			1247	DC	XL16' 000000000000000000000000220000000F'	V1
00001938	00000022 0000000F						
00001940	00000000 00000000			1248	DC	XL16' 000000000000000000000000220000000A'	V2
00001948	00000022 0000000A						
				1249			
				1250 *	V1: nonzero V2: negative PC=' - ' NZ=' â€‘' V1_sign=D CC=1		
				1251	VRI_G	VPSOP, 159, 128, 1, 1	nz=0 pc=0
00001950		00001950		1252+	DS	0FD	
00001950				1253+	USING	*, R5	base for test data and test routine
00001950	00001970			1254+T22	DC	A(X22)	address of test routine
00001954	0016			1255+	DC	H' 22'	test number
00001956	00			1256+	DC	X' 00'	
00001957	9F			1257+	DC	HL1' 159'	i3
00001958	80			1258+	DC	HL1' 128'	i4
00001959	01			1259+	DC	HL1' 1'	m5
0000195A	01			1260+	DC	HL1' 1'	cc
0000195B	0B			1261+	DC	HL1' 11'	cc failed mask
0000195C	000019A0			1262+V2_22	DC	A(RE22+16)	address of v2: 16-byte packed decimal

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001960	E5D7E2D6 D7404040			1263+	DC	CL8' VPSOP'	instruction name
00001968	00000010			1264+	DC	A(16)	result length
0000196C	00001990			1265+REA22	DC	A(RE22)	result address
				1266+*			INSTRUCTION UNDER TEST ROUTINE
00001970				1267+X22	DS	0F	
00001970	5820 500C		0000195C	1268+	L	R2, V2_22	get v2
00001974	E722 0000 0006		00000000	1269+	VL	V2, 0(R2)	
0000197A	E612 8019 F05B			1270+	VPSOP	V1, V2, 159, 128, 1	test instruction
00001980	E710 8F10 000E		00001110	1271+	VST	V1, V10UTPUT	save result
00001986	B98D 0020			1272+	EPSW	R2, R0	exptract psw
0000198A	5020 8EF0		000010F0	1273+	ST	R2, CCPSW	to save CC
0000198E	07FB			1274+	BR	R11	return
00001990				1275+RE22	DC	0F	
00001990				1276+	DROP	R5	
00001990	00000000 00000000			1277	DC	XL16' 000000000000000000000000220000000D'	V1
00001998	00000022 0000000D						
000019A0	00000000 00000000			1278	DC	XL16' 000000000000000000000000220000000B'	V2
000019A8	00000022 0000000B						
				1279			
000019B0				1280	VRI_G	VPSOP, 159, 130, 1, 1	nz=0 pc=1
000019B0		000019B0		1281+	DS	0FD	
000019B0	000019D0			1282+	USING	*, R5	base for test data and test routine
000019B4	0017			1283+T23	DC	A(X23)	address of test routine
000019B6	00			1284+	DC	H' 23'	test number
000019B7	9F			1285+	DC	X' 00'	
000019B8	82			1286+	DC	HL1' 159'	i3
000019B9	01			1287+	DC	HL1' 130'	i4
000019BA	01			1288+	DC	HL1' 1'	m5
000019BB	0B			1289+	DC	HL1' 1'	cc
000019BC	00001A00			1290+	DC	HL1' 11'	cc failed mask
000019C0	E5D7E2D6 D7404040			1291+V2_23	DC	A(RE23+16)	address of v2: 16-byte packed decimal
000019C8	00000010			1292+	DC	CL8' VPSOP'	instruction name
000019CC	000019F0			1293+	DC	A(16)	result length
				1294+REA23	DC	A(RE23)	result address
				1295+*			INSTRUCTION UNDER TEST ROUTINE
000019D0				1296+X23	DS	0F	
000019D0	5820 500C		000019BC	1297+	L	R2, V2_23	get v2
000019D4	E722 0000 0006		00000000	1298+	VL	V2, 0(R2)	
000019DA	E612 8219 F05B			1299+	VPSOP	V1, V2, 159, 130, 1	test instruction
000019E0	E710 8F10 000E		00001110	1300+	VST	V1, V10UTPUT	save result
000019E6	B98D 0020			1301+	EPSW	R2, R0	exptract psw
000019EA	5020 8EF0		000010F0	1302+	ST	R2, CCPSW	to save CC
000019EE	07FB			1303+	BR	R11	return
000019F0				1304+RE23	DC	0F	
000019F0				1305+	DROP	R5	
000019F0	00000000 00000000			1306	DC	XL16' 000000000000000000000000220000000D'	V1
000019F8	00000022 0000000D						
00001A00	00000000 00000000			1307	DC	XL16' 000000000000000000000000220000000B'	V2
00001A08	00000022 0000000B						
				1308			
00001A10				1309	VRI_G	VPSOP, 159, 192, 1, 1	nz=1 pc=0
00001A10		00001A10		1310+	DS	0FD	
00001A10	00001A30			1311+	USING	*, R5	base for test data and test routine
00001A14	0018			1312+T24	DC	A(X24)	address of test routine
00001A16	00			1313+	DC	H' 24'	test number
				1314+	DC	X' 00'	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001A17	9F			1315+	DC	HL1' 159'	i3
00001A18	C0			1316+	DC	HL1' 192'	i4
00001A19	01			1317+	DC	HL1' 1'	m5
00001A1A	01			1318+	DC	HL1' 1'	cc
00001A1B	0B			1319+	DC	HL1' 11'	cc failed mask
00001A1C	00001A60			1320+V2_24	DC	A(RE24+16)	address of v2: 16-byte packed decimal
00001A20	E5D7E2D6 D7404040			1321+	DC	CL8' VPSOP'	instruction name
00001A28	00000010			1322+	DC	A(16)	result length
00001A2C	00001A50			1323+REA24	DC	A(RE24)	result address
				1324+*			INSTRUCTION UNDER TEST ROUTINE
00001A30				1325+X24	DS	0F	
00001A30	5820 500C		00001A1C	1326+	L	R2, V2_24	get v2
00001A34	E722 0000 0006		00000000	1327+	VL	V2, 0(R2)	
00001A3A	E612 C019 F05B			1328+	VPSOP	V1, V2, 159, 192, 1	test instruction
00001A40	E710 8F10 000E		00001110	1329+	VST	V1, V10UTPUT	save result
00001A46	B98D 0020			1330+	EPSW	R2, R0	exptract psw
00001A4A	5020 8EF0		000010F0	1331+	ST	R2, CCPSW	to save CC
00001A4E	07FB			1332+	BR	R11	return
00001A50				1333+RE24	DC	0F	
00001A50				1334+	DROP	R5	
00001A50	00000000 00000000			1335	DC	XL16' 000000000000000000000000220000000D'	V1
00001A58	00000022 0000000D						
00001A60	00000000 00000000			1336	DC	XL16' 000000000000000000000000220000000B'	V2
00001A68	00000022 0000000B						
				1337			
				1338	VRI_G	VPSOP, 159, 194, 1, 1	nz=1 pc=1
00001A70				1339+	DS	0FD	
00001A70		00001A70		1340+	USING	*, R5	base for test data and test routine
00001A70	00001A90			1341+T25	DC	A(X25)	address of test routine
00001A74	0019			1342+	DC	H' 25'	test number
00001A76	00			1343+	DC	X' 00'	
00001A77	9F			1344+	DC	HL1' 159'	i3
00001A78	C2			1345+	DC	HL1' 194'	i4
00001A79	01			1346+	DC	HL1' 1'	m5
00001A7A	01			1347+	DC	HL1' 1'	cc
00001A7B	0B			1348+	DC	HL1' 11'	cc failed mask
00001A7C	00001AC0			1349+V2_25	DC	A(RE25+16)	address of v2: 16-byte packed decimal
00001A80	E5D7E2D6 D7404040			1350+	DC	CL8' VPSOP'	instruction name
00001A88	00000010			1351+	DC	A(16)	result length
00001A8C	00001AB0			1352+REA25	DC	A(RE25)	result address
				1353+*			INSTRUCTION UNDER TEST ROUTINE
00001A90				1354+X25	DS	0F	
00001A90	5820 500C		00001A7C	1355+	L	R2, V2_25	get v2
00001A94	E722 0000 0006		00000000	1356+	VL	V2, 0(R2)	
00001A9A	E612 C219 F05B			1357+	VPSOP	V1, V2, 159, 194, 1	test instruction
00001AA0	E710 8F10 000E		00001110	1358+	VST	V1, V10UTPUT	save result
00001AA6	B98D 0020			1359+	EPSW	R2, R0	exptract psw
00001AAA	5020 8EF0		000010F0	1360+	ST	R2, CCPSW	to save CC
00001AAE	07FB			1361+	BR	R11	return
00001AB0				1362+RE25	DC	0F	
00001AB0				1363+	DROP	R5	
00001AB0	00000000 00000000			1364	DC	XL16' 000000000000000000000000220000000D'	V1
00001AB8	00000022 0000000D						
00001AC0	00000000 00000000			1365	DC	XL16' 000000000000000000000000220000000B'	V2
00001AC8	00000022 0000000B						
				1366			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				1367 *	V1: nonzero	V2: invalid	PC=' - '	NZ=' â€‘' V1_sign=V2 CC=2
				1368	VRI_G	VPSOP, 159, 128, 1, 2		nz=0 pc=0
00001AD0				1369+	DS	OFD		
00001AD0		00001AD0		1370+	USING	*, R5		base for test data and test routine
00001AD0	00001AF0			1371+T26	DC	A(X26)		address of test routine
00001AD4	001A			1372+	DC	H' 26'		test number
00001AD6	00			1373+	DC	X' 00'		
00001AD7	9F			1374+	DC	HL1' 159'		i3
00001AD8	80			1375+	DC	HL1' 128'		i4
00001AD9	01			1376+	DC	HL1' 1'		m5
00001ADA	02			1377+	DC	HL1' 2'		cc
00001ADB	0D			1378+	DC	HL1' 13'		cc failed mask
00001ADC	00001B20			1379+V2_26	DC	A(RE26+16)		address of v2: 16-byte packed decimal
00001AE0	E5D7E2D6 D7404040			1380+	DC	CL8' VPSOP'		instruction name
00001AE8	00000010			1381+	DC	A(16)		result length
00001AEC	00001B10			1382+REA26	DC	A(RE26)		result address
				1383+*				INSTRUCTION UNDER TEST ROUTINE
00001AF0				1384+X26	DS	OF		
00001AF0	5820 500C		00001ADC	1385+	L	R2, V2_26		get v2
00001AF4	E722 0000 0006		00000000	1386+	VL	V2, 0(R2)		
00001AFA	E612 8019 F05B			1387+	VPSOP	V1, V2, 159, 128, 1		test instruction
00001B00	E710 8F10 000E		00001110	1388+	VST	V1, V10UTPUT		save result
00001B06	B98D 0020			1389+	EPSW	R2, R0		exptract psw
00001B0A	5020 8EF0		000010F0	1390+	ST	R2, CCPSW		to save CC
00001B0E	07FB			1391+	BR	R11		return
00001B10				1392+RE26	DC	OF		
00001B10				1393+	DROP	R5		
00001B10	00000000 00000000			1394	DC	XL16' 0000000000000000000000002200000009'		V1
00001B18	00000022 00000009							
00001B20	00000000 00000000			1395	DC	XL16' 0000000000000000000000002200000009'		V2
00001B28	00000022 00000009							
				1396				
				1397	VRI_G	VPSOP, 159, 130, 1, 2		nz=0 pc=1
00001B30				1398+	DS	OFD		
00001B30		00001B30		1399+	USING	*, R5		base for test data and test routine
00001B30	00001B50			1400+T27	DC	A(X27)		address of test routine
00001B34	001B			1401+	DC	H' 27'		test number
00001B36	00			1402+	DC	X' 00'		
00001B37	9F			1403+	DC	HL1' 159'		i3
00001B38	82			1404+	DC	HL1' 130'		i4
00001B39	01			1405+	DC	HL1' 1'		m5
00001B3A	02			1406+	DC	HL1' 2'		cc
00001B3B	0D			1407+	DC	HL1' 13'		cc failed mask
00001B3C	00001B80			1408+V2_27	DC	A(RE27+16)		address of v2: 16-byte packed decimal
00001B40	E5D7E2D6 D7404040			1409+	DC	CL8' VPSOP'		instruction name
00001B48	00000010			1410+	DC	A(16)		result length
00001B4C	00001B70			1411+REA27	DC	A(RE27)		result address
				1412+*				INSTRUCTION UNDER TEST ROUTINE
00001B50				1413+X27	DS	OF		
00001B50	5820 500C		00001B3C	1414+	L	R2, V2_27		get v2
00001B54	E722 0000 0006		00000000	1415+	VL	V2, 0(R2)		
00001B5A	E612 8219 F05B			1416+	VPSOP	V1, V2, 159, 130, 1		test instruction
00001B60	E710 8F10 000E		00001110	1417+	VST	V1, V10UTPUT		save result
00001B66	B98D 0020			1418+	EPSW	R2, R0		exptract psw
00001B6A	5020 8EF0		000010F0	1419+	ST	R2, CCPSW		to save CC
00001B6E	07FB			1420+	BR	R11		return

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00001B70				1421+RE27	DC	0F		
00001B70				1422+	DROP	R5		
00001B70	00000000 00000000			1423	DC	XL16' 00000000000000000000000002200000009'	V1	
00001B78	00000022 00000009							
00001B80	00000000 00000000			1424	DC	XL16' 00000000000000000000000002200000009'	V2	
00001B88	00000022 00000009							
				1425				
				1426	VRI_G	VPSOP, 159, 192, 1, 2	nz=1 pc=0	
00001B90				1427+	DS	0FD		
00001B90		00001B90		1428+	USING	*, R5	base for test data and test routine	
00001B90	00001BB0			1429+T28	DC	A(X28)	address of test routine	
00001B94	001C			1430+	DC	H' 28'	test number	
00001B96	00			1431+	DC	X' 00'		
00001B97	9F			1432+	DC	HL1' 159'	i3	
00001B98	C0			1433+	DC	HL1' 192'	i4	
00001B99	01			1434+	DC	HL1' 1'	m5	
00001B9A	02			1435+	DC	HL1' 2'	cc	
00001B9B	0D			1436+	DC	HL1' 13'	cc failed mask	
00001B9C	00001BE0			1437+V2_28	DC	A(RE28+16)	address of v2: 16-byte packed decimal	
00001BA0	E5D7E2D6 D7404040			1438+	DC	CL8' VPSOP'	instruction name	
00001BA8	00000010			1439+	DC	A(16)	result length	
00001BAC	00001BD0			1440+REA28	DC	A(RE28)	result address	
				1441+*			INSTRUCTION UNDER TEST ROUTINE	
00001BB0				1442+X28	DS	0F		
00001BB0	5820 500C		00001B9C	1443+	L	R2, V2_28	get v2	
00001BB4	E722 0000 0006		00000000	1444+	VL	V2, 0(R2)		
00001BBA	E612 C019 F05B			1445+	VPSOP	V1, V2, 159, 192, 1	test instruction	
00001BC0	E710 8F10 000E		00001110	1446+	VST	V1, V10UTPUT	save result	
00001BC6	B98D 0020			1447+	EPSW	R2, R0	exptract psw	
00001BCA	5020 8EF0		000010F0	1448+	ST	R2, CCPSW	to save CC	
00001BCE	07FB			1449+	BR	R11	return	
00001BD0				1450+RE28	DC	0F		
00001BD0				1451+	DROP	R5		
00001BD0	00000000 00000000			1452	DC	XL16' 00000000000000000000000002200000009'	V1	
00001BD8	00000022 00000009							
00001BE0	00000000 00000000			1453	DC	XL16' 00000000000000000000000002200000009'	V2	
00001BE8	00000022 00000009							
				1454				
				1455	VRI_G	VPSOP, 159, 194, 1, 2	nz=1 pc=1	
00001BF0				1456+	DS	0FD		
00001BF0		00001BF0		1457+	USING	*, R5	base for test data and test routine	
00001BF0	00001C10			1458+T29	DC	A(X29)	address of test routine	
00001BF4	001D			1459+	DC	H' 29'	test number	
00001BF6	00			1460+	DC	X' 00'		
00001BF7	9F			1461+	DC	HL1' 159'	i3	
00001BF8	C2			1462+	DC	HL1' 194'	i4	
00001BF9	01			1463+	DC	HL1' 1'	m5	
00001BFA	02			1464+	DC	HL1' 2'	cc	
00001BFB	0D			1465+	DC	HL1' 13'	cc failed mask	
00001BFC	00001C40			1466+V2_29	DC	A(RE29+16)	address of v2: 16-byte packed decimal	
00001C00	E5D7E2D6 D7404040			1467+	DC	CL8' VPSOP'	instruction name	
00001C08	00000010			1468+	DC	A(16)	result length	
00001C0C	00001C30			1469+REA29	DC	A(RE29)	result address	
				1470+*			INSTRUCTION UNDER TEST ROUTINE	
00001C10				1471+X29	DS	0F		
00001C10	5820 500C		00001BFC	1472+	L	R2, V2_29	get v2	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00001C14	E722 0000 0006		00000000	1473+	VL	V2, 0(R2)		
00001C1A	E612 C219 F05B			1474+	VPSOP	V1, V2, 159, 194, 1	test instruction	
00001C20	E710 8F10 000E		00001110	1475+	VST	V1, V10UTPUT	save result	
00001C26	B98D 0020			1476+	EPSW	R2, R0	exptract psw	
00001C2A	5020 8EF0		000010F0	1477+	ST	R2, CCPSW	to save CC	
00001C2E	07FB			1478+	BR	R11	return	
00001C30				1479+RE29	DC	0F		
00001C30				1480+	DROP	R5		
00001C30	00000000 00000000			1481	DC	XL16' 00000000000000000000000002200000009'	V1	
00001C38	00000022 00000009							
00001C40	00000000 00000000			1482	DC	XL16' 00000000000000000000000002200000009'	V2	
00001C48	00000022 00000009							
				1483				
				1484 *	V1: zero V2: positive PC=' 0' NZ=' â€‘' V1_sign=C CC=0			
				1485	VRI_G	VPSOP, 159, 128, 1, 0	nz=0 pc=0	
00001C50				1486+	DS	0FD		
00001C50		00001C50		1487+	USING	*, R5	base for test data and test routine	
00001C50	00001C70			1488+T30	DC	A(X30)	address of test routine	
00001C54	001E			1489+	DC	H' 30'	test number	
00001C56	00			1490+	DC	X' 00'		
00001C57	9F			1491+	DC	HL1' 159'	i3	
00001C58	80			1492+	DC	HL1' 128'	i4	
00001C59	01			1493+	DC	HL1' 1'	m5	
00001C5A	00			1494+	DC	HL1' 0'	cc	
00001C5B	07			1495+	DC	HL1' 7'	cc failed mask	
00001C5C	00001CA0			1496+V2_30	DC	A(RE30+16)	address of v2: 16-byte packed decimal	
00001C60	E5D7E2D6 D7404040			1497+	DC	CL8' VPSOP'	instruction name	
00001C68	00000010			1498+	DC	A(16)	result length	
00001C6C	00001C90			1499+REA30	DC	A(RE30)	result address	
				1500+*			INSTRUCTION UNDER TEST ROUTINE	
00001C70				1501+X30	DS	0F		
00001C70	5820 500C		00001C5C	1502+	L	R2, V2_30	get v2	
00001C74	E722 0000 0006		00000000	1503+	VL	V2, 0(R2)		
00001C7A	E612 8019 F05B			1504+	VPSOP	V1, V2, 159, 128, 1	test instruction	
00001C80	E710 8F10 000E		00001110	1505+	VST	V1, V10UTPUT	save result	
00001C86	B98D 0020			1506+	EPSW	R2, R0	exptract psw	
00001C8A	5020 8EF0		000010F0	1507+	ST	R2, CCPSW	to save CC	
00001C8E	07FB			1508+	BR	R11	return	
00001C90				1509+RE30	DC	0F		
00001C90				1510+	DROP	R5		
00001C90	00000000 00000000			1511	DC	XL16' 0000000000000000000000000000000C'	V1	
00001C98	00000000 0000000C							
00001CA0	00000000 00000000			1512	DC	XL16' 0000000000000000000000000000000F'	V2	
00001CA8	00000000 0000000F							
				1513				
				1514	VRI_G	VPSOP, 159, 192, 1, 0	nz=1 pc=0	
00001CB0				1515+	DS	0FD		
00001CB0		00001CB0		1516+	USING	*, R5	base for test data and test routine	
00001CB0	00001CD0			1517+T31	DC	A(X31)	address of test routine	
00001CB4	001F			1518+	DC	H' 31'	test number	
00001CB6	00			1519+	DC	X' 00'		
00001CB7	9F			1520+	DC	HL1' 159'	i3	
00001CB8	C0			1521+	DC	HL1' 192'	i4	
00001CB9	01			1522+	DC	HL1' 1'	m5	
00001CBA	00			1523+	DC	HL1' 0'	cc	
00001CBB	07			1524+	DC	HL1' 7'	cc failed mask	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001CBC	00001D00			1525+V2_31	DC	A(RE31+16)	address of v2: 16-byte packed decimal
00001CC0	E5D7E2D6 D7404040			1526+	DC	CL8' VPSOP'	instruction name
00001CC8	00000010			1527+	DC	A(16)	result length
00001CCC	00001CF0			1528+REA31	DC	A(RE31)	result address
				1529+*			INSTRUCTION UNDER TEST ROUTINE
00001CD0				1530+X31	DS	0F	
00001CD0	5820 500C		00001CBC	1531+	L	R2, V2_31	get v2
00001CD4	E722 0000 0006		00000000	1532+	VL	V2, 0(R2)	
00001CDA	E612 C019 F05B			1533+	VPSOP	V1, V2, 159, 192, 1	test instruction
00001CE0	E710 8F10 000E		00001110	1534+	VST	V1, V10UTPUT	save result
00001CE6	B98D 0020			1535+	EPSW	R2, R0	exptract psw
00001CEA	5020 8EF0		000010F0	1536+	ST	R2, CCPSW	to save CC
00001CEE	07FB			1537+	BR	R11	return
00001CF0				1538+RE31	DC	0F	
00001CF0				1539+	DROP	R5	
00001CF0	00000000 00000000			1540	DC	XL16' 00000000000000000000000000000000C'	V1
00001CF8	00000000 0000000C						
00001D00	00000000 00000000			1541	DC	XL16' 00000000000000000000000000000000F'	V2
00001D08	00000000 0000000F						
				1542			
				1543 * V1: zero V2: positive PC=' 1' NZ=' â€‘' V1_sign=F CC=0			
				1544	VRI_G	VPSOP, 159, 130, 1, 0	nz=0 pc=1
00001D10				1545+	DS	0FD	
00001D10		00001D10		1546+	USING	*, R5	base for test data and test routine
00001D10	00001D30			1547+T32	DC	A(X32)	address of test routine
00001D14	0020			1548+	DC	H' 32'	test number
00001D16	00			1549+	DC	X' 00'	
00001D17	9F			1550+	DC	HL1' 159'	i3
00001D18	82			1551+	DC	HL1' 130'	i4
00001D19	01			1552+	DC	HL1' 1'	m5
00001D1A	00			1553+	DC	HL1' 0'	cc
00001D1B	07			1554+	DC	HL1' 7'	cc failed mask
00001D1C	00001D60			1555+V2_32	DC	A(RE32+16)	address of v2: 16-byte packed decimal
00001D20	E5D7E2D6 D7404040			1556+	DC	CL8' VPSOP'	instruction name
00001D28	00000010			1557+	DC	A(16)	result length
00001D2C	00001D50			1558+REA32	DC	A(RE32)	result address
				1559+*			INSTRUCTION UNDER TEST ROUTINE
00001D30				1560+X32	DS	0F	
00001D30	5820 500C		00001D1C	1561+	L	R2, V2_32	get v2
00001D34	E722 0000 0006		00000000	1562+	VL	V2, 0(R2)	
00001D3A	E612 8219 F05B			1563+	VPSOP	V1, V2, 159, 130, 1	test instruction
00001D40	E710 8F10 000E		00001110	1564+	VST	V1, V10UTPUT	save result
00001D46	B98D 0020			1565+	EPSW	R2, R0	exptract psw
00001D4A	5020 8EF0		000010F0	1566+	ST	R2, CCPSW	to save CC
00001D4E	07FB			1567+	BR	R11	return
00001D50				1568+RE32	DC	0F	
00001D50				1569+	DROP	R5	
00001D50	00000000 00000000			1570	DC	XL16' 00000000000000000000000000000000F'	V1
00001D58	00000000 0000000F						
00001D60	00000000 00000000			1571	DC	XL16' 00000000000000000000000000000000C'	V2
00001D68	00000000 0000000C						
				1572			
				1573	VRI_G	VPSOP, 159, 194, 1, 0	nz=1 pc=1
00001D70		00001D70		1574+	DS	0FD	
00001D70				1575+	USING	*, R5	base for test data and test routine
00001D70	00001D90			1576+T33	DC	A(X33)	address of test routine

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001D74	0021			1577+	DC	H' 33'	test number
00001D76	00			1578+	DC	X' 00'	
00001D77	9F			1579+	DC	HL1' 159'	i3
00001D78	C2			1580+	DC	HL1' 194'	i4
00001D79	01			1581+	DC	HL1' 1'	m5
00001D7A	00			1582+	DC	HL1' 0'	cc
00001D7B	07			1583+	DC	HL1' 7'	cc failed mask
00001D7C	00001DC0			1584+V2_33	DC	A(RE33+16)	address of v2: 16-byte packed decimal
00001D80	E5D7E2D6 D7404040			1585+	DC	CL8' VPSOP'	instruction name
00001D88	00000010			1586+	DC	A(16)	result length
00001D8C	00001DB0			1587+REA33	DC	A(RE33)	result address
				1588+*			INSTRUCTION UNDER TEST ROUTINE
00001D90				1589+X33	DS	0F	
00001D90	5820 500C		00001D7C	1590+	L	R2, V2_33	get v2
00001D94	E722 0000 0006		00000000	1591+	VL	V2, 0(R2)	
00001D9A	E612 C219 F05B			1592+	VPSOP	V1, V2, 159, 194, 1	test instruction
00001DA0	E710 8F10 000E		00001110	1593+	VST	V1, V10UTPUT	save result
00001DA6	B98D 0020			1594+	EPSW	R2, R0	exptract psw
00001DAA	5020 8EF0		000010F0	1595+	ST	R2, CCPSW	to save CC
00001DAE	07FB			1596+	BR	R11	return
00001DB0				1597+RE33	DC	0F	
00001DB0				1598+	DROP	R5	
00001DB0	00000000 00000000			1599	DC	XL16' 00000000000000000000000000000000F'	V1
00001DB8	00000000 0000000F						
00001DC0	00000000 00000000			1600	DC	XL16' 00000000000000000000000000000000C'	V2
00001DC8	00000000 0000000C						
				1601			
				1602 * V1: zero V2: negative PC=' 0' NZ=' 0' V1_sign=C CC=0			
				1603	VRI_G	VPSOP, 159, 128, 1, 0	nz=0 pc=0
00001DD0				1604+	DS	0FD	
00001DD0		00001DD0		1605+	USING	*, R5	base for test data and test routine
00001DD0	00001DF0			1606+T34	DC	A(X34)	address of test routine
00001DD4	0022			1607+	DC	H' 34'	test number
00001DD6	00			1608+	DC	X' 00'	
00001DD7	9F			1609+	DC	HL1' 159'	i3
00001DD8	80			1610+	DC	HL1' 128'	i4
00001DD9	01			1611+	DC	HL1' 1'	m5
00001DDA	00			1612+	DC	HL1' 0'	cc
00001ddb	07			1613+	DC	HL1' 7'	cc failed mask
00001DDC	00001E20			1614+V2_34	DC	A(RE34+16)	address of v2: 16-byte packed decimal
00001DE0	E5D7E2D6 D7404040			1615+	DC	CL8' VPSOP'	instruction name
00001DE8	00000010			1616+	DC	A(16)	result length
00001DEC	00001E10			1617+REA34	DC	A(RE34)	result address
				1618+*			INSTRUCTION UNDER TEST ROUTINE
00001DF0				1619+X34	DS	0F	
00001DF0	5820 500C		00001DDC	1620+	L	R2, V2_34	get v2
00001DF4	E722 0000 0006		00000000	1621+	VL	V2, 0(R2)	
00001DFA	E612 8019 F05B			1622+	VPSOP	V1, V2, 159, 128, 1	test instruction
00001E00	E710 8F10 000E		00001110	1623+	VST	V1, V10UTPUT	save result
00001E06	B98D 0020			1624+	EPSW	R2, R0	exptract psw
00001E0A	5020 8EF0		000010F0	1625+	ST	R2, CCPSW	to save CC
00001E0E	07FB			1626+	BR	R11	return
00001E10				1627+RE34	DC	0F	
00001E10				1628+	DROP	R5	
00001E10	00000000 00000000			1629	DC	XL16' 00000000000000000000000000000000C'	V1
00001E18	00000000 0000000C						

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001E20	00000000 00000000			1630	DC	XL16' 0000000000000000000000000000000D'	V2
00001E28	00000000 0000000D						
				1631			
				1632	* V1: zero V2: negative PC=' 1' NZ=' 0' V1_sign=F CC=0		
				1633	VRI_G VPSOP, 159, 130, 1, 0 nz=0 pc=1		
00001E30				1634+	DS	0FD	
00001E30		00001E30		1635+	USING	*, R5	base for test data and test routine
00001E30	00001E50			1636+T35	DC	A(X35)	address of test routine
00001E34	0023			1637+	DC	H' 35'	test number
00001E36	00			1638+	DC	X' 00'	
00001E37	9F			1639+	DC	HL1' 159'	i3
00001E38	82			1640+	DC	HL1' 130'	i4
00001E39	01			1641+	DC	HL1' 1'	m5
00001E3A	00			1642+	DC	HL1' 0'	cc
00001E3B	07			1643+	DC	HL1' 7'	cc failed mask
00001E3C	00001E80			1644+V2_35	DC	A(RE35+16)	address of v2: 16-byte packed decimal
00001E40	E5D7E2D6 D7404040			1645+	DC	CL8' VPSOP'	instruction name
00001E48	00000010			1646+	DC	A(16)	result length
00001E4C	00001E70			1647+REA35	DC	A(RE35)	result address
				1648+*			INSTRUCTION UNDER TEST ROUTINE
00001E50				1649+X35	DS	0F	
00001E50	5820 500C		00001E3C	1650+	L	R2, V2_35	get v2
00001E54	E722 0000 0006		00000000	1651+	VL	V2, 0(R2)	
00001E5A	E612 8219 F05B			1652+	VPSOP	V1, V2, 159, 130, 1	test instruction
00001E60	E710 8F10 000E		00001110	1653+	VST	V1, V10UTPUT	save result
00001E66	B98D 0020			1654+	EPSW	R2, R0	exptract psw
00001E6A	5020 8EF0		000010F0	1655+	ST	R2, CCPSW	to save CC
00001E6E	07FB			1656+	BR	R11	return
00001E70				1657+RE35	DC	0F	
00001E70				1658+	DROP	R5	
00001E70	00000000 00000000			1659	DC	XL16' 000000000000000000000000000000F'	V1
00001E78	00000000 0000000F						
00001E80	00000000 00000000			1660	DC	XL16' 000000000000000000000000000000D'	V2
00001E88	00000000 0000000D						
				1661			
				1662	* V1: zero V2: negative PC=' - ' NZ=' 1' V1_sign=D CC=0		
				1663	VRI_G VPSOP, 159, 192, 1, 0 nz=1 pc=0		
00001E90				1664+	DS	0FD	
00001E90		00001E90		1665+	USING	*, R5	base for test data and test routine
00001E90	00001EB0			1666+T36	DC	A(X36)	address of test routine
00001E94	0024			1667+	DC	H' 36'	test number
00001E96	00			1668+	DC	X' 00'	
00001E97	9F			1669+	DC	HL1' 159'	i3
00001E98	C0			1670+	DC	HL1' 192'	i4
00001E99	01			1671+	DC	HL1' 1'	m5
00001E9A	00			1672+	DC	HL1' 0'	cc
00001E9B	07			1673+	DC	HL1' 7'	cc failed mask
00001E9C	00001EE0			1674+V2_36	DC	A(RE36+16)	address of v2: 16-byte packed decimal
00001EA0	E5D7E2D6 D7404040			1675+	DC	CL8' VPSOP'	instruction name
00001EA8	00000010			1676+	DC	A(16)	result length
00001EAC	00001ED0			1677+REA36	DC	A(RE36)	result address
				1678+*			INSTRUCTION UNDER TEST ROUTINE
00001EB0				1679+X36	DS	0F	
00001EB0	5820 500C		00001E9C	1680+	L	R2, V2_36	get v2
00001EB4	E722 0000 0006		00000000	1681+	VL	V2, 0(R2)	
00001EBA	E612 C019 F05B			1682+	VPSOP	V1, V2, 159, 192, 1	test instruction

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001EC0	E710 8F10 000E		00001110	1683+	VST	V1, V10OUTPUT	save result
00001EC6	B98D 0020			1684+	EPSW	R2, R0	exptract psw
00001ECA	5020 8EF0		000010F0	1685+	ST	R2, CCPSW	to save CC
00001ECE	07FB			1686+	BR	R11	return
00001ED0				1687+RE36	DC	0F	
00001ED0				1688+	DROP	R5	
00001ED0	00000000 00000000			1689	DC	XL16' 0000000000000000000000000000000D'	V1
00001ED8	00000000 0000000D						
00001EE0	00000000 00000000			1690	DC	XL16' 0000000000000000000000000000000B'	V2
00001EE8	00000000 0000000B						
				1691			
00001EF0				1692	VRI_G	VPSOP, 159, 194, 1, 0	nz=1 pc=1
00001EF0		00001EF0		1693+	DS	0FD	
00001EF0	00001F10			1694+	USING	*, R5	base for test data and test routine
00001EF4	0025			1695+T37	DC	A(X37)	address of test routine
00001EF6	00			1696+	DC	H' 37'	test number
00001EF7	9F			1697+	DC	X' 00'	
00001EF8	C2			1698+	DC	HL1' 159'	i3
00001EF9	01			1699+	DC	HL1' 194'	i4
00001EFA	00			1700+	DC	HL1' 1'	m5
00001EFB	07			1701+	DC	HL1' 0'	cc
00001EFC	00001F40			1702+	DC	HL1' 7'	cc failed mask
00001F00	E5D7E2D6 D7404040			1703+V2_37	DC	A(RE37+16)	address of v2: 16-byte packed decimal
00001F08	00000010			1704+	DC	CL8' VPSOP'	instruction name
00001F0C	00001F30			1705+	DC	A(16)	result length
				1706+REA37	DC	A(RE37)	result address
				1707+*			INSTRUCTION UNDER TEST ROUTINE
00001F10				1708+X37	DS	0F	
00001F10	5820 500C		00001EFC	1709+	L	R2, V2_37	get v2
00001F14	E722 0000 0006		00000000	1710+	VL	V2, 0(R2)	
00001F1A	E612 C219 F05B			1711+	VPSOP	V1, V2, 159, 194, 1	test instruction
00001F20	E710 8F10 000E		00001110	1712+	VST	V1, V10OUTPUT	save result
00001F26	B98D 0020			1713+	EPSW	R2, R0	exptract psw
00001F2A	5020 8EF0		000010F0	1714+	ST	R2, CCPSW	to save CC
00001F2E	07FB			1715+	BR	R11	return
00001F30				1716+RE37	DC	0F	
00001F30				1717+	DROP	R5	
00001F30	00000000 00000000			1718	DC	XL16' 0000000000000000000000000000000D'	V1
00001F38	00000000 0000000D						
00001F40	00000000 00000000			1719	DC	XL16' 0000000000000000000000000000000B'	V2
00001F48	00000000 0000000B						
				1720			
				1721 * V1: zero V2: invalid PC=' - ' NZ=' â€‘' V1_sign=V2 CC=0			
				1722	VRI_G	VPSOP, 159, 128, 1, 0	nz=0 pc=0
00001F50				1723+	DS	0FD	
00001F50		00001F50		1724+	USING	*, R5	base for test data and test routine
00001F50	00001F70			1725+T38	DC	A(X38)	address of test routine
00001F54	0026			1726+	DC	H' 38'	test number
00001F56	00			1727+	DC	X' 00'	
00001F57	9F			1728+	DC	HL1' 159'	i3
00001F58	80			1729+	DC	HL1' 128'	i4
00001F59	01			1730+	DC	HL1' 1'	m5
00001F5A	00			1731+	DC	HL1' 0'	cc
00001F5B	07			1732+	DC	HL1' 7'	cc failed mask
00001F5C	00001FA0			1733+V2_38	DC	A(RE38+16)	address of v2: 16-byte packed decimal
00001F60	E5D7E2D6 D7404040			1734+	DC	CL8' VPSOP'	instruction name

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00001F68	00000010			1735+	DC	A(16)	result length
00001F6C	00001F90			1736+REA38	DC	A(RE38)	result address
				1737+*			INSTRUCTION UNDER TEST ROUTINE
00001F70				1738+X38	DS	0F	
00001F70	5820 500C		00001F5C	1739+	L	R2, V2_38	get v2
00001F74	E722 0000 0006		00000000	1740+	VL	V2, 0(R2)	
00001F7A	E612 8019 F05B			1741+	VPSOP	V1, V2, 159, 128, 1	test instruction
00001F80	E710 8F10 000E		00001110	1742+	VST	V1, V10UTPUT	save result
00001F86	B98D 0020			1743+	EPSW	R2, R0	exptract psw
00001F8A	5020 8EF0		000010F0	1744+	ST	R2, CCPSW	to save CC
00001F8E	07FB			1745+	BR	R11	return
00001F90				1746+RE38	DC	0F	
00001F90				1747+	DROP	R5	
00001F90	00000000 00000000			1748	DC	XL16' 00000000000000000000000000000009'	V1
00001F98	00000000 00000009						
00001FA0	00000000 00000000			1749	DC	XL16' 00000000000000000000000000000009'	V2
00001FA8	00000000 00000009						
				1750			
00001FB0				1751	VRI_G	VPSOP, 159, 130, 1, 0	nz=0 pc=1
00001FB0		00001FB0		1752+	DS	0FD	
00001FB0	00001FD0			1753+	USING	*, R5	base for test data and test routine
00001FB4	0027			1754+T39	DC	A(X39)	address of test routine
00001FB6	00			1755+	DC	H' 39'	test number
00001FB6	00			1756+	DC	X' 00'	
00001FB7	9F			1757+	DC	HL1' 159'	i3
00001FB8	82			1758+	DC	HL1' 130'	i4
00001FB9	01			1759+	DC	HL1' 1'	m5
00001FBA	00			1760+	DC	HL1' 0'	cc
00001FBB	07			1761+	DC	HL1' 7'	cc failed mask
00001FBC	00002000			1762+V2_39	DC	A(RE39+16)	address of v2: 16-byte packed decimal
00001FC0	E5D7E2D6 D7404040			1763+	DC	CL8' VPSOP'	instruction name
00001FC8	00000010			1764+	DC	A(16)	result length
00001FCC	00001FF0			1765+REA39	DC	A(RE39)	result address
				1766+*			INSTRUCTION UNDER TEST ROUTINE
00001FD0				1767+X39	DS	0F	
00001FD0	5820 500C		00001FBC	1768+	L	R2, V2_39	get v2
00001FD4	E722 0000 0006		00000000	1769+	VL	V2, 0(R2)	
00001FDA	E612 8219 F05B			1770+	VPSOP	V1, V2, 159, 130, 1	test instruction
00001FE0	E710 8F10 000E		00001110	1771+	VST	V1, V10UTPUT	save result
00001FE6	B98D 0020			1772+	EPSW	R2, R0	exptract psw
00001FEA	5020 8EF0		000010F0	1773+	ST	R2, CCPSW	to save CC
00001FEE	07FB			1774+	BR	R11	return
00001FF0				1775+RE39	DC	0F	
00001FF0				1776+	DROP	R5	
00001FF0	00000000 00000000			1777	DC	XL16' 00000000000000000000000000000009'	V1
00001FF8	00000000 00000009						
00002000	00000000 00000000			1778	DC	XL16' 00000000000000000000000000000009'	V2
00002008	00000000 00000009						
				1779			
00002010				1780	VRI_G	VPSOP, 159, 192, 1, 0	nz=1 pc=0
00002010		00002010		1781+	DS	0FD	
00002010	00002030			1782+	USING	*, R5	base for test data and test routine
00002014	0028			1783+T40	DC	A(X40)	address of test routine
00002016	00			1784+	DC	H' 40'	test number
00002016	00			1785+	DC	X' 00'	
00002017	9F			1786+	DC	HL1' 159'	i3

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002018	C0			1787+	DC	HL1' 192'	i4
00002019	01			1788+	DC	HL1' 1'	m5
0000201A	00			1789+	DC	HL1' 0'	cc
0000201B	07			1790+	DC	HL1' 7'	cc failed mask
0000201C	00002060			1791+V2_40	DC	A(RE40+16)	address of v2: 16-byte packed decimal
00002020	E5D7E2D6 D7404040			1792+	DC	CL8' VPSOP'	instruction name
00002028	00000010			1793+	DC	A(16)	result length
0000202C	00002050			1794+REA40	DC	A(RE40)	result address
				1795+*			INSTRUCTION UNDER TEST ROUTINE
00002030				1796+X40	DS	0F	
00002030	5820 500C		0000201C	1797+	L	R2, V2_40	get v2
00002034	E722 0000 0006		00000000	1798+	VL	V2, 0(R2)	
0000203A	E612 C019 F05B			1799+	VPSOP	V1, V2, 159, 192, 1	test instruction
00002040	E710 8F10 000E		00001110	1800+	VST	V1, V10UTPUT	save result
00002046	B98D 0020			1801+	EPSW	R2, R0	exptract psw
0000204A	5020 8EF0		000010F0	1802+	ST	R2, CCPSW	to save CC
0000204E	07FB			1803+	BR	R11	return
00002050				1804+RE40	DC	0F	
00002050				1805+	DROP	R5	
00002050	00000000 00000000			1806	DC	XL16' 00000000000000000000000000000009'	V1
00002058	00000000 00000009						
00002060	00000000 00000000			1807	DC	XL16' 00000000000000000000000000000009'	V2
00002068	00000000 00000009						
				1808			
				1809	VRI_G	VPSOP, 159, 194, 1, 0	nz=1 pc=1
00002070				1810+	DS	0FD	
00002070		00002070		1811+	USING	*, R5	base for test data and test routine
00002070	00002090			1812+T41	DC	A(X41)	address of test routine
00002074	0029			1813+	DC	H' 41'	test number
00002076	00			1814+	DC	X' 00'	
00002077	9F			1815+	DC	HL1' 159'	i3
00002078	C2			1816+	DC	HL1' 194'	i4
00002079	01			1817+	DC	HL1' 1'	m5
0000207A	00			1818+	DC	HL1' 0'	cc
0000207B	07			1819+	DC	HL1' 7'	cc failed mask
0000207C	000020C0			1820+V2_41	DC	A(RE41+16)	address of v2: 16-byte packed decimal
00002080	E5D7E2D6 D7404040			1821+	DC	CL8' VPSOP'	instruction name
00002088	00000010			1822+	DC	A(16)	result length
0000208C	000020B0			1823+REA41	DC	A(RE41)	result address
				1824+*			INSTRUCTION UNDER TEST ROUTINE
00002090				1825+X41	DS	0F	
00002090	5820 500C		0000207C	1826+	L	R2, V2_41	get v2
00002094	E722 0000 0006		00000000	1827+	VL	V2, 0(R2)	
0000209A	E612 C219 F05B			1828+	VPSOP	V1, V2, 159, 194, 1	test instruction
000020A0	E710 8F10 000E		00001110	1829+	VST	V1, V10UTPUT	save result
000020A6	B98D 0020			1830+	EPSW	R2, R0	exptract psw
000020AA	5020 8EF0		000010F0	1831+	ST	R2, CCPSW	to save CC
000020AE	07FB			1832+	BR	R11	return
000020B0				1833+RE41	DC	0F	
000020B0				1834+	DROP	R5	
000020B0	00000000 00000000			1835	DC	XL16' 00000000000000000000000000000009'	V1
000020B8	00000000 00000009						
000020C0	00000000 00000000			1836	DC	XL16' 00000000000000000000000000000009'	V2
000020C8	00000000 00000009						
				1837			
				1838 *			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1839 * SC=01 (complement): nv=1 to avoid data exceptions
				1840 *-----
				1841
				1842 * V1: nonzero V2: positive PC='-' NZ='â€™' V1_sign=D CC=1
				1843 VRI_G VPSOP, 159, 132, 1, 1 nz=0 pc=0
000020D0				1844+ DS OFD
000020D0		000020D0		1845+ USING *, R5 base for test data and test routine
000020D0	000020F0			1846+T42 DC A(X42) address of test routine
000020D4	002A			1847+ DC H' 42' test number
000020D6	00			1848+ DC X' 00'
000020D7	9F			1849+ DC HL1' 159' i3
000020D8	84			1850+ DC HL1' 132' i4
000020D9	01			1851+ DC HL1' 1' m5
000020DA	01			1852+ DC HL1' 1' cc
000020DB	0B			1853+ DC HL1' 11' cc failed mask
000020DC	00002120			1854+V2_42 DC A(RE42+16) address of v2: 16-byte packed decimal
000020E0	E5D7E2D6 D7404040			1855+ DC CL8' VPSOP' instruction name
000020E8	00000010			1856+ DC A(16) result length
000020EC	00002110			1857+REA42 DC A(RE42) result address
				1858+* INSTRUCTION UNDER TEST ROUTINE
000020F0				1859+X42 DS OF
000020F0	5820 500C		000020DC	1860+ L R2, V2_42 get v2
000020F4	E722 0000 0006		00000000	1861+ VL V2, 0(R2)
000020FA	E612 8419 F05B			1862+ VPSOP V1, V2, 159, 132, 1 test instruction
00002100	E710 8F10 000E		00001110	1863+ VST V1, V10UTPUT save result
00002106	B98D 0020			1864+ EPSW R2, R0 exptrect psw
0000210A	5020 8EF0		000010F0	1865+ ST R2, CCPSW to save CC
0000210E	07FB			1866+ BR R11 return
00002110				1867+RE42 DC OF
00002110				1868+ DROP R5
00002110	00000000 00000000			1869 DC XL16' 000000000000000000000000220000000D' V1
00002118	00000022 0000000D			
00002120	00000000 00000000			1870 DC XL16' 000000000000000000000000220000000C' V2
00002128	00000022 0000000C			
				1871
				1872 VRI_G VPSOP, 159, 134, 1, 1 nz=0 pc=1
00002130				1873+ DS OFD
00002130		00002130		1874+ USING *, R5 base for test data and test routine
00002130	00002150			1875+T43 DC A(X43) address of test routine
00002134	002B			1876+ DC H' 43' test number
00002136	00			1877+ DC X' 00'
00002137	9F			1878+ DC HL1' 159' i3
00002138	86			1879+ DC HL1' 134' i4
00002139	01			1880+ DC HL1' 1' m5
0000213A	01			1881+ DC HL1' 1' cc
0000213B	0B			1882+ DC HL1' 11' cc failed mask
0000213C	00002180			1883+V2_43 DC A(RE43+16) address of v2: 16-byte packed decimal
00002140	E5D7E2D6 D7404040			1884+ DC CL8' VPSOP' instruction name
00002148	00000010			1885+ DC A(16) result length
0000214C	00002170			1886+REA43 DC A(RE43) result address
				1887+* INSTRUCTION UNDER TEST ROUTINE
00002150				1888+X43 DS OF
00002150	5820 500C		0000213C	1889+ L R2, V2_43 get v2
00002154	E722 0000 0006		00000000	1890+ VL V2, 0(R2)
0000215A	E612 8619 F05B			1891+ VPSOP V1, V2, 159, 134, 1 test instruction
00002160	E710 8F10 000E		00001110	1892+ VST V1, V10UTPUT save result

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002166	B98D 0020			1893+	EPSW	R2, R0	extract psw
0000216A	5020 8EF0		000010F0	1894+	ST	R2, CCPSW	to save CC
0000216E	07FB			1895+	BR	R11	return
00002170				1896+RE43	DC	0F	
00002170				1897+	DROP	R5	
00002170	00000000 00000000			1898	DC	XL16' 000000000000000000000000220000000D'	V1
00002178	00000022 0000000D						
00002180	00000000 00000000			1899	DC	XL16' 000000000000000000000000220000000A'	V2
00002188	00000022 0000000A						
				1900			
				1901	VRI_G	VPSOP, 159, 196, 1, 1	nz=1 pc=0
00002190				1902+	DS	0FD	
00002190		00002190		1903+	USING	*, R5	base for test data and test routine
00002190	000021B0			1904+T44	DC	A(X44)	address of test routine
00002194	002C			1905+	DC	H' 44'	test number
00002196	00			1906+	DC	X' 00'	
00002197	9F			1907+	DC	HL1' 159'	i3
00002198	C4			1908+	DC	HL1' 196'	i4
00002199	01			1909+	DC	HL1' 1'	m5
0000219A	01			1910+	DC	HL1' 1'	cc
0000219B	0B			1911+	DC	HL1' 11'	cc failed mask
0000219C	000021E0			1912+V2_44	DC	A(RE44+16)	address of v2: 16-byte packed decimal
000021A0	E5D7E2D6 D7404040			1913+	DC	CL8' VPSOP'	instruction name
000021A8	00000010			1914+	DC	A(16)	result length
000021AC	000021D0			1915+REA44	DC	A(RE44)	result address
				1916+*			INSTRUCTION UNDER TEST ROUTINE
000021B0				1917+X44	DS	0F	
000021B0	5820 500C		0000219C	1918+	L	R2, V2_44	get v2
000021B4	E722 0000 0006		00000000	1919+	VL	V2, 0(R2)	
000021BA	E612 C419 F05B			1920+	VPSOP	V1, V2, 159, 196, 1	test instruction
000021C0	E710 8F10 000E		00001110	1921+	VST	V1, V10UTPUT	save result
000021C6	B98D 0020			1922+	EPSW	R2, R0	extract psw
000021CA	5020 8EF0		000010F0	1923+	ST	R2, CCPSW	to save CC
000021CE	07FB			1924+	BR	R11	return
000021D0				1925+RE44	DC	0F	
000021D0				1926+	DROP	R5	
000021D0	00000000 00000000			1927	DC	XL16' 000000000000000000000000220000000D'	V1
000021D8	00000022 0000000D						
000021E0	00000000 00000000			1928	DC	XL16' 000000000000000000000000220000000F'	V2
000021E8	00000022 0000000F						
				1929			
				1930	VRI_G	VPSOP, 159, 198, 1, 1	nz=1 pc=1
000021F0				1931+	DS	0FD	
000021F0		000021F0		1932+	USING	*, R5	base for test data and test routine
000021F0	00002210			1933+T45	DC	A(X45)	address of test routine
000021F4	002D			1934+	DC	H' 45'	test number
000021F6	00			1935+	DC	X' 00'	
000021F7	9F			1936+	DC	HL1' 159'	i3
000021F8	C6			1937+	DC	HL1' 198'	i4
000021F9	01			1938+	DC	HL1' 1'	m5
000021FA	01			1939+	DC	HL1' 1'	cc
000021FB	0B			1940+	DC	HL1' 11'	cc failed mask
000021FC	00002240			1941+V2_45	DC	A(RE45+16)	address of v2: 16-byte packed decimal
00002200	E5D7E2D6 D7404040			1942+	DC	CL8' VPSOP'	instruction name
00002208	00000010			1943+	DC	A(16)	result length
0000220C	00002230			1944+REA45	DC	A(RE45)	result address

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				1945+*	INSTRUCTION UNDER TEST ROUTINE		
00002210				1946+X45	DS	0F	
00002210	5820 500C		000021FC	1947+	L	R2, V2_45	get v2
00002214	E722 0000 0006		00000000	1948+	VL	V2, 0(R2)	
0000221A	E612 C619 F05B			1949+	VPSOP	V1, V2, 159, 198, 1	test instruction
00002220	E710 8F10 000E		00001110	1950+	VST	V1, V10UTPUT	save result
00002226	B98D 0020			1951+	EPSW	R2, R0	exptract psw
0000222A	5020 8EF0		000010F0	1952+	ST	R2, CCPSW	to save CC
0000222E	07FB			1953+	BR	R11	return
00002230				1954+RE45	DC	0F	
00002230				1955+	DROP	R5	
00002230	00000000 00000000			1956	DC	XL16' 000000000000000000000000220000000D'	V1
00002238	00000022 0000000D						
00002240	00000000 00000000			1957	DC	XL16' 000000000000000000000000220000000E'	V2
00002248	00000022 0000000E						
				1958			
				1959 * V1: nonzero V2: negative PC=' 0' NZ=' â€‘' V1_sign=C CC=2			
				1960	VRI_G	VPSOP, 159, 132, 1, 2	nz=0 pc=0
00002250				1961+	DS	0FD	
00002250		00002250		1962+	USING	*, R5	base for test data and test routine
00002250	00002270			1963+T46	DC	A(X46)	address of test routine
00002254	002E			1964+	DC	H' 46'	test number
00002256	00			1965+	DC	X' 00'	
00002257	9F			1966+	DC	HL1' 159'	i3
00002258	84			1967+	DC	HL1' 132'	i4
00002259	01			1968+	DC	HL1' 1'	m5
0000225A	02			1969+	DC	HL1' 2'	cc
0000225B	0D			1970+	DC	HL1' 13'	cc failed mask
0000225C	000022A0			1971+V2_46	DC	A(RE46+16)	address of v2: 16-byte packed decimal
00002260	E5D7E2D6 D7404040			1972+	DC	CL8' VPSOP'	instruction name
00002268	00000010			1973+	DC	A(16)	result length
0000226C	00002290			1974+REA46	DC	A(RE46)	result address
				1975+*	INSTRUCTION UNDER TEST ROUTINE		
00002270				1976+X46	DS	0F	
00002270	5820 500C		0000225C	1977+	L	R2, V2_46	get v2
00002274	E722 0000 0006		00000000	1978+	VL	V2, 0(R2)	
0000227A	E612 8419 F05B			1979+	VPSOP	V1, V2, 159, 132, 1	test instruction
00002280	E710 8F10 000E		00001110	1980+	VST	V1, V10UTPUT	save result
00002286	B98D 0020			1981+	EPSW	R2, R0	exptract psw
0000228A	5020 8EF0		000010F0	1982+	ST	R2, CCPSW	to save CC
0000228E	07FB			1983+	BR	R11	return
00002290				1984+RE46	DC	0F	
00002290				1985+	DROP	R5	
00002290	00000000 00000000			1986	DC	XL16' 000000000000000000000000220000000C'	V1
00002298	00000022 0000000C						
000022A0	00000000 00000000			1987	DC	XL16' 000000000000000000000000220000000D'	V2
000022A8	00000022 0000000D						
				1988			
				1989	VRI_G	VPSOP, 159, 196, 1, 2	nz=1 pc=0
000022B0				1990+	DS	0FD	
000022B0		000022B0		1991+	USING	*, R5	base for test data and test routine
000022B0	000022D0			1992+T47	DC	A(X47)	address of test routine
000022B4	002F			1993+	DC	H' 47'	test number
000022B6	00			1994+	DC	X' 00'	
000022B7	9F			1995+	DC	HL1' 159'	i3
000022B8	C4			1996+	DC	HL1' 196'	i4

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000022B9	01			1997+	DC	HL1' 1'	m5
000022BA	02			1998+	DC	HL1' 2'	cc
000022BB	0D			1999+	DC	HL1' 13'	cc failed mask
000022BC	00002300			2000+V2_47	DC	A(RE47+16)	address of v2: 16-byte packed decimal
000022C0	E5D7E2D6 D7404040			2001+	DC	CL8' VPSOP'	instruction name
000022C8	00000010			2002+	DC	A(16)	result length
000022CC	000022F0			2003+REA47	DC	A(RE47)	result address
				2004+*			INSTRUCTION UNDER TEST ROUTINE
000022D0				2005+X47	DS	0F	
000022D0	5820 500C		000022BC	2006+	L	R2, V2_47	get v2
000022D4	E722 0000 0006		00000000	2007+	VL	V2, 0(R2)	
000022DA	E612 C419 F05B			2008+	VPSOP	V1, V2, 159, 196, 1	test instruction
000022E0	E710 8F10 000E		00001110	2009+	VST	V1, V10UTPUT	save result
000022E6	B98D 0020			2010+	EPSW	R2, R0	exptract psw
000022EA	5020 8EF0		000010F0	2011+	ST	R2, CCPSW	to save CC
000022EE	07FB			2012+	BR	R11	return
000022F0				2013+RE47	DC	0F	
000022F0				2014+	DROP	R5	
000022F0	00000000 00000000			2015	DC	XL16' 000000000000000000000000220000000C'	V1
000022F8	00000022 0000000C						
00002300	00000000 00000000			2016	DC	XL16' 000000000000000000000000220000000D'	V2
00002308	00000022 0000000D						
				2017			
				2018 *		V1: nonzero V2: negative PC=' 1' NZ=' â€‘' V1_sign=F CC=2	
				2019	VRI_G	VPSOP, 159, 134, 1, 2	nz=0 pc=1
00002310				2020+	DS	0FD	
00002310		00002310		2021+	USING	*, R5	base for test data and test routine
00002310	00002330			2022+T48	DC	A(X48)	address of test routine
00002314	0030			2023+	DC	H' 48'	test number
00002316	00			2024+	DC	X' 00'	
00002317	9F			2025+	DC	HL1' 159'	i3
00002318	86			2026+	DC	HL1' 134'	i4
00002319	01			2027+	DC	HL1' 1'	m5
0000231A	02			2028+	DC	HL1' 2'	cc
0000231B	0D			2029+	DC	HL1' 13'	cc failed mask
0000231C	00002360			2030+V2_48	DC	A(RE48+16)	address of v2: 16-byte packed decimal
00002320	E5D7E2D6 D7404040			2031+	DC	CL8' VPSOP'	instruction name
00002328	00000010			2032+	DC	A(16)	result length
0000232C	00002350			2033+REA48	DC	A(RE48)	result address
				2034+*			INSTRUCTION UNDER TEST ROUTINE
00002330				2035+X48	DS	0F	
00002330	5820 500C		0000231C	2036+	L	R2, V2_48	get v2
00002334	E722 0000 0006		00000000	2037+	VL	V2, 0(R2)	
0000233A	E612 8619 F05B			2038+	VPSOP	V1, V2, 159, 134, 1	test instruction
00002340	E710 8F10 000E		00001110	2039+	VST	V1, V10UTPUT	save result
00002346	B98D 0020			2040+	EPSW	R2, R0	exptract psw
0000234A	5020 8EF0		000010F0	2041+	ST	R2, CCPSW	to save CC
0000234E	07FB			2042+	BR	R11	return
00002350				2043+RE48	DC	0F	
00002350				2044+	DROP	R5	
00002350	00000000 00000000			2045	DC	XL16' 000000000000000000000000220000000F'	V1
00002358	00000022 0000000F						
00002360	00000000 00000000			2046	DC	XL16' 000000000000000000000000220000000D'	V2
00002368	00000022 0000000D						
				2047			
				2048	VRI_G	VPSOP, 159, 198, 1, 2	nz=1 pc=1

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002370				2049+	DS	0FD	
00002370		00002370		2050+	USING	*,R5	base for test data and test routine
00002370	00002390			2051+T49	DC	A(X49)	address of test routine
00002374	0031			2052+	DC	H' 49'	test number
00002376	00			2053+	DC	X' 00'	
00002377	9F			2054+	DC	HL1' 159'	i3
00002378	C6			2055+	DC	HL1' 198'	i4
00002379	01			2056+	DC	HL1' 1'	m5
0000237A	02			2057+	DC	HL1' 2'	cc
0000237B	0D			2058+	DC	HL1' 13'	cc failed mask
0000237C	000023C0			2059+V2_49	DC	A(RE49+16)	address of v2: 16-byte packed decimal
00002380	E5D7E2D6 D7404040			2060+	DC	CL8' VPSOP'	instruction name
00002388	00000010			2061+	DC	A(16)	result length
0000238C	000023B0			2062+REA49	DC	A(RE49)	result address
				2063+*			INSTRUCTION UNDER TEST ROUTINE
00002390				2064+X49	DS	0F	
00002390	5820 500C		0000237C	2065+	L	R2, V2_49	get v2
00002394	E722 0000 0006		00000000	2066+	VL	V2, 0(R2)	
0000239A	E612 C619 F05B			2067+	VPSOP	V1, V2, 159, 198, 1	test instruction
000023A0	E710 8F10 000E		00001110	2068+	VST	V1, V10UTPUT	save result
000023A6	B98D 0020			2069+	EPSW	R2, R0	exptract psw
000023AA	5020 8EF0		000010F0	2070+	ST	R2, CCPSW	to save CC
000023AE	07FB			2071+	BR	R11	return
000023B0				2072+RE49	DC	0F	
000023B0				2073+	DROP	R5	
000023B0	00000000 00000000			2074	DC	XL16' 000000000000000000000000220000000F'	V1
000023B8	00000022 0000000F						
000023C0	00000000 00000000			2075	DC	XL16' 000000000000000000000000220000000D'	V2
000023C8	00000022 0000000D						
				2076			
				2077 *	V1:----- V2:invalid PC='- ' NZ='â€‘'		V1_sign=- CC=-
				2078 *	???? test without exceptions?		
				2079			
				2080			
				2081 *	V1: zero V2: positive PC=' 0' NZ=' 0'		V1_sign=C CC=0
				2082	VRI_G	VPSOP, 159, 132, 1, 0	nz=0 pc=0
000023D0				2083+	DS	0FD	
000023D0		000023D0		2084+	USING	*,R5	base for test data and test routine
000023D0	000023F0			2085+T50	DC	A(X50)	address of test routine
000023D4	0032			2086+	DC	H' 50'	test number
000023D6	00			2087+	DC	X' 00'	
000023D7	9F			2088+	DC	HL1' 159'	i3
000023D8	84			2089+	DC	HL1' 132'	i4
000023D9	01			2090+	DC	HL1' 1'	m5
000023DA	00			2091+	DC	HL1' 0'	cc
000023DB	07			2092+	DC	HL1' 7'	cc failed mask
000023DC	00002420			2093+V2_50	DC	A(RE50+16)	address of v2: 16-byte packed decimal
000023E0	E5D7E2D6 D7404040			2094+	DC	CL8' VPSOP'	instruction name
000023E8	00000010			2095+	DC	A(16)	result length
000023EC	00002410			2096+REA50	DC	A(RE50)	result address
				2097+*			INSTRUCTION UNDER TEST ROUTINE
000023F0				2098+X50	DS	0F	
000023F0	5820 500C		000023DC	2099+	L	R2, V2_50	get v2
000023F4	E722 0000 0006		00000000	2100+	VL	V2, 0(R2)	
000023FA	E612 8419 F05B			2101+	VPSOP	V1, V2, 159, 132, 1	test instruction
00002400	E710 8F10 000E		00001110	2102+	VST	V1, V10UTPUT	save result

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00002406	B98D 0020			2103+	EPSW	R2, R0	extract psw	
0000240A	5020 8EF0		000010F0	2104+	ST	R2, CCPSW	to save CC	
0000240E	07FB			2105+	BR	R11	return	
00002410				2106+RE50	DC	0F		
00002410				2107+	DROP	R5		
00002410	00000000 00000000			2108	DC	XL16' 00000000000000000000000000000000C'	V1	
00002418	00000000 0000000C							
00002420	00000000 00000000			2109	DC	XL16' 00000000000000000000000000000000A'	V2	
00002428	00000000 0000000A							
				2110				
				2111 *	V1: zero V2: positive PC=' 1' NZ=' 0' V1_sign=F CC=0			
				2112	VRI_G	VPSOP, 159, 134, 1, 0	nz=0 pc=1	
00002430				2113+	DS	0FD		
00002430		00002430		2114+	USING	*, R5	base for test data and test routine	
00002430	00002450			2115+T51	DC	A(X51)	address of test routine	
00002434	0033			2116+	DC	H' 51'	test number	
00002436	00			2117+	DC	X' 00'		
00002437	9F			2118+	DC	HL1' 159'	i3	
00002438	86			2119+	DC	HL1' 134'	i4	
00002439	01			2120+	DC	HL1' 1'	m5	
0000243A	00			2121+	DC	HL1' 0'	cc	
0000243B	07			2122+	DC	HL1' 7'	cc failed mask	
0000243C	00002480			2123+V2_51	DC	A(RE51+16)	address of v2: 16-byte packed decimal	
00002440	E5D7E2D6 D7404040			2124+	DC	CL8' VPSOP'	instruction name	
00002448	00000010			2125+	DC	A(16)	result length	
0000244C	00002470			2126+REA51	DC	A(RE51)	result address	
				2127+*			INSTRUCTION UNDER TEST ROUTINE	
00002450				2128+X51	DS	0F		
00002450	5820 500C		0000243C	2129+	L	R2, V2_51	get v2	
00002454	E722 0000 0006		00000000	2130+	VL	V2, 0(R2)		
0000245A	E612 8619 F05B			2131+	VPSOP	V1, V2, 159, 134, 1	test instruction	
00002460	E710 8F10 000E		00001110	2132+	VST	V1, V10UTPUT	save result	
00002466	B98D 0020			2133+	EPSW	R2, R0	extract psw	
0000246A	5020 8EF0		000010F0	2134+	ST	R2, CCPSW	to save CC	
0000246E	07FB			2135+	BR	R11	return	
00002470				2136+RE51	DC	0F		
00002470				2137+	DROP	R5		
00002470	00000000 00000000			2138	DC	XL16' 00000000000000000000000000000000F'	V1	
00002478	00000000 0000000F							
00002480	00000000 00000000			2139	DC	XL16' 00000000000000000000000000000000A'	V2	
00002488	00000000 0000000A							
				2140				
				2141 *	V1: zero V2: positive PC=' - ' NZ=' 1' V1_sign=D CC=0			
				2142	VRI_G	VPSOP, 159, 196, 1, 0	nz=1 pc=0	
00002490				2143+	DS	0FD		
00002490		00002490		2144+	USING	*, R5	base for test data and test routine	
00002490	000024B0			2145+T52	DC	A(X52)	address of test routine	
00002494	0034			2146+	DC	H' 52'	test number	
00002496	00			2147+	DC	X' 00'		
00002497	9F			2148+	DC	HL1' 159'	i3	
00002498	C4			2149+	DC	HL1' 196'	i4	
00002499	01			2150+	DC	HL1' 1'	m5	
0000249A	00			2151+	DC	HL1' 0'	cc	
0000249B	07			2152+	DC	HL1' 7'	cc failed mask	
0000249C	000024E0			2153+V2_52	DC	A(RE52+16)	address of v2: 16-byte packed decimal	
000024A0	E5D7E2D6 D7404040			2154+	DC	CL8' VPSOP'	instruction name	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000024A8	00000010			2155+	DC	A(16)	result length
000024AC	000024D0			2156+REA52	DC	A(RE52)	result address
				2157+*			INSTRUCTION UNDER TEST ROUTINE
000024B0				2158+X52	DS	0F	
000024B0	5820 500C		0000249C	2159+	L	R2, V2_52	get v2
000024B4	E722 0000 0006		00000000	2160+	VL	V2, 0(R2)	
000024BA	E612 C419 F05B			2161+	VPSOP	V1, V2, 159, 196, 1	test instruction
000024C0	E710 8F10 000E		00001110	2162+	VST	V1, V10UTPUT	save result
000024C6	B98D 0020			2163+	EPSW	R2, R0	exptract psw
000024CA	5020 8EF0		000010F0	2164+	ST	R2, CCPSW	to save CC
000024CE	07FB			2165+	BR	R11	return
000024D0				2166+RE52	DC	0F	
000024D0				2167+	DROP	R5	
000024D0	00000000 00000000			2168	DC	XL16' 00000000000000000000000000000000D'	V1
000024D8	00000000 0000000D						
000024E0	00000000 00000000			2169	DC	XL16' 00000000000000000000000000000000A'	V2
000024E8	00000000 0000000A						
				2170			
000024F0				2171	VRI_G	VPSOP, 159, 198, 1, 0	nz=1 pc=1
000024F0		000024F0		2172+	DS	0FD	
000024F0	00002510			2173+	USING	*, R5	base for test data and test routine
000024F4	0035			2174+T53	DC	A(X53)	address of test routine
000024F6	00			2175+	DC	H' 53'	test number
000024F7	9F			2176+	DC	X' 00'	
000024F8	C6			2177+	DC	HL1' 159'	i3
000024F9	01			2178+	DC	HL1' 198'	i4
000024FA	00			2179+	DC	HL1' 1'	m5
000024FB	07			2180+	DC	HL1' 0'	cc
000024FC	00002540			2181+	DC	HL1' 7'	cc failed mask
00002500	E5D7E2D6 D7404040			2182+V2_53	DC	A(RE53+16)	address of v2: 16-byte packed decimal
00002508	00000010			2183+	DC	CL8' VPSOP'	instruction name
0000250C	00002530			2184+	DC	A(16)	result length
				2185+REA53	DC	A(RE53)	result address
				2186+*			INSTRUCTION UNDER TEST ROUTINE
00002510				2187+X53	DS	0F	
00002510	5820 500C		000024FC	2188+	L	R2, V2_53	get v2
00002514	E722 0000 0006		00000000	2189+	VL	V2, 0(R2)	
0000251A	E612 C619 F05B			2190+	VPSOP	V1, V2, 159, 198, 1	test instruction
00002520	E710 8F10 000E		00001110	2191+	VST	V1, V10UTPUT	save result
00002526	B98D 0020			2192+	EPSW	R2, R0	exptract psw
0000252A	5020 8EF0		000010F0	2193+	ST	R2, CCPSW	to save CC
0000252E	07FB			2194+	BR	R11	return
00002530				2195+RE53	DC	0F	
00002530				2196+	DROP	R5	
00002530	00000000 00000000			2197	DC	XL16' 00000000000000000000000000000000D'	V1
00002538	00000000 0000000D						
00002540	00000000 00000000			2198	DC	XL16' 00000000000000000000000000000000C'	V2
00002548	00000000 0000000C						
				2199			
				2200 * V1: zero V2: negative PC=' 0' NZ=' - ' V1_sign=C CC=0			
				2201	VRI_G	VPSOP, 159, 132, 1, 0	nz=0 pc=0
00002550		00002550		2202+	DS	0FD	
00002550				2203+	USING	*, R5	base for test data and test routine
00002550	00002570			2204+T54	DC	A(X54)	address of test routine
00002554	0036			2205+	DC	H' 54'	test number
00002556	00			2206+	DC	X' 00'	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002557	9F			2207+	DC	HL1' 159'	i3
00002558	84			2208+	DC	HL1' 132'	i4
00002559	01			2209+	DC	HL1' 1'	m5
0000255A	00			2210+	DC	HL1' 0'	cc
0000255B	07			2211+	DC	HL1' 7'	cc failed mask
0000255C	000025A0			2212+V2_54	DC	A(RE54+16)	address of v2: 16-byte packed decimal
00002560	E5D7E2D6 D7404040			2213+	DC	CL8' VPSOP'	instruction name
00002568	00000010			2214+	DC	A(16)	result length
0000256C	00002590			2215+REA54	DC	A(RE54)	result address
				2216+*			INSTRUCTION UNDER TEST ROUTINE
00002570				2217+X54	DS	0F	
00002570	5820 500C		0000255C	2218+	L	R2, V2_54	get v2
00002574	E722 0000 0006		00000000	2219+	VL	V2, 0(R2)	
0000257A	E612 8419 F05B			2220+	VPSOP	V1, V2, 159, 132, 1	test instruction
00002580	E710 8F10 000E		00001110	2221+	VST	V1, V10UTPUT	save result
00002586	B98D 0020			2222+	EPSW	R2, R0	exptract psw
0000258A	5020 8EF0		000010F0	2223+	ST	R2, CCPSW	to save CC
0000258E	07FB			2224+	BR	R11	return
00002590				2225+RE54	DC	0F	
00002590				2226+	DROP	R5	
00002590	00000000 00000000			2227	DC	XL16' 00000000000000000000000000000000C'	V1
00002598	00000000 0000000C						
000025A0	00000000 00000000			2228	DC	XL16' 00000000000000000000000000000000D'	V2
000025A8	00000000 0000000D						
				2229			
				2230	VRI_G	VPSOP, 159, 196, 1, 0	nz=1 pc=0
000025B0				2231+	DS	0FD	
000025B0		000025B0		2232+	USING	*, R5	base for test data and test routine
000025B0	000025D0			2233+T55	DC	A(X55)	address of test routine
000025B4	0037			2234+	DC	H' 55'	test number
000025B6	00			2235+	DC	X' 00'	
000025B7	9F			2236+	DC	HL1' 159'	i3
000025B8	C4			2237+	DC	HL1' 196'	i4
000025B9	01			2238+	DC	HL1' 1'	m5
000025BA	00			2239+	DC	HL1' 0'	cc
000025BB	07			2240+	DC	HL1' 7'	cc failed mask
000025BC	00002600			2241+V2_55	DC	A(RE55+16)	address of v2: 16-byte packed decimal
000025C0	E5D7E2D6 D7404040			2242+	DC	CL8' VPSOP'	instruction name
000025C8	00000010			2243+	DC	A(16)	result length
000025CC	000025F0			2244+REA55	DC	A(RE55)	result address
				2245+*			INSTRUCTION UNDER TEST ROUTINE
000025D0				2246+X55	DS	0F	
000025D0	5820 500C		000025BC	2247+	L	R2, V2_55	get v2
000025D4	E722 0000 0006		00000000	2248+	VL	V2, 0(R2)	
000025DA	E612 C419 F05B			2249+	VPSOP	V1, V2, 159, 196, 1	test instruction
000025E0	E710 8F10 000E		00001110	2250+	VST	V1, V10UTPUT	save result
000025E6	B98D 0020			2251+	EPSW	R2, R0	exptract psw
000025EA	5020 8EF0		000010F0	2252+	ST	R2, CCPSW	to save CC
000025EE	07FB			2253+	BR	R11	return
000025F0				2254+RE55	DC	0F	
000025F0				2255+	DROP	R5	
000025F0	00000000 00000000			2256	DC	XL16' 00000000000000000000000000000000C'	V1
000025F8	00000000 0000000C						
00002600	00000000 00000000			2257	DC	XL16' 00000000000000000000000000000000B'	V2
00002608	00000000 0000000B						
				2258			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				2259 * V1: zero V2: negative PC=' 1' NZ=' - ' V1_sign=F CC=0	
				2260 VRI_G VPSOP, 159, 134, 1, 0 nz=0 pc=1	
00002610				2261+ DS OFD	
00002610		00002610		2262+ USING *, R5	base for test data and test routine
00002610	00002630			2263+T56 DC A(X56)	address of test routine
00002614	0038			2264+ DC H' 56'	test number
00002616	00			2265+ DC X' 00'	
00002617	9F			2266+ DC HL1' 159'	i3
00002618	86			2267+ DC HL1' 134'	i4
00002619	01			2268+ DC HL1' 1'	m5
0000261A	00			2269+ DC HL1' 0'	cc
0000261B	07			2270+ DC HL1' 7'	cc failed mask
0000261C	00002660			2271+V2_56 DC A(RE56+16)	address of v2: 16-byte packed decimal
00002620	E5D7E2D6 D7404040			2272+ DC CL8' VPSOP'	instruction name
00002628	00000010			2273+ DC A(16)	result length
0000262C	00002650			2274+REA56 DC A(RE56)	result address
				2275+*	INSTRUCTION UNDER TEST ROUTINE
00002630				2276+X56 DS OF	
00002630	5820 500C		0000261C	2277+ L R2, V2_56	get v2
00002634	E722 0000 0006		00000000	2278+ VL V2, 0(R2)	
0000263A	E612 8619 F05B			2279+ VPSOP V1, V2, 159, 134, 1	test instruction
00002640	E710 8F10 000E		00001110	2280+ VST V1, V10UTPUT	save result
00002646	B98D 0020			2281+ EPSW R2, R0	exptract psw
0000264A	5020 8EF0		000010F0	2282+ ST R2, CCPSW	to save CC
0000264E	07FB			2283+ BR R11	return
00002650				2284+RE56 DC OF	
00002650				2285+ DROP R5	
00002650	00000000 00000000			2286 DC XL16' 00000000000000000000000000000000F'	V1
00002658	00000000 0000000F				
00002660	00000000 00000000			2287 DC XL16' 00000000000000000000000000000000D'	V2
00002668	00000000 0000000D				
				2288	
				2289 VRI_G VPSOP, 159, 198, 1, 0 nz=1 pc=1	
00002670				2290+ DS OFD	
00002670		00002670		2291+ USING *, R5	base for test data and test routine
00002670	00002690			2292+T57 DC A(X57)	address of test routine
00002674	0039			2293+ DC H' 57'	test number
00002676	00			2294+ DC X' 00'	
00002677	9F			2295+ DC HL1' 159'	i3
00002678	C6			2296+ DC HL1' 198'	i4
00002679	01			2297+ DC HL1' 1'	m5
0000267A	00			2298+ DC HL1' 0'	cc
0000267B	07			2299+ DC HL1' 7'	cc failed mask
0000267C	000026C0			2300+V2_57 DC A(RE57+16)	address of v2: 16-byte packed decimal
00002680	E5D7E2D6 D7404040			2301+ DC CL8' VPSOP'	instruction name
00002688	00000010			2302+ DC A(16)	result length
0000268C	000026B0			2303+REA57 DC A(RE57)	result address
				2304+*	INSTRUCTION UNDER TEST ROUTINE
00002690				2305+X57 DS OF	
00002690	5820 500C		0000267C	2306+ L R2, V2_57	get v2
00002694	E722 0000 0006		00000000	2307+ VL V2, 0(R2)	
0000269A	E612 C619 F05B			2308+ VPSOP V1, V2, 159, 198, 1	test instruction
000026A0	E710 8F10 000E		00001110	2309+ VST V1, V10UTPUT	save result
000026A6	B98D 0020			2310+ EPSW R2, R0	exptract psw
000026AA	5020 8EF0		000010F0	2311+ ST R2, CCPSW	to save CC
000026AE	07FB			2312+ BR R11	return

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000026B0				2313+RE57	DC	0F	
000026B0				2314+	DROP	R5	
000026B0	00000000 00000000			2315	DC	XL16' 00000000000000000000000000000000F'	V1
000026B8	00000000 0000000F						
000026C0	00000000 00000000			2316	DC	XL16' 0000000000000000000000000000000B'	V2
000026C8	00000000 0000000B						
				2317			
				2318 *			
				2319 * SC=10 (force positive): nv=1 to avoid data exceptions			
				2320 *			
				2321			
				2322 * V1: nonzero V2: ----- PC=' 0' NZ=' â€‘' V1_sign=C CC=2			
				2323 * V2: positive			
				2324	VRI_G	VPSOP, 159, 136, 1, 2	nz=0 pc=0
000026D0				2325+	DS	0FD	
000026D0		000026D0		2326+	USING	*, R5	base for test data and test routine
000026D0	000026F0			2327+T58	DC	A(X58)	address of test routine
000026D4	003A			2328+	DC	H' 58'	test number
000026D6	00			2329+	DC	X' 00'	
000026D7	9F			2330+	DC	HL1' 159'	i3
000026D8	88			2331+	DC	HL1' 136'	i4
000026D9	01			2332+	DC	HL1' 1'	m5
000026DA	02			2333+	DC	HL1' 2'	cc
000026DB	0D			2334+	DC	HL1' 13'	cc failed mask
000026DC	00002720			2335+V2_58	DC	A(RE58+16)	address of v2: 16-byte packed decimal
000026E0	E5D7E2D6 D7404040			2336+	DC	CL8' VPSOP'	instruction name
000026E8	00000010			2337+	DC	A(16)	result length
000026EC	00002710			2338+REA58	DC	A(RE58)	result address
				2339+*			INSTRUCTION UNDER TEST ROUTINE
000026F0				2340+X58	DS	0F	
000026F0	5820 500C		000026DC	2341+	L	R2, V2_58	get v2
000026F4	E722 0000 0006		00000000	2342+	VL	V2, 0(R2)	
000026FA	E612 8819 F05B			2343+	VPSOP	V1, V2, 159, 136, 1	test instruction
00002700	E710 8F10 000E		00001110	2344+	VST	V1, V10UTPUT	save result
00002706	B98D 0020			2345+	EPSW	R2, R0	exptract psw
0000270A	5020 8EF0		000010F0	2346+	ST	R2, CCPSW	to save CC
0000270E	07FB			2347+	BR	R11	return
00002710				2348+RE58	DC	0F	
00002710				2349+	DROP	R5	
00002710	00000000 00000000			2350	DC	XL16' 000000000000000000000000220000000C'	V1
00002718	00000022 0000000C						
00002720	00000000 00000000			2351	DC	XL16' 000000000000000000000000220000000A'	V2
00002728	00000022 0000000A						
				2352			
				2353	VRI_G	VPSOP, 159, 200, 1, 2	nz=1 pc=0
00002730				2354+	DS	0FD	
00002730		00002730		2355+	USING	*, R5	base for test data and test routine
00002730	00002750			2356+T59	DC	A(X59)	address of test routine
00002734	003B			2357+	DC	H' 59'	test number
00002736	00			2358+	DC	X' 00'	
00002737	9F			2359+	DC	HL1' 159'	i3
00002738	C8			2360+	DC	HL1' 200'	i4
00002739	01			2361+	DC	HL1' 1'	m5
0000273A	02			2362+	DC	HL1' 2'	cc
0000273B	0D			2363+	DC	HL1' 13'	cc failed mask
0000273C	00002780			2364+V2_59	DC	A(RE59+16)	address of v2: 16-byte packed decimal

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002740	E5D7E2D6 D7404040			2365+	DC	CL8' VPSOP'	instruction name
00002748	00000010			2366+	DC	A(16)	result length
0000274C	00002770			2367+REA59	DC	A(RE59)	result address
				2368+*			INSTRUCTION UNDER TEST ROUTINE
00002750				2369+X59	DS	0F	
00002750	5820 500C		0000273C	2370+	L	R2, V2_59	get v2
00002754	E722 0000 0006		00000000	2371+	VL	V2, 0(R2)	
0000275A	E612 C819 F05B			2372+	VPSOP	V1, V2, 159, 200, 1	test instruction
00002760	E710 8F10 000E		00001110	2373+	VST	V1, V10UTPUT	save result
00002766	B98D 0020			2374+	EPSW	R2, R0	exptract psw
0000276A	5020 8EF0		000010F0	2375+	ST	R2, CCPSW	to save CC
0000276E	07FB			2376+	BR	R11	return
00002770				2377+RE59	DC	0F	
00002770				2378+	DROP	R5	
00002770	00000000 00000000			2379	DC	XL16' 000000000000000000000000220000000C'	V1
00002778	00000022 0000000C						
00002780	00000000 00000000			2380	DC	XL16' 000000000000000000000000220000000A'	V2
00002788	00000022 0000000A						
				2381 * V2: negative			
				2382	VRI_G	VPSOP, 159, 136, 1, 2	nz=0 pc=0
00002790				2383+	DS	0FD	
00002790		00002790		2384+	USING	*, R5	base for test data and test routine
00002790	000027B0			2385+T60	DC	A(X60)	address of test routine
00002794	003C			2386+	DC	H' 60'	test number
00002796	00			2387+	DC	X' 00'	
00002797	9F			2388+	DC	HL1' 159'	i3
00002798	88			2389+	DC	HL1' 136'	i4
00002799	01			2390+	DC	HL1' 1'	m5
0000279A	02			2391+	DC	HL1' 2'	cc
0000279B	0D			2392+	DC	HL1' 13'	cc failed mask
0000279C	000027E0			2393+V2_60	DC	A(RE60+16)	address of v2: 16-byte packed decimal
000027A0	E5D7E2D6 D7404040			2394+	DC	CL8' VPSOP'	instruction name
000027A8	00000010			2395+	DC	A(16)	result length
000027AC	000027D0			2396+REA60	DC	A(RE60)	result address
				2397+*			INSTRUCTION UNDER TEST ROUTINE
000027B0				2398+X60	DS	0F	
000027B0	5820 500C		0000279C	2399+	L	R2, V2_60	get v2
000027B4	E722 0000 0006		00000000	2400+	VL	V2, 0(R2)	
000027BA	E612 8819 F05B			2401+	VPSOP	V1, V2, 159, 136, 1	test instruction
000027C0	E710 8F10 000E		00001110	2402+	VST	V1, V10UTPUT	save result
000027C6	B98D 0020			2403+	EPSW	R2, R0	exptract psw
000027CA	5020 8EF0		000010F0	2404+	ST	R2, CCPSW	to save CC
000027CE	07FB			2405+	BR	R11	return
000027D0				2406+RE60	DC	0F	
000027D0				2407+	DROP	R5	
000027D0	00000000 00000000			2408	DC	XL16' 000000000000000000000000220000000C'	V1
000027D8	00000022 0000000C						
000027E0	00000000 00000000			2409	DC	XL16' 000000000000000000000000220000000D'	V2
000027E8	00000022 0000000D						
				2410			
				2411	VRI_G	VPSOP, 159, 200, 1, 2	nz=1 pc=0
000027F0				2412+	DS	0FD	
000027F0		000027F0		2413+	USING	*, R5	base for test data and test routine
000027F0	00002810			2414+T61	DC	A(X61)	address of test routine
000027F4	003D			2415+	DC	H' 61'	test number
000027F6	00			2416+	DC	X' 00'	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000028A8	00000022 0000000A			2470		
				2471	VRI_G VPSOP, 159, 202, 1, 2	nz=1 pc=1
000028B0				2472+	DS OFD	
000028B0		000028B0		2473+	USING *, R5	base for test data and test routine
000028B0	000028D0			2474+T63	DC A(X63)	address of test routine
000028B4	003F			2475+	DC H' 63'	test number
000028B6	00			2476+	DC X' 00'	
000028B7	9F			2477+	DC HL1' 159'	i3
000028B8	CA			2478+	DC HL1' 202'	i4
000028B9	01			2479+	DC HL1' 1'	m5
000028BA	02			2480+	DC HL1' 2'	cc
000028BB	0D			2481+	DC HL1' 13'	cc failed mask
000028BC	00002900			2482+V2_63	DC A(RE63+16)	address of v2: 16-byte packed decimal
000028C0	E5D7E2D6 D7404040			2483+	DC CL8' VPSOP'	instruction name
000028C8	00000010			2484+	DC A(16)	result length
000028CC	000028F0			2485+REA63	DC A(RE63)	result address
				2486+*		INSTRUCTION UNDER TEST ROUTINE
000028D0				2487+X63	DS OF	
000028D0	5820 500C		000028BC	2488+	L R2, V2_63	get v2
000028D4	E722 0000 0006		00000000	2489+	VL V2, 0(R2)	
000028DA	E612 CA19 F05B			2490+	VPSOP V1, V2, 159, 202, 1	test instruction
000028E0	E710 8F10 000E		00001110	2491+	VST V1, V10UTPUT	save result
000028E6	B98D 0020			2492+	EPSW R2, R0	exptract psw
000028EA	5020 8EF0		000010F0	2493+	ST R2, CCPSW	to save CC
000028EE	07FB			2494+	BR R11	return
000028F0				2495+RE63	DC OF	
000028F0				2496+	DROP R5	
000028F0	00000000 00000000			2497	DC XL16' 000000000000000000000000220000000F'	V1
000028F8	00000022 0000000F					
00002900	00000000 00000000			2498	DC XL16' 000000000000000000000000220000000A'	V2
00002908	00000022 0000000A					
				2499 * V2: negative		
				2500	VRI_G VPSOP, 159, 138, 1, 2	nz=0 pc=1
00002910				2501+	DS OFD	
00002910		00002910		2502+	USING *, R5	base for test data and test routine
00002910	00002930			2503+T64	DC A(X64)	address of test routine
00002914	0040			2504+	DC H' 64'	test number
00002916	00			2505+	DC X' 00'	
00002917	9F			2506+	DC HL1' 159'	i3
00002918	8A			2507+	DC HL1' 138'	i4
00002919	01			2508+	DC HL1' 1'	m5
0000291A	02			2509+	DC HL1' 2'	cc
0000291B	0D			2510+	DC HL1' 13'	cc failed mask
0000291C	00002960			2511+V2_64	DC A(RE64+16)	address of v2: 16-byte packed decimal
00002920	E5D7E2D6 D7404040			2512+	DC CL8' VPSOP'	instruction name
00002928	00000010			2513+	DC A(16)	result length
0000292C	00002950			2514+REA64	DC A(RE64)	result address
				2515+*		INSTRUCTION UNDER TEST ROUTINE
00002930				2516+X64	DS OF	
00002930	5820 500C		0000291C	2517+	L R2, V2_64	get v2
00002934	E722 0000 0006		00000000	2518+	VL V2, 0(R2)	
0000293A	E612 8A19 F05B			2519+	VPSOP V1, V2, 159, 138, 1	test instruction
00002940	E710 8F10 000E		00001110	2520+	VST V1, V10UTPUT	save result
00002946	B98D 0020			2521+	EPSW R2, R0	exptract psw
0000294A	5020 8EF0		000010F0	2522+	ST R2, CCPSW	to save CC

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
0000294E	07FB			2523+	BR	R11	return	
00002950				2524+RE64	DC	0F		
00002950				2525+	DROP	R5		
00002950	00000000 00000000			2526	DC	XL16'	000000000000000000000000220000000F'	V1
00002958	00000022 0000000F							
00002960	00000000 00000000			2527	DC	XL16'	000000000000000000000000220000000D'	V2
00002968	00000022 0000000D							
				2528				
				2529	VRI_G	VPSOP, 159, 202, 1, 2	nz=1 pc=1	
00002970				2530+	DS	0FD		
00002970		00002970		2531+	USING	*, R5	base for test data and test routine	
00002970	00002990			2532+T65	DC	A(X65)	address of test routine	
00002974	0041			2533+	DC	H' 65'	test number	
00002976	00			2534+	DC	X' 00'		
00002977	9F			2535+	DC	HL1' 159'	i3	
00002978	CA			2536+	DC	HL1' 202'	i4	
00002979	01			2537+	DC	HL1' 1'	m5	
0000297A	02			2538+	DC	HL1' 2'	cc	
0000297B	0D			2539+	DC	HL1' 13'	cc failed mask	
0000297C	000029C0			2540+V2_65	DC	A(RE65+16)	address of v2: 16-byte packed decimal	
00002980	E5D7E2D6 D7404040			2541+	DC	CL8' VPSOP'	instruction name	
00002988	00000010			2542+	DC	A(16)	result length	
0000298C	000029B0			2543+REA65	DC	A(RE65)	result address	
				2544+*			INSTRUCTION UNDER TEST ROUTINE	
00002990				2545+X65	DS	0F		
00002990	5820 500C		0000297C	2546+	L	R2, V2_65	get v2	
00002994	E722 0000 0006		00000000	2547+	VL	V2, 0(R2)		
0000299A	E612 CA19 F05B			2548+	VPSOP	V1, V2, 159, 202, 1	test instruction	
000029A0	E710 8F10 000E		00001110	2549+	VST	V1, V10UTPUT	save result	
000029A6	B98D 0020			2550+	EPSW	R2, R0	exptract psw	
000029AA	5020 8EF0		000010F0	2551+	ST	R2, CCPSW	to save CC	
000029AE	07FB			2552+	BR	R11	return	
000029B0				2553+RE65	DC	0F		
000029B0				2554+	DROP	R5		
000029B0	00000000 00000000			2555	DC	XL16'	000000000000000000000000220000000F'	V1
000029B8	00000022 0000000F							
000029C0	00000000 00000000			2556	DC	XL16'	000000000000000000000000220000000B'	V2
000029C8	00000022 0000000B							
				2557				
				2558 *	V1: zero	V2: - - - - -	PC=' 0' NZ=' â€‘'	V1_sign=C CC=0
				2559 *	V2: positive			
				2560	VRI_G	VPSOP, 159, 136, 1, 0	nz=0 pc=0	
000029D0				2561+	DS	0FD		
000029D0		000029D0		2562+	USING	*, R5	base for test data and test routine	
000029D0	000029F0			2563+T66	DC	A(X66)	address of test routine	
000029D4	0042			2564+	DC	H' 66'	test number	
000029D6	00			2565+	DC	X' 00'		
000029D7	9F			2566+	DC	HL1' 159'	i3	
000029D8	88			2567+	DC	HL1' 136'	i4	
000029D9	01			2568+	DC	HL1' 1'	m5	
000029DA	00			2569+	DC	HL1' 0'	cc	
000029DB	07			2570+	DC	HL1' 7'	cc failed mask	
000029DC	00002A20			2571+V2_66	DC	A(RE66+16)	address of v2: 16-byte packed decimal	
000029E0	E5D7E2D6 D7404040			2572+	DC	CL8' VPSOP'	instruction name	
000029E8	00000010			2573+	DC	A(16)	result length	
000029EC	00002A10			2574+REA66	DC	A(RE66)	result address	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				2575+*		INSTRUCTION UNDER TEST ROUTINE	
000029F0				2576+X66	DS	0F	
000029F0	5820 500C		000029DC	2577+	L	R2, V2_66	get v2
000029F4	E722 0000 0006		00000000	2578+	VL	V2, 0(R2)	
000029FA	E612 8819 F05B			2579+	VPSOP	V1, V2, 159, 136, 1	test instruction
00002A00	E710 8F10 000E		00001110	2580+	VST	V1, V10UTPUT	save result
00002A06	B98D 0020			2581+	EPSW	R2, R0	exptract psw
00002A0A	5020 8EF0		000010F0	2582+	ST	R2, CCPSW	to save CC
00002A0E	07FB			2583+	BR	R11	return
00002A10				2584+RE66	DC	0F	
00002A10				2585+	DROP	R5	
00002A10	00000000 00000000			2586	DC	XL16' 00000000000000000000000000000000C'	V1
00002A18	00000000 0000000C						
00002A20	00000000 00000000			2587	DC	XL16' 00000000000000000000000000000000A'	V2
00002A28	00000000 0000000A						
				2588			
00002A30				2589	VRI_G	VPSOP, 159, 200, 1, 0	nz=1 pc=0
00002A30		00002A30		2590+	DS	0FD	
00002A30	00002A50			2591+	USING	*, R5	base for test data and test routine
00002A34	0043			2592+T67	DC	A(X67)	address of test routine
00002A36	00			2593+	DC	H' 67'	test number
00002A37	9F			2594+	DC	X' 00'	
00002A38	C8			2595+	DC	HL1' 159'	i3
00002A39	01			2596+	DC	HL1' 200'	i4
00002A3A	00			2597+	DC	HL1' 1'	m5
00002A3B	07			2598+	DC	HL1' 0'	cc
00002A3B	07			2599+	DC	HL1' 7'	cc failed mask
00002A3C	00002A80			2600+V2_67	DC	A(RE67+16)	address of v2: 16-byte packed decimal
00002A40	E5D7E2D6 D7404040			2601+	DC	CL8' VPSOP'	instruction name
00002A48	00000010			2602+	DC	A(16)	result length
00002A4C	00002A70			2603+REA67	DC	A(RE67)	result address
				2604+*			INSTRUCTION UNDER TEST ROUTINE
00002A50				2605+X67	DS	0F	
00002A50	5820 500C		00002A3C	2606+	L	R2, V2_67	get v2
00002A54	E722 0000 0006		00000000	2607+	VL	V2, 0(R2)	
00002A5A	E612 C819 F05B			2608+	VPSOP	V1, V2, 159, 200, 1	test instruction
00002A60	E710 8F10 000E		00001110	2609+	VST	V1, V10UTPUT	save result
00002A66	B98D 0020			2610+	EPSW	R2, R0	exptract psw
00002A6A	5020 8EF0		000010F0	2611+	ST	R2, CCPSW	to save CC
00002A6E	07FB			2612+	BR	R11	return
00002A70				2613+RE67	DC	0F	
00002A70				2614+	DROP	R5	
00002A70	00000000 00000000			2615	DC	XL16' 00000000000000000000000000000000C'	V1
00002A78	00000000 0000000C						
00002A80	00000000 00000000			2616	DC	XL16' 00000000000000000000000000000000A'	V2
00002A88	00000000 0000000A						
				2617 * V2: negative			
00002A90				2618	VRI_G	VPSOP, 159, 136, 1, 0	nz=0 pc=0
00002A90		00002A90		2619+	DS	0FD	
00002A90	00002AB0			2620+	USING	*, R5	base for test data and test routine
00002A94	0044			2621+T68	DC	A(X68)	address of test routine
00002A96	00			2622+	DC	H' 68'	test number
00002A97	9F			2623+	DC	X' 00'	
00002A97	9F			2624+	DC	HL1' 159'	i3
00002A98	88			2625+	DC	HL1' 136'	i4
00002A99	01			2626+	DC	HL1' 1'	m5

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002A9A	00			2627+	DC	HL1' 0'	cc
00002A9B	07			2628+	DC	HL1' 7'	cc failed mask
00002A9C	00002AE0			2629+V2_68	DC	A(RE68+16)	address of v2: 16-byte packed decimal
00002AA0	E5D7E2D6 D7404040			2630+	DC	CL8' VPSOP'	instruction name
00002AA8	00000010			2631+	DC	A(16)	result length
00002AAC	00002AD0			2632+REA68	DC	A(RE68)	result address
				2633+*			INSTRUCTION UNDER TEST ROUTINE
00002AB0				2634+X68	DS	0F	
00002AB0	5820 500C		00002A9C	2635+	L	R2, V2_68	get v2
00002AB4	E722 0000 0006		00000000	2636+	VL	V2, 0(R2)	
00002ABA	E612 8819 F05B			2637+	VPSOP	V1, V2, 159, 136, 1	test instruction
00002AC0	E710 8F10 000E		00001110	2638+	VST	V1, V10UTPUT	save result
00002AC6	B98D 0020			2639+	EPSW	R2, R0	exptract psw
00002ACA	5020 8EF0		000010F0	2640+	ST	R2, CCPSW	to save CC
00002ACE	07FB			2641+	BR	R11	return
00002AD0				2642+RE68	DC	0F	
00002AD0				2643+	DROP	R5	
00002AD0	00000000 00000000			2644	DC	XL16' 00000000000000000000000000000000C'	V1
00002AD8	00000000 0000000C						
00002AE0	00000000 00000000			2645	DC	XL16' 00000000000000000000000000000000D'	V2
00002AE8	00000000 0000000D						
				2646			
				2647	VRI_G	VPSOP, 159, 200, 1, 0	nz=1 pc=0
00002AF0				2648+	DS	0FD	
00002AF0		00002AF0		2649+	USING	*, R5	base for test data and test routine
00002AF0	00002B10			2650+T69	DC	A(X69)	address of test routine
00002AF4	0045			2651+	DC	H' 69'	test number
00002AF6	00			2652+	DC	X' 00'	
00002AF7	9F			2653+	DC	HL1' 159'	i3
00002AF8	C8			2654+	DC	HL1' 200'	i4
00002AF9	01			2655+	DC	HL1' 1'	m5
00002AFA	00			2656+	DC	HL1' 0'	cc
00002AFB	07			2657+	DC	HL1' 7'	cc failed mask
00002AFC	00002B40			2658+V2_69	DC	A(RE69+16)	address of v2: 16-byte packed decimal
00002B00	E5D7E2D6 D7404040			2659+	DC	CL8' VPSOP'	instruction name
00002B08	00000010			2660+	DC	A(16)	result length
00002B0C	00002B30			2661+REA69	DC	A(RE69)	result address
				2662+*			INSTRUCTION UNDER TEST ROUTINE
00002B10				2663+X69	DS	0F	
00002B10	5820 500C		00002AFC	2664+	L	R2, V2_69	get v2
00002B14	E722 0000 0006		00000000	2665+	VL	V2, 0(R2)	
00002B1A	E612 C819 F05B			2666+	VPSOP	V1, V2, 159, 200, 1	test instruction
00002B20	E710 8F10 000E		00001110	2667+	VST	V1, V10UTPUT	save result
00002B26	B98D 0020			2668+	EPSW	R2, R0	exptract psw
00002B2A	5020 8EF0		000010F0	2669+	ST	R2, CCPSW	to save CC
00002B2E	07FB			2670+	BR	R11	return
00002B30				2671+RE69	DC	0F	
00002B30				2672+	DROP	R5	
00002B30	00000000 00000000			2673	DC	XL16' 00000000000000000000000000000000C'	V1
00002B38	00000000 0000000C						
00002B40	00000000 00000000			2674	DC	XL16' 00000000000000000000000000000000B'	V2
00002B48	00000000 0000000B						
				2675			
				2676	*	V1: zero V2: - - - - - PC=' 1' NZ=' â€‘'	V1_sign=F CC=0
				2677	*	V2: positive	
				2678	VRI_G	VPSOP, 159, 138, 1, 0	nz=0 pc=1

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002B50				2679+	DS	0FD	
00002B50		00002B50		2680+	USING	*, R5	base for test data and test routine
00002B50	00002B70			2681+T70	DC	A(X70)	address of test routine
00002B54	0046			2682+	DC	H' 70'	test number
00002B56	00			2683+	DC	X' 00'	
00002B57	9F			2684+	DC	HL1' 159'	i3
00002B58	8A			2685+	DC	HL1' 138'	i4
00002B59	01			2686+	DC	HL1' 1'	m5
00002B5A	00			2687+	DC	HL1' 0'	cc
00002B5B	07			2688+	DC	HL1' 7'	cc failed mask
00002B5C	00002BA0			2689+V2_70	DC	A(RE70+16)	address of v2: 16-byte packed decimal
00002B60	E5D7E2D6 D7404040			2690+	DC	CL8' VPSOP'	instruction name
00002B68	00000010			2691+	DC	A(16)	result length
00002B6C	00002B90			2692+REA70	DC	A(RE70)	result address
				2693+*			INSTRUCTION UNDER TEST ROUTINE
00002B70				2694+X70	DS	0F	
00002B70	5820 500C		00002B5C	2695+	L	R2, V2_70	get v2
00002B74	E722 0000 0006		00000000	2696+	VL	V2, 0(R2)	
00002B7A	E612 8A19 F05B			2697+	VPSOP	V1, V2, 159, 138, 1	test instruction
00002B80	E710 8F10 000E		00001110	2698+	VST	V1, V10UTPUT	save result
00002B86	B98D 0020			2699+	EPSW	R2, R0	exptract psw
00002B8A	5020 8EF0		000010F0	2700+	ST	R2, CCPSW	to save CC
00002B8E	07FB			2701+	BR	R11	return
00002B90				2702+RE70	DC	0F	
00002B90				2703+	DROP	R5	
00002B90	00000000 00000000			2704	DC	XL16' 00000000000000000000000000000000F'	V1
00002B98	00000000 0000000F						
00002BA0	00000000 00000000			2705	DC	XL16' 00000000000000000000000000000000A'	V2
00002BA8	00000000 0000000A						
				2706			
				2707	VRI_G	VPSOP, 159, 202, 1, 0	nz=1 pc=1
00002BB0				2708+	DS	0FD	
00002BB0		00002BB0		2709+	USING	*, R5	base for test data and test routine
00002BB0	00002BD0			2710+T71	DC	A(X71)	address of test routine
00002BB4	0047			2711+	DC	H' 71'	test number
00002BB6	00			2712+	DC	X' 00'	
00002BB7	9F			2713+	DC	HL1' 159'	i3
00002BB8	CA			2714+	DC	HL1' 202'	i4
00002BB9	01			2715+	DC	HL1' 1'	m5
00002BBA	00			2716+	DC	HL1' 0'	cc
00002BBB	07			2717+	DC	HL1' 7'	cc failed mask
00002BBC	00002C00			2718+V2_71	DC	A(RE71+16)	address of v2: 16-byte packed decimal
00002BC0	E5D7E2D6 D7404040			2719+	DC	CL8' VPSOP'	instruction name
00002BC8	00000010			2720+	DC	A(16)	result length
00002BCC	00002BF0			2721+REA71	DC	A(RE71)	result address
				2722+*			INSTRUCTION UNDER TEST ROUTINE
00002BD0				2723+X71	DS	0F	
00002BD0	5820 500C		00002BBC	2724+	L	R2, V2_71	get v2
00002BD4	E722 0000 0006		00000000	2725+	VL	V2, 0(R2)	
00002BDA	E612 CA19 F05B			2726+	VPSOP	V1, V2, 159, 202, 1	test instruction
00002BE0	E710 8F10 000E		00001110	2727+	VST	V1, V10UTPUT	save result
00002BE6	B98D 0020			2728+	EPSW	R2, R0	exptract psw
00002BEA	5020 8EF0		000010F0	2729+	ST	R2, CCPSW	to save CC
00002BEE	07FB			2730+	BR	R11	return
00002BF0				2731+RE71	DC	0F	
00002BF0				2732+	DROP	R5	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002BF0	00000000 00000000			2733	DC	XL16' 00000000000000000000000000000000F'	V1
00002BF8	00000000 0000000F						
00002C00	00000000 00000000			2734	DC	XL16' 00000000000000000000000000000000A'	V2
00002C08	00000000 0000000A						
				2735 * V2: negative			
00002C10				2736	VRI_G	VPSOP, 159, 138, 1, 0	nz=0 pc=1
00002C10		00002C10		2737+	DS	OFD	
00002C10	00002C30			2738+	USING	*, R5	base for test data and test routine
00002C14	0048			2739+T72	DC	A(X72)	address of test routine
00002C16	00			2740+	DC	H' 72'	test number
00002C17	9F			2741+	DC	X' 00'	
00002C18	8A			2742+	DC	HL1' 159'	i3
00002C19	01			2743+	DC	HL1' 138'	i4
00002C1A	00			2744+	DC	HL1' 1'	m5
00002C1B	07			2745+	DC	HL1' 0'	cc
00002C1C	00002C60			2746+	DC	HL1' 7'	cc failed mask
00002C20	E5D7E2D6 D7404040			2747+V2_72	DC	A(RE72+16)	address of v2: 16-byte packed decimal
00002C28	00000010			2748+	DC	CL8' VPSOP'	instruction name
00002C2C	00002C50			2749+	DC	A(16)	result length
				2750+REA72	DC	A(RE72)	result address
				2751+*			INSTRUCTION UNDER TEST ROUTINE
00002C30				2752+X72	DS	OF	
00002C30	5820 500C		00002C1C	2753+	L	R2, V2_72	get v2
00002C34	E722 0000 0006		00000000	2754+	VL	V2, 0(R2)	
00002C3A	E612 8A19 F05B			2755+	VPSOP	V1, V2, 159, 138, 1	test instruction
00002C40	E710 8F10 000E		00001110	2756+	VST	V1, V10UTPUT	save result
00002C46	B98D 0020			2757+	EPSW	R2, R0	exptract psw
00002C4A	5020 8EF0		000010F0	2758+	ST	R2, CCPSW	to save CC
00002C4E	07FB			2759+	BR	R11	return
00002C50				2760+RE72	DC	OF	
00002C50				2761+	DROP	R5	
00002C50	00000000 00000000			2762	DC	XL16' 00000000000000000000000000000000F'	V1
00002C58	00000000 0000000F						
00002C60	00000000 00000000			2763	DC	XL16' 00000000000000000000000000000000D'	V2
00002C68	00000000 0000000D						
				2764			
00002C70				2765	VRI_G	VPSOP, 159, 202, 1, 0	nz=1 pc=1
00002C70		00002C70		2766+	DS	OFD	
00002C70	00002C90			2767+	USING	*, R5	base for test data and test routine
00002C74	0049			2768+T73	DC	A(X73)	address of test routine
00002C76	00			2769+	DC	H' 73'	test number
00002C77	9F			2770+	DC	X' 00'	
00002C78	CA			2771+	DC	HL1' 159'	i3
00002C79	01			2772+	DC	HL1' 202'	i4
00002C7A	00			2773+	DC	HL1' 1'	m5
00002C7B	07			2774+	DC	HL1' 0'	cc
00002C7C	00002CC0			2775+	DC	HL1' 7'	cc failed mask
00002C80	E5D7E2D6 D7404040			2776+V2_73	DC	A(RE73+16)	address of v2: 16-byte packed decimal
00002C88	00000010			2777+	DC	CL8' VPSOP'	instruction name
00002C8C	00002CB0			2778+	DC	A(16)	result length
				2779+REA73	DC	A(RE73)	result address
				2780+*			INSTRUCTION UNDER TEST ROUTINE
00002C90				2781+X73	DS	OF	
00002C90	5820 500C		00002C7C	2782+	L	R2, V2_73	get v2
00002C94	E722 0000 0006		00000000	2783+	VL	V2, 0(R2)	
00002C9A	E612 CA19 F05B			2784+	VPSOP	V1, V2, 159, 202, 1	test instruction

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
00002CA0	E710 8F10 000E		00001110	2785+	VST	V1, V10OUTPUT	save result	
00002CA6	B98D 0020			2786+	EPSW	R2, R0	exptract psw	
00002CAA	5020 8EF0		000010F0	2787+	ST	R2, CCPSW	to save CC	
00002CAE	07FB			2788+	BR	R11	return	
00002CB0				2789+RE73	DC	0F		
00002CB0				2790+	DROP	R5		
00002CB0	00000000 00000000			2791	DC	XL16' 00000000000000000000000000000000F'	V1	
00002CB8	00000000 0000000F							
00002CC0	00000000 00000000			2792	DC	XL16' 00000000000000000000000000000000B'	V2	
00002CC8	00000000 0000000B							
				2793				
				2794 *				
				2795 *	SC=11 (force negative): nv=1 to avoid data exceptions			
				2796 *				
				2797				
				2798 *	V1: nonzero	V2: - - - - -	PC=' - ' NZ=' â€‘'	V1_sign=D CC=1
				2799 *	V2: positive	PC=0		
				2800	VRI_G	VPSOP, 159, 140, 1, 1	nz=0 pc=0	
00002CD0				2801+	DS	0FD		
00002CD0		00002CD0		2802+	USING	*, R5	base for test data and test routine	
00002CD0	00002CF0			2803+T74	DC	A(X74)	address of test routine	
00002CD4	004A			2804+	DC	H' 74'	test number	
00002CD6	00			2805+	DC	X' 00'		
00002CD7	9F			2806+	DC	HL1' 159'	i3	
00002CD8	8C			2807+	DC	HL1' 140'	i4	
00002CD9	01			2808+	DC	HL1' 1'	m5	
00002CDA	01			2809+	DC	HL1' 1'	cc	
00002CDB	0B			2810+	DC	HL1' 11'	cc failed mask	
00002CDC	00002D20			2811+V2_74	DC	A(RE74+16)	address of v2: 16-byte packed decimal	
00002CE0	E5D7E2D6 D7404040			2812+	DC	CL8' VPSOP'	instruction name	
00002CE8	00000010			2813+	DC	A(16)	result length	
00002CEC	00002D10			2814+REA74	DC	A(RE74)	result address	
				2815+*			INSTRUCTION UNDER TEST ROUTINE	
00002CF0				2816+X74	DS	0F		
00002CF0	5820 500C		00002CDC	2817+	L	R2, V2_74	get v2	
00002CF4	E722 0000 0006		00000000	2818+	VL	V2, 0(R2)		
00002CFA	E612 8C19 F05B			2819+	VPSOP	V1, V2, 159, 140, 1	test instruction	
00002D00	E710 8F10 000E		00001110	2820+	VST	V1, V10OUTPUT	save result	
00002D06	B98D 0020			2821+	EPSW	R2, R0	exptract psw	
00002D0A	5020 8EF0		000010F0	2822+	ST	R2, CCPSW	to save CC	
00002D0E	07FB			2823+	BR	R11	return	
00002D10				2824+RE74	DC	0F		
00002D10				2825+	DROP	R5		
00002D10	00000000 00000000			2826	DC	XL16' 000000000000000000000000220000000D'	V1	
00002D18	00000022 0000000D							
00002D20	00000000 00000000			2827	DC	XL16' 000000000000000000000000220000000A'	V2	
00002D28	00000022 0000000A							
				2828				
				2829	VRI_G	VPSOP, 159, 204, 1, 1	nz=1 pc=0	
00002D30				2830+	DS	0FD		
00002D30		00002D30		2831+	USING	*, R5	base for test data and test routine	
00002D30	00002D50			2832+T75	DC	A(X75)	address of test routine	
00002D34	004B			2833+	DC	H' 75'	test number	
00002D36	00			2834+	DC	X' 00'		
00002D37	9F			2835+	DC	HL1' 159'	i3	
00002D38	CC			2836+	DC	HL1' 204'	i4	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002D39	01			2837+	DC	HL1' 1'	m5
00002D3A	01			2838+	DC	HL1' 1'	cc
00002D3B	0B			2839+	DC	HL1' 11'	cc failed mask
00002D3C	00002D80			2840+V2_75	DC	A(RE75+16)	address of v2: 16-byte packed decimal
00002D40	E5D7E2D6 D7404040			2841+	DC	CL8' VPSOP'	instruction name
00002D48	00000010			2842+	DC	A(16)	result length
00002D4C	00002D70			2843+REA75	DC	A(RE75)	result address
				2844+*			INSTRUCTION UNDER TEST ROUTINE
00002D50				2845+X75	DS	0F	
00002D50	5820 500C		00002D3C	2846+	L	R2, V2_75	get v2
00002D54	E722 0000 0006		00000000	2847+	VL	V2, 0(R2)	
00002D5A	E612 CC19 F05B			2848+	VPSOP	V1, V2, 159, 204, 1	test instruction
00002D60	E710 8F10 000E		00001110	2849+	VST	V1, V10OUTPUT	save result
00002D66	B98D 0020			2850+	EPSW	R2, R0	exptract psw
00002D6A	5020 8EF0		000010F0	2851+	ST	R2, CCPSW	to save CC
00002D6E	07FB			2852+	BR	R11	return
00002D70				2853+RE75	DC	0F	
00002D70				2854+	DROP	R5	
00002D70	00000000 00000000			2855	DC	XL16' 000000000000000000000000220000000D'	V1
00002D78	00000022 0000000D						
00002D80	00000000 00000000			2856	DC	XL16' 000000000000000000000000220000000A'	V2
00002D88	00000022 0000000A						
				2857 * V2: negative		PC=0	
00002D90				2858	VRI_G	VPSOP, 159, 140, 1, 1	nz=0 pc=0
00002D90		00002D90		2859+	DS	0FD	
00002D90	00002DB0			2860+	USING	*, R5	base for test data and test routine
00002D94	004C			2861+T76	DC	A(X76)	address of test routine
00002D96	00			2862+	DC	H' 76'	test number
00002D96	00			2863+	DC	X' 00'	
00002D97	9F			2864+	DC	HL1' 159'	i3
00002D98	8C			2865+	DC	HL1' 140'	i4
00002D99	01			2866+	DC	HL1' 1'	m5
00002D9A	01			2867+	DC	HL1' 1'	cc
00002D9B	0B			2868+	DC	HL1' 11'	cc failed mask
00002D9C	00002DE0			2869+V2_76	DC	A(RE76+16)	address of v2: 16-byte packed decimal
00002DA0	E5D7E2D6 D7404040			2870+	DC	CL8' VPSOP'	instruction name
00002DA8	00000010			2871+	DC	A(16)	result length
00002DAC	00002DD0			2872+REA76	DC	A(RE76)	result address
				2873+*			INSTRUCTION UNDER TEST ROUTINE
00002DB0				2874+X76	DS	0F	
00002DB0	5820 500C		00002D9C	2875+	L	R2, V2_76	get v2
00002DB4	E722 0000 0006		00000000	2876+	VL	V2, 0(R2)	
00002DBA	E612 8C19 F05B			2877+	VPSOP	V1, V2, 159, 140, 1	test instruction
00002DC0	E710 8F10 000E		00001110	2878+	VST	V1, V10OUTPUT	save result
00002DC6	B98D 0020			2879+	EPSW	R2, R0	exptract psw
00002DCA	5020 8EF0		000010F0	2880+	ST	R2, CCPSW	to save CC
00002DCE	07FB			2881+	BR	R11	return
00002DD0				2882+RE76	DC	0F	
00002DD0				2883+	DROP	R5	
00002DD0	00000000 00000000			2884	DC	XL16' 000000000000000000000000220000000D'	V1
00002DD8	00000022 0000000D						
00002DE0	00000000 00000000			2885	DC	XL16' 000000000000000000000000220000000D'	V2
00002DE8	00000022 0000000D						
				2886			
00002DF0				2887	VRI_G	VPSOP, 159, 204, 1, 1	nz=1 pc=0
				2888+	DS	0FD	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002E98	00000022 0000000D						
00002EA0	00000000 00000000			2943	DC	XL16' 000000000000000000000000220000000A'	V2
00002EA8	00000022 0000000A						
				2944			
				2945	VRI_G	VPSOP, 159, 206, 1, 1	nz=1 pc=1
00002EB0				2946+	DS	0FD	
00002EB0		00002EB0		2947+	USING	*, R5	base for test data and test routine
00002EB0	00002ED0			2948+T79	DC	A(X79)	address of test routine
00002EB4	004F			2949+	DC	H' 79'	test number
00002EB6	00			2950+	DC	X' 00'	
00002EB7	9F			2951+	DC	HL1' 159'	i3
00002EB8	CE			2952+	DC	HL1' 206'	i4
00002EB9	01			2953+	DC	HL1' 1'	m5
00002EBA	01			2954+	DC	HL1' 1'	cc
00002EBB	0B			2955+	DC	HL1' 11'	cc failed mask
00002EBC	00002F00			2956+V2_79	DC	A(RE79+16)	address of v2: 16-byte packed decimal
00002EC0	E5D7E2D6 D7404040			2957+	DC	CL8' VPSOP'	instruction name
00002EC8	00000010			2958+	DC	A(16)	result length
00002ECC	00002EF0			2959+REA79	DC	A(RE79)	result address
				2960+*			INSTRUCTION UNDER TEST ROUTINE
00002ED0				2961+X79	DS	0F	
00002ED0	5820 500C		00002EBC	2962+	L	R2, V2_79	get v2
00002ED4	E722 0000 0006		00000000	2963+	VL	V2, 0(R2)	
00002EDA	E612 CE19 F05B			2964+	VPSOP	V1, V2, 159, 206, 1	test instruction
00002EE0	E710 8F10 000E		00001110	2965+	VST	V1, V10OUTPUT	save result
00002EE6	B98D 0020			2966+	EPSW	R2, R0	exptract psw
00002EEA	5020 8EF0		000010F0	2967+	ST	R2, CCPSW	to save CC
00002EEE	07FB			2968+	BR	R11	return
00002EF0				2969+RE79	DC	0F	
00002EF0				2970+	DROP	R5	
00002EF0	00000000 00000000			2971	DC	XL16' 000000000000000000000000220000000D'	V1
00002EF8	00000022 0000000D						
00002F00	00000000 00000000			2972	DC	XL16' 000000000000000000000000220000000A'	V2
00002F08	00000022 0000000A						
				2973 * V2: negative	PC=1		
				2974	VRI_G	VPSOP, 159, 142, 1, 1	nz=0 pc=1
00002F10				2975+	DS	0FD	
00002F10		00002F10		2976+	USING	*, R5	base for test data and test routine
00002F10	00002F30			2977+T80	DC	A(X80)	address of test routine
00002F14	0050			2978+	DC	H' 80'	test number
00002F16	00			2979+	DC	X' 00'	
00002F17	9F			2980+	DC	HL1' 159'	i3
00002F18	8E			2981+	DC	HL1' 142'	i4
00002F19	01			2982+	DC	HL1' 1'	m5
00002F1A	01			2983+	DC	HL1' 1'	cc
00002F1B	0B			2984+	DC	HL1' 11'	cc failed mask
00002F1C	00002F60			2985+V2_80	DC	A(RE80+16)	address of v2: 16-byte packed decimal
00002F20	E5D7E2D6 D7404040			2986+	DC	CL8' VPSOP'	instruction name
00002F28	00000010			2987+	DC	A(16)	result length
00002F2C	00002F50			2988+REA80	DC	A(RE80)	result address
				2989+*			INSTRUCTION UNDER TEST ROUTINE
00002F30				2990+X80	DS	0F	
00002F30	5820 500C		00002F1C	2991+	L	R2, V2_80	get v2
00002F34	E722 0000 0006		00000000	2992+	VL	V2, 0(R2)	
00002F3A	E612 8E19 F05B			2993+	VPSOP	V1, V2, 159, 142, 1	test instruction
00002F40	E710 8F10 000E		00001110	2994+	VST	V1, V10OUTPUT	save result

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002F46	B98D 0020			2995+	EPSW	R2, R0	extract psw
00002F4A	5020 8EF0		000010F0	2996+	ST	R2, CCPSW	to save CC
00002F4E	07FB			2997+	BR	R11	return
00002F50				2998+RE80	DC	0F	
00002F50				2999+	DROP	R5	
00002F50	00000000 00000000			3000	DC	XL16' 000000000000000000000000220000000D'	V1
00002F58	00000022 0000000D						
00002F60	00000000 00000000			3001	DC	XL16' 000000000000000000000000220000000D'	V2
00002F68	00000022 0000000D						
				3002			
				3003	VRI_G	VPSOP, 159, 206, 1, 1	nz=1 pc=1
00002F70				3004+	DS	0FD	
00002F70		00002F70		3005+	USING	*, R5	base for test data and test routine
00002F70	00002F90			3006+T81	DC	A(X81)	address of test routine
00002F74	0051			3007+	DC	H' 81'	test number
00002F76	00			3008+	DC	X' 00'	
00002F77	9F			3009+	DC	HL1' 159'	i3
00002F78	CE			3010+	DC	HL1' 206'	i4
00002F79	01			3011+	DC	HL1' 1'	m5
00002F7A	01			3012+	DC	HL1' 1'	cc
00002F7B	0B			3013+	DC	HL1' 11'	cc failed mask
00002F7C	00002FC0			3014+V2_81	DC	A(RE81+16)	address of v2: 16-byte packed decimal
00002F80	E5D7E2D6 D7404040			3015+	DC	CL8' VPSOP'	instruction name
00002F88	00000010			3016+	DC	A(16)	result length
00002F8C	00002FB0			3017+REA81	DC	A(RE81)	result address
				3018+*			INSTRUCTION UNDER TEST ROUTINE
00002F90				3019+X81	DS	0F	
00002F90	5820 500C		00002F7C	3020+	L	R2, V2_81	get v2
00002F94	E722 0000 0006		00000000	3021+	VL	V2, 0(R2)	
00002F9A	E612 CE19 F05B			3022+	VPSOP	V1, V2, 159, 206, 1	test instruction
00002FA0	E710 8F10 000E		00001110	3023+	VST	V1, V10UTPUT	save result
00002FA6	B98D 0020			3024+	EPSW	R2, R0	extract psw
00002FAA	5020 8EF0		000010F0	3025+	ST	R2, CCPSW	to save CC
00002FAE	07FB			3026+	BR	R11	return
00002FB0				3027+RE81	DC	0F	
00002FB0				3028+	DROP	R5	
00002FB0	00000000 00000000			3029	DC	XL16' 000000000000000000000000220000000D'	V1
00002FB8	00000022 0000000D						
00002FC0	00000000 00000000			3030	DC	XL16' 000000000000000000000000220000000B'	V2
00002FC8	00000022 0000000B						
				3031			
				3032 *	V1: zero	V2: - - - - -	PC=' 0' NZ=' 0' V1_sign=C CC=0
				3033 *	V2: positive		
				3034	VRI_G	VPSOP, 159, 140, 1, 0	nz=0 pc=0
00002FD0				3035+	DS	0FD	
00002FD0		00002FD0		3036+	USING	*, R5	base for test data and test routine
00002FD0	00002FF0			3037+T82	DC	A(X82)	address of test routine
00002FD4	0052			3038+	DC	H' 82'	test number
00002FD6	00			3039+	DC	X' 00'	
00002FD7	9F			3040+	DC	HL1' 159'	i3
00002FD8	8C			3041+	DC	HL1' 140'	i4
00002FD9	01			3042+	DC	HL1' 1'	m5
00002FDA	00			3043+	DC	HL1' 0'	cc
00002FDB	07			3044+	DC	HL1' 7'	cc failed mask
00002FDC	00003020			3045+V2_82	DC	A(RE82+16)	address of v2: 16-byte packed decimal
00002FE0	E5D7E2D6 D7404040			3046+	DC	CL8' VPSOP'	instruction name

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00002FE8	00000010			3047+	DC	A(16)	result length
00002FEC	00003010			3048+REA82	DC	A(RE82)	result address
				3049+*			INSTRUCTION UNDER TEST ROUTINE
00002FF0				3050+X82	DS	0F	
00002FF0	5820 500C		00002FDC	3051+	L	R2, V2_82	get v2
00002FF4	E722 0000 0006		00000000	3052+	VL	V2, 0(R2)	
00002FFA	E612 8C19 F05B			3053+	VPSOP	V1, V2, 159, 140, 1	test instruction
00003000	E710 8F10 000E		00001110	3054+	VST	V1, V10UTPUT	save result
00003006	B98D 0020			3055+	EPSW	R2, R0	exptract psw
0000300A	5020 8EF0		000010F0	3056+	ST	R2, CCPSW	to save CC
0000300E	07FB			3057+	BR	R11	return
00003010				3058+RE82	DC	0F	
00003010				3059+	DROP	R5	
00003010	00000000 00000000			3060	DC	XL16' 00000000000000000000000000000000C'	V1
00003018	00000000 0000000C						
00003020	00000000 00000000			3061	DC	XL16' 00000000000000000000000000000000A'	V2
00003028	00000000 0000000A						
				3062 * V2: negative			
00003030				3063	VRI_G	VPSOP, 159, 140, 1, 0	nz=0 pc=0
00003030		00003030		3064+	DS	0FD	
00003030	00003050			3065+	USING	*, R5	base for test data and test routine
00003034	0053			3066+T83	DC	A(X83)	address of test routine
00003036	00			3067+	DC	H' 83'	test number
00003037	9F			3068+	DC	X' 00'	
00003038	8C			3069+	DC	HL1' 159'	i3
00003039	01			3070+	DC	HL1' 140'	i4
0000303A	00			3071+	DC	HL1' 1'	m5
0000303B	07			3072+	DC	HL1' 0'	cc
0000303C	00003080			3073+	DC	HL1' 7'	cc failed mask
00003040	E5D7E2D6 D7404040			3074+V2_83	DC	A(RE83+16)	address of v2: 16-byte packed decimal
00003048	00000010			3075+	DC	CL8' VPSOP'	instruction name
0000304C	00003070			3076+	DC	A(16)	result length
				3077+REA83	DC	A(RE83)	result address
				3078+*			INSTRUCTION UNDER TEST ROUTINE
00003050				3079+X83	DS	0F	
00003050	5820 500C		0000303C	3080+	L	R2, V2_83	get v2
00003054	E722 0000 0006		00000000	3081+	VL	V2, 0(R2)	
0000305A	E612 8C19 F05B			3082+	VPSOP	V1, V2, 159, 140, 1	test instruction
00003060	E710 8F10 000E		00001110	3083+	VST	V1, V10UTPUT	save result
00003066	B98D 0020			3084+	EPSW	R2, R0	exptract psw
0000306A	5020 8EF0		000010F0	3085+	ST	R2, CCPSW	to save CC
0000306E	07FB			3086+	BR	R11	return
00003070				3087+RE83	DC	0F	
00003070				3088+	DROP	R5	
00003070	00000000 00000000			3089	DC	XL16' 00000000000000000000000000000000C'	V1
00003078	00000000 0000000C						
00003080	00000000 00000000			3090	DC	XL16' 00000000000000000000000000000000D'	V2
00003088	00000000 0000000D						
				3091			
				3092 * V1: zero V2: - - - - - PC=' 1' NZ=' 0' V1_sign=F CC=0			
				3093 * V2: positive			
00003090				3094	VRI_G	VPSOP, 159, 142, 1, 0	nz=0 pc=1
00003090		00003090		3095+	DS	0FD	
00003090	000030B0			3096+	USING	*, R5	base for test data and test routine
00003094	0054			3097+T84	DC	A(X84)	address of test routine
				3098+	DC	H' 84'	test number

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00003096	00			3099+	DC	X' 00'	
00003097	9F			3100+	DC	HL1' 159'	i3
00003098	8E			3101+	DC	HL1' 142'	i4
00003099	01			3102+	DC	HL1' 1'	m5
0000309A	00			3103+	DC	HL1' 0'	cc
0000309B	07			3104+	DC	HL1' 7'	cc failed mask
0000309C	000030E0			3105+V2_84	DC	A(RE84+16)	address of v2: 16-byte packed decimal
000030A0	E5D7E2D6 D7404040			3106+	DC	CL8' VPSOP'	instruction name
000030A8	00000010			3107+	DC	A(16)	result length
000030AC	000030D0			3108+REA84	DC	A(RE84)	result address
				3109+*			INSTRUCTION UNDER TEST ROUTINE
000030B0				3110+X84	DS	0F	
000030B0	5820 500C		0000309C	3111+	L	R2, V2_84	get v2
000030B4	E722 0000 0006		00000000	3112+	VL	V2, 0(R2)	
000030BA	E612 8E19 F05B			3113+	VPSOP	V1, V2, 159, 142, 1	test instruction
000030C0	E710 8F10 000E		00001110	3114+	VST	V1, V10UTPUT	save result
000030C6	B98D 0020			3115+	EPSW	R2, R0	exptract psw
000030CA	5020 8EF0		000010F0	3116+	ST	R2, CCPSW	to save CC
000030CE	07FB			3117+	BR	R11	return
000030D0				3118+RE84	DC	0F	
000030D0				3119+	DROP	R5	
000030D0	00000000 00000000			3120	DC	XL16' 00000000000000000000000000000000F'	V1
000030D8	00000000 0000000F						
000030E0	00000000 00000000			3121	DC	XL16' 00000000000000000000000000000000A'	V2
000030E8	00000000 0000000A						
				3122 * V2: negative			
				3123	VRI_G	VPSOP, 159, 142, 1, 0	nz=0 pc=1
000030F0				3124+	DS	0FD	
000030F0		000030F0		3125+	USING	*, R5	base for test data and test routine
000030F0	00003110			3126+T85	DC	A(X85)	address of test routine
000030F4	0055			3127+	DC	H' 85'	test number
000030F6	00			3128+	DC	X' 00'	
000030F7	9F			3129+	DC	HL1' 159'	i3
000030F8	8E			3130+	DC	HL1' 142'	i4
000030F9	01			3131+	DC	HL1' 1'	m5
000030FA	00			3132+	DC	HL1' 0'	cc
000030FB	07			3133+	DC	HL1' 7'	cc failed mask
000030FC	00003140			3134+V2_85	DC	A(RE85+16)	address of v2: 16-byte packed decimal
00003100	E5D7E2D6 D7404040			3135+	DC	CL8' VPSOP'	instruction name
00003108	00000010			3136+	DC	A(16)	result length
0000310C	00003130			3137+REA85	DC	A(RE85)	result address
				3138+*			INSTRUCTION UNDER TEST ROUTINE
00003110				3139+X85	DS	0F	
00003110	5820 500C		000030FC	3140+	L	R2, V2_85	get v2
00003114	E722 0000 0006		00000000	3141+	VL	V2, 0(R2)	
0000311A	E612 8E19 F05B			3142+	VPSOP	V1, V2, 159, 142, 1	test instruction
00003120	E710 8F10 000E		00001110	3143+	VST	V1, V10UTPUT	save result
00003126	B98D 0020			3144+	EPSW	R2, R0	exptract psw
0000312A	5020 8EF0		000010F0	3145+	ST	R2, CCPSW	to save CC
0000312E	07FB			3146+	BR	R11	return
00003130				3147+RE85	DC	0F	
00003130				3148+	DROP	R5	
00003130	00000000 00000000			3149	DC	XL16' 00000000000000000000000000000000F'	V1
00003138	00000000 0000000F						
00003140	00000000 00000000			3150	DC	XL16' 00000000000000000000000000000000D'	V2
00003148	00000000 0000000D						

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				3151	
				3152 * V1: zero V2: - - - - - PC=' - ' NZ=' 1' V1_sign=d CC=0	
				3153 * V2: positive	
				3154 VRI_G VPSOP, 159, 204, 1, 0 nz=1 pc=0	
00003150				3155+ DS OFD	
00003150		00003150		3156+ USING *, R5	base for test data and test routine
00003150	00003170			3157+T86 DC A(X86)	address of test routine
00003154	0056			3158+ DC H' 86'	test number
00003156	00			3159+ DC X' 00'	
00003157	9F			3160+ DC HL1' 159'	i3
00003158	CC			3161+ DC HL1' 204'	i4
00003159	01			3162+ DC HL1' 1'	m5
0000315A	00			3163+ DC HL1' 0'	cc
0000315B	07			3164+ DC HL1' 7'	cc failed mask
0000315C	000031A0			3165+V2_86 DC A(RE86+16)	address of v2: 16-byte packed decimal
00003160	E5D7E2D6 D7404040			3166+ DC CL8' VPSOP'	instruction name
00003168	00000010			3167+ DC A(16)	result length
0000316C	00003190			3168+REA86 DC A(RE86)	result address
				3169+*	INSTRUCTION UNDER TEST ROUTINE
00003170				3170+X86 DS OF	
00003170	5820 500C		0000315C	3171+ L R2, V2_86	get v2
00003174	E722 0000 0006		00000000	3172+ VL V2, 0(R2)	
0000317A	E612 CC19 F05B			3173+ VPSOP V1, V2, 159, 204, 1	test instruction
00003180	E710 8F10 000E		00001110	3174+ VST V1, V10UTPUT	save result
00003186	B98D 0020			3175+ EPSW R2, R0	exptract psw
0000318A	5020 8EF0		000010F0	3176+ ST R2, CCPSW	to save CC
0000318E	07FB			3177+ BR R11	return
00003190				3178+RE86 DC OF	
00003190				3179+ DROP R5	
00003190	00000000 00000000			3180 DC XL16' 00000000000000000000000000000000D'	V1
00003198	00000000 0000000D				
000031A0	00000000 00000000			3181 DC XL16' 00000000000000000000000000000000A'	V2
000031A8	00000000 0000000A				
				3182 * V2: negative	
				3183 VRI_G VPSOP, 159, 204, 1, 0 nz=1 pc=0	
000031B0				3184+ DS OFD	
000031B0		000031B0		3185+ USING *, R5	base for test data and test routine
000031B0	000031D0			3186+T87 DC A(X87)	address of test routine
000031B4	0057			3187+ DC H' 87'	test number
000031B6	00			3188+ DC X' 00'	
000031B7	9F			3189+ DC HL1' 159'	i3
000031B8	CC			3190+ DC HL1' 204'	i4
000031B9	01			3191+ DC HL1' 1'	m5
000031BA	00			3192+ DC HL1' 0'	cc
000031BB	07			3193+ DC HL1' 7'	cc failed mask
000031BC	00003200			3194+V2_87 DC A(RE87+16)	address of v2: 16-byte packed decimal
000031C0	E5D7E2D6 D7404040			3195+ DC CL8' VPSOP'	instruction name
000031C8	00000010			3196+ DC A(16)	result length
000031CC	000031F0			3197+REA87 DC A(RE87)	result address
				3198+*	INSTRUCTION UNDER TEST ROUTINE
000031D0				3199+X87 DS OF	
000031D0	5820 500C		000031BC	3200+ L R2, V2_87	get v2
000031D4	E722 0000 0006		00000000	3201+ VL V2, 0(R2)	
000031DA	E612 CC19 F05B			3202+ VPSOP V1, V2, 159, 204, 1	test instruction
000031E0	E710 8F10 000E		00001110	3203+ VST V1, V10UTPUT	save result
000031E6	B98D 0020			3204+ EPSW R2, R0	exptract psw

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
000031EA	5020 8EF0		000010F0	3205+	ST	R2, CCPSW	to save CC	
000031EE	07FB			3206+	BR	R11	return	
000031F0				3207+RE87	DC	0F		
000031F0				3208+	DROP	R5		
000031F0	00000000 00000000			3209	DC	XL16' 0000000000000000000000000000000D'		V1
000031F8	00000000 0000000D							
00003200	00000000 00000000		3210	DC	XL16' 0000000000000000000000000000000B'			V2
00003208	00000000 0000000B							
			3211					
			3212 * V2: positive					
			3213	VRI_G	VPSOP, 159, 206, 1, 0		nz=1 pc=1	
00003210			3214+	DS	0FD			
00003210		00003210	3215+	USING	*, R5		base for test data and test routine	
00003210	00003230		3216+T88	DC	A(X88)		address of test routine	
00003214	0058		3217+	DC	H' 88'		test number	
00003216	00		3218+	DC	X' 00'			
00003217	9F		3219+	DC	HL1' 159'		i3	
00003218	CE		3220+	DC	HL1' 206'		i4	
00003219	01		3221+	DC	HL1' 1'		m5	
0000321A	00		3222+	DC	HL1' 0'		cc	
0000321B	07		3223+	DC	HL1' 7'		cc failed mask	
0000321C	00003260		3224+V2_88	DC	A(RE88+16)		address of v2: 16-byte packed decimal	
00003220	E5D7E2D6 D7404040		3225+	DC	CL8' VPSOP'		instruction name	
00003228	00000010		3226+	DC	A(16)		result length	
0000322C	00003250		3227+REA88	DC	A(RE88)		result address	
			3228+*				INSTRUCTION UNDER TEST ROUTINE	
00003230			3229+X88	DS	0F			
00003230	5820 500C		3230+	L	R2, V2_88		get v2	
00003234	E722 0000 0006		3231+	VL	V2, 0(R2)			
0000323A	E612 CE19 F05B		3232+	VPSOP	V1, V2, 159, 206, 1		test instruction	
00003240	E710 8F10 000E		3233+	VST	V1, V10UTPUT		save result	
00003246	B98D 0020		3234+	EPSW	R2, R0		exptract psw	
0000324A	5020 8EF0		3235+	ST	R2, CCPSW		to save CC	
0000324E	07FB		3236+	BR	R11		return	
00003250			3237+RE88	DC	0F			
00003250			3238+	DROP	R5			
00003250	00000000 00000000		3239	DC	XL16' 0000000000000000000000000000000D'			V1
00003258	00000000 0000000D							
00003260	00000000 00000000		3240	DC	XL16' 0000000000000000000000000000000A'			V2
00003268	00000000 0000000A							
			3241 * V2: negative					
			3242	VRI_G	VPSOP, 159, 206, 1, 0		nz=1 pc=1	
00003270			3243+	DS	0FD			
00003270		00003270	3244+	USING	*, R5		base for test data and test routine	
00003270	00003290		3245+T89	DC	A(X89)		address of test routine	
00003274	0059		3246+	DC	H' 89'		test number	
00003276	00		3247+	DC	X' 00'			
00003277	9F		3248+	DC	HL1' 159'		i3	
00003278	CE		3249+	DC	HL1' 206'		i4	
00003279	01		3250+	DC	HL1' 1'		m5	
0000327A	00		3251+	DC	HL1' 0'		cc	
0000327B	07		3252+	DC	HL1' 7'		cc failed mask	
0000327C	000032C0		3253+V2_89	DC	A(RE89+16)		address of v2: 16-byte packed decimal	
00003280	E5D7E2D6 D7404040		3254+	DC	CL8' VPSOP'		instruction name	
00003288	00000010		3255+	DC	A(16)		result length	
0000328C	000032B0		3256+REA89	DC	A(RE89)		result address	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
				3257+*		INSTRUCTION UNDER TEST ROUTINE	
00003290				3258+X89	DS	0F	
00003290	5820 500C		0000327C	3259+	L	R2, V2_89	get v2
00003294	E722 0000 0006		00000000	3260+	VL	V2, 0(R2)	
0000329A	E612 CE19 F05B			3261+	VPSOP	V1, V2, 159, 206, 1	test instruction
000032A0	E710 8F10 000E		00001110	3262+	VST	V1, V10OUTPUT	save result
000032A6	B98D 0020			3263+	EPSW	R2, R0	exptrect psw
000032AA	5020 8EF0		000010F0	3264+	ST	R2, CCPSW	to save CC
000032AE	07FB			3265+	BR	R11	return
000032B0				3266+RE89	DC	0F	
000032B0				3267+	DROP	R5	
000032B0	00000000 00000000			3268	DC	XL16' 00000000000000000000000000000000D'	V1
000032B8	00000000 0000000D						
000032C0	00000000 00000000			3269	DC	XL16' 00000000000000000000000000000000B'	V2
000032C8	00000000 0000000B						
				3270			
				3271 *			
				3272 *	SOME cc=3 (overflow) tests with rdc=4		
				3273 *			
				3274 *	V1: zero V2: positive PC=' 0' NZ=' 0' V1_sign=C CC=0		
				3275	VRI_G	VPSOP, 132, 128, 1, 3	nz=0 pc=0
000032D0				3276+	DS	0FD	
000032D0		000032D0		3277+	USING	*, R5	base for test data and test routine
000032D0	000032F0			3278+T90	DC	A(X90)	address of test routine
000032D4	005A			3279+	DC	H' 90'	test number
000032D6	00			3280+	DC	X' 00'	
000032D7	84			3281+	DC	HL1' 132'	i3
000032D8	80			3282+	DC	HL1' 128'	i4
000032D9	01			3283+	DC	HL1' 1'	m5
000032DA	03			3284+	DC	HL1' 3'	cc
000032DB	0E			3285+	DC	HL1' 14'	cc failed mask
000032DC	00003320			3286+V2_90	DC	A(RE90+16)	address of v2: 16-byte packed decimal
000032E0	E5D7E2D6 D7404040			3287+	DC	CL8' VPSOP'	instruction name
000032E8	00000010			3288+	DC	A(16)	result length
000032EC	00003310			3289+REA90	DC	A(RE90)	result address
				3290+*			INSTRUCTION UNDER TEST ROUTINE
000032F0				3291+X90	DS	0F	
000032F0	5820 500C		000032DC	3292+	L	R2, V2_90	get v2
000032F4	E722 0000 0006		00000000	3293+	VL	V2, 0(R2)	
000032FA	E612 8018 405B			3294+	VPSOP	V1, V2, 132, 128, 1	test instruction
00003300	E710 8F10 000E		00001110	3295+	VST	V1, V10OUTPUT	save result
00003306	B98D 0020			3296+	EPSW	R2, R0	exptrect psw
0000330A	5020 8EF0		000010F0	3297+	ST	R2, CCPSW	to save CC
0000330E	07FB			3298+	BR	R11	return
00003310				3299+RE90	DC	0F	
00003310				3300+	DROP	R5	
00003310	00000000 00000000			3301	DC	XL16' 00000000000000000000000000000000C'	V1
00003318	00000000 0000000C						
00003320	00000000 00009990			3302	DC	XL16' 00000000000009990000000000000000F'	V2
00003328	00000000 0000000F						
				3303			
				3304 *	SC=01 (complement): nv=1 to avoid data exceptions		
				3305 *	V1: zero V2: positive PC=' 0' NZ=' 0' V1_sign=C CC=0		
				3306	VRI_G	VPSOP, 132, 132, 1, 3	nz=0 pc=0
00003330				3307+	DS	0FD	
00003330		00003330		3308+	USING	*, R5	base for test data and test routine

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
00003330	00003350			3309+T91	DC	A(X91)
00003334	005B			3310+	DC	H' 91'
00003336	00			3311+	DC	X' 00'
00003337	84			3312+	DC	HL1' 132'
00003338	84			3313+	DC	HL1' 132'
00003339	01			3314+	DC	HL1' 1'
0000333A	03			3315+	DC	HL1' 3'
0000333B	0E			3316+	DC	HL1' 14'
0000333C	00003380			3317+V2_91	DC	A(RE91+16)
00003340	E5D7E2D6 D7404040			3318+	DC	CL8' VPSOP'
00003348	00000010			3319+	DC	A(16)
0000334C	00003370			3320+REA91	DC	A(RE91)
				3321+*		INSTRUCTION UNDER TEST ROUTINE
00003350				3322+X91	DS	0F
00003350	5820 500C		0000333C	3323+	L	R2, V2_91
00003354	E722 0000 0006		00000000	3324+	VL	V2, 0(R2)
0000335A	E612 8418 405B			3325+	VPSOP	V1, V2, 132, 132, 1
00003360	E710 8F10 000E		00001110	3326+	VST	V1, V10UTPUT
00003366	B98D 0020			3327+	EPSW	R2, R0
0000336A	5020 8EF0		000010F0	3328+	ST	R2, CCPSW
0000336E	07FB			3329+	BR	R11
00003370				3330+RE91	DC	0F
00003370				3331+	DROP	R5
00003370	00000000 00000000			3332	DC	XL16' 00000000000000000000000000000000C' V1
00003378	00000000 0000000C					
00003380	00000000 00009990			3333	DC	XL16' 00000000000009990000000000000000A' V2
00003388	00000000 0000000A					
				3334		
				3335 *	SC=10 (force positive): nv=1 to avoid data exceptions	
				3336 *	V1: zero V2: ----- PC=' 0' NZ=' â€‘' V1_sign=C CC=0	
				3337 *	V2: positive	
				3338	VRI_G	VPSOP, 132, 136, 1, 3 nz=0 pc=0
00003390				3339+	DS	0FD
00003390		00003390		3340+	USING	*, R5
00003390	000033B0			3341+T92	DC	A(X92)
00003394	005C			3342+	DC	H' 92'
00003396	00			3343+	DC	X' 00'
00003397	84			3344+	DC	HL1' 132'
00003398	88			3345+	DC	HL1' 136'
00003399	01			3346+	DC	HL1' 1'
0000339A	03			3347+	DC	HL1' 3'
0000339B	0E			3348+	DC	HL1' 14'
0000339C	000033E0			3349+V2_92	DC	A(RE92+16)
000033A0	E5D7E2D6 D7404040			3350+	DC	CL8' VPSOP'
000033A8	00000010			3351+	DC	A(16)
000033AC	000033D0			3352+REA92	DC	A(RE92)
				3353+*		INSTRUCTION UNDER TEST ROUTINE
000033B0				3354+X92	DS	0F
000033B0	5820 500C		0000339C	3355+	L	R2, V2_92
000033B4	E722 0000 0006		00000000	3356+	VL	V2, 0(R2)
000033BA	E612 8818 405B			3357+	VPSOP	V1, V2, 132, 136, 1
000033C0	E710 8F10 000E		00001110	3358+	VST	V1, V10UTPUT
000033C6	B98D 0020			3359+	EPSW	R2, R0
000033CA	5020 8EF0		000010F0	3360+	ST	R2, CCPSW
000033CE	07FB			3361+	BR	R11
000033D0				3362+RE92	DC	0F

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
000033D0				3363+	DROP R5	
000033D0	00000000 00000000			3364	DC	XL16' 00000000000000000000000000000000C' V1
000033D8	00000000 0000000C					
000033E0	00000000 00000999			3365	DC	XL16' 0000000000000099900000000000000A' V2
000033E8	00000000 0000000A					
				3366		
				3367	* SC=11 (force negative): nv=1 to avoid data exceptions	
				3368	* V1: zero V2: ----- PC=' 0' NZ=' 0' V1_sign=C CC=0	
				3369	* V2: positive	
				3370	VRI_G VPSOP, 132, 140, 1, 3 nz=0 pc=0	
000033F0				3371+	DS	0FD
000033F0		000033F0		3372+	USING	*, R5 base for test data and test routine
000033F0	00003410			3373+T93	DC	A(X93) address of test routine
000033F4	005D			3374+	DC	H' 93' test number
000033F6	00			3375+	DC	X' 00'
000033F7	84			3376+	DC	HL1' 132' i3
000033F8	8C			3377+	DC	HL1' 140' i4
000033F9	01			3378+	DC	HL1' 1' m5
000033FA	03			3379+	DC	HL1' 3' cc
000033FB	0E			3380+	DC	HL1' 14' cc failed mask
000033FC	00003440			3381+V2_93	DC	A(RE93+16) address of v2: 16-byte packed decimal
00003400	E5D7E2D6 D7404040			3382+	DC	CL8' VPSOP' instruction name
00003408	00000010			3383+	DC	A(16) result length
0000340C	00003430			3384+REA93	DC	A(RE93) result address
				3385+*		INSTRUCTION UNDER TEST ROUTINE
00003410				3386+X93	DS	0F
00003410	5820 500C		000033FC	3387+	L	R2, V2_93 get v2
00003414	E722 0000 0006		00000000	3388+	VL	V2, 0(R2)
0000341A	E612 8C18 405B			3389+	VPSOP	V1, V2, 132, 140, 1 test instruction
00003420	E710 8F10 000E		00001110	3390+	VST	V1, V10UTPUT save result
00003426	B98D 0020			3391+	EPSW	R2, R0 exptract psw
0000342A	5020 8EF0		000010F0	3392+	ST	R2, CCPSW to save CC
0000342E	07FB			3393+	BR	R11 return
00003430				3394+RE93	DC	0F
00003430				3395+	DROP	R5
00003430	00000000 00000000			3396	DC	XL16' 00000000000000000000000000000000C' V1
00003438	00000000 0000000C					
00003440	00000000 00000000			3397	DC	XL16' 0000000000000000999000000000000A' V2
00003448	99900000 0000000A					
				3398		
				3399	* -----	
				3400	* SOME cc=3 (overflow) tests with rdc=7	
				3401	* -----	
				3402	* SC=00 (maintain): nv=1 to avoid data exceptions	
				3403	* V1: nonzero V2: positive PC=' 0' NZ=' 0' V1_sign=C CC=2	
				3404	VRI_G VPSOP, 135, 128, 1, 3 nz=0	
00003450				3405+	DS	0FD
00003450		00003450		3406+	USING	*, R5 base for test data and test routine
00003450	00003470			3407+T94	DC	A(X94) address of test routine
00003454	005E			3408+	DC	H' 94' test number
00003456	00			3409+	DC	X' 00'
00003457	87			3410+	DC	HL1' 135' i3
00003458	80			3411+	DC	HL1' 128' i4
00003459	01			3412+	DC	HL1' 1' m5
0000345A	03			3413+	DC	HL1' 3' cc
0000345B	0E			3414+	DC	HL1' 14' cc failed mask

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000345C	000034A0			3415+V2_94	DC	A(RE94+16)	address of v2: 16-byte packed decimal
00003460	E5D7E2D6 D7404040			3416+	DC	CL8' VPSOP'	instruction name
00003468	00000010			3417+	DC	A(16)	result length
0000346C	00003490			3418+REA94	DC	A(RE94)	result address
				3419+*			INSTRUCTION UNDER TEST ROUTINE
00003470				3420+X94	DS	0F	
00003470	5820 500C		0000345C	3421+	L	R2, V2_94	get v2
00003474	E722 0000 0006		00000000	3422+	VL	V2, 0(R2)	
0000347A	E612 8018 705B			3423+	VPSOP	V1, V2, 135, 128, 1	test instruction
00003480	E710 8F10 000E		00001110	3424+	VST	V1, V10UTPUT	save result
00003486	B98D 0020			3425+	EPSW	R2, R0	exptract psw
0000348A	5020 8EF0		000010F0	3426+	ST	R2, CCPSW	to save CC
0000348E	07FB			3427+	BR	R11	return
00003490				3428+RE94	DC	0F	
00003490				3429+	DROP	R5	
00003490	00000000 00000000			3430	DC	XL16' 00000000000000000000000002000000C'	V1
00003498	00000000 2000000C						
000034A0	00000000 00000000			3431	DC	XL16' 000000000000000000000000222000000F'	V2
000034A8	00000022 2000000F						
				3432			
				3433	* SC=01 (complement): nv=1 to avoid data exceptions		
				3434	* V1: nonzero V2: positive PC='-' NZ='â€‘' V1_sign=D CC=1		
				3435	VRI_G VPSOP, 135, 132, 1, 3 nz=0 pc=0		
000034B0				3436+	DS	0FD	
000034B0		000034B0		3437+	USING	*, R5	base for test data and test routine
000034B0	000034D0			3438+T95	DC	A(X95)	address of test routine
000034B4	005F			3439+	DC	H' 95'	test number
000034B6	00			3440+	DC	X' 00'	
000034B7	87			3441+	DC	HL1' 135'	i3
000034B8	84			3442+	DC	HL1' 132'	i4
000034B9	01			3443+	DC	HL1' 1'	m5
000034BA	03			3444+	DC	HL1' 3'	cc
000034BB	0E			3445+	DC	HL1' 14'	cc failed mask
000034BC	00003500			3446+V2_95	DC	A(RE95+16)	address of v2: 16-byte packed decimal
000034C0	E5D7E2D6 D7404040			3447+	DC	CL8' VPSOP'	instruction name
000034C8	00000010			3448+	DC	A(16)	result length
000034CC	000034F0			3449+REA95	DC	A(RE95)	result address
				3450+*			INSTRUCTION UNDER TEST ROUTINE
000034D0				3451+X95	DS	0F	
000034D0	5820 500C		000034BC	3452+	L	R2, V2_95	get v2
000034D4	E722 0000 0006		00000000	3453+	VL	V2, 0(R2)	
000034DA	E612 8418 705B			3454+	VPSOP	V1, V2, 135, 132, 1	test instruction
000034E0	E710 8F10 000E		00001110	3455+	VST	V1, V10UTPUT	save result
000034E6	B98D 0020			3456+	EPSW	R2, R0	exptract psw
000034EA	5020 8EF0		000010F0	3457+	ST	R2, CCPSW	to save CC
000034EE	07FB			3458+	BR	R11	return
000034F0				3459+RE95	DC	0F	
000034F0				3460+	DROP	R5	
000034F0	00000000 00000000			3461	DC	XL16' 00000000000000000000000002000000D'	V1
000034F8	00000000 2000000D						
00003500	00000000 00000000			3462	DC	XL16' 000000000000000000000000222000000C'	V2
00003508	00000022 2000000C						
				3463			
				3464	* SC=10 (force positive): nv=1 to avoid data exceptions		
				3465	* V1: nonzero V2: - - - - - PC='0' NZ='â€‘' V1_sign=C CC=2		
				3466	* V2: positive		

LOC	OBJECT CODE	ADDR1	ADDR2	STMT		
				3467	VRI_G VPSOP, 135, 136, 1, 3	nz=0 pc=0
00003510				3468+	DS OFD	
00003510		00003510		3469+	USING *, R5	base for test data and test routine
00003510	00003530			3470+T96	DC A(X96)	address of test routine
00003514	0060			3471+	DC H' 96'	test number
00003516	00			3472+	DC X' 00'	
00003517	87			3473+	DC HL1' 135'	i3
00003518	88			3474+	DC HL1' 136'	i4
00003519	01			3475+	DC HL1' 1'	m5
0000351A	03			3476+	DC HL1' 3'	cc
0000351B	0E			3477+	DC HL1' 14'	cc failed mask
0000351C	00003560			3478+V2_96	DC A(RE96+16)	address of v2: 16-byte packed decimal
00003520	E5D7E2D6 D7404040			3479+	DC CL8' VPSOP'	instruction name
00003528	00000010			3480+	DC A(16)	result length
0000352C	00003550			3481+REA96	DC A(RE96)	result address
				3482+*		INSTRUCTION UNDER TEST ROUTINE
00003530				3483+X96	DS OF	
00003530	5820 500C		0000351C	3484+	L R2, V2_96	get v2
00003534	E722 0000 0006		00000000	3485+	VL V2, 0(R2)	
0000353A	E612 8818 705B			3486+	VPSOP V1, V2, 135, 136, 1	test instruction
00003540	E710 8F10 000E		00001110	3487+	VST V1, V10UTPUT	save result
00003546	B98D 0020			3488+	EPSW R2, R0	exptract psw
0000354A	5020 8EF0		000010F0	3489+	ST R2, CCPSW	to save CC
0000354E	07FB			3490+	BR R11	return
00003550				3491+RE96	DC OF	
00003550				3492+	DROP R5	
00003550	00000000 00000000			3493	DC XL16' 00000000000000000000000002000000C'	V1
00003558	00000000 2000000C					
00003560	00000000 00000000			3494	DC XL16' 000000000000000000000000222000000A'	V2
00003568	00000022 2000000A					
				3495		
				3496 *	SC=11 (force negative): nv=1 to avoid data exceptions	
				3497 *	V1: nonzero V2: ----- PC='-' NZ='â€‘' V1_sign=D CC=1	
				3498 *	V2: positive PC=0	
				3499	VRI_G VPSOP, 135, 140, 1, 3	nz=0 pc=0
00003570				3500+	DS OFD	
00003570		00003570		3501+	USING *, R5	base for test data and test routine
00003570	00003590			3502+T97	DC A(X97)	address of test routine
00003574	0061			3503+	DC H' 97'	test number
00003576	00			3504+	DC X' 00'	
00003577	87			3505+	DC HL1' 135'	i3
00003578	8C			3506+	DC HL1' 140'	i4
00003579	01			3507+	DC HL1' 1'	m5
0000357A	03			3508+	DC HL1' 3'	cc
0000357B	0E			3509+	DC HL1' 14'	cc failed mask
0000357C	000035C0			3510+V2_97	DC A(RE97+16)	address of v2: 16-byte packed decimal
00003580	E5D7E2D6 D7404040			3511+	DC CL8' VPSOP'	instruction name
00003588	00000010			3512+	DC A(16)	result length
0000358C	000035B0			3513+REA97	DC A(RE97)	result address
				3514+*		INSTRUCTION UNDER TEST ROUTINE
00003590				3515+X97	DS OF	
00003590	5820 500C		0000357C	3516+	L R2, V2_97	get v2
00003594	E722 0000 0006		00000000	3517+	VL V2, 0(R2)	
0000359A	E612 8C18 705B			3518+	VPSOP V1, V2, 135, 140, 1	test instruction
000035A0	E710 8F10 000E		00001110	3519+	VST V1, V10UTPUT	save result
000035A6	B98D 0020			3520+	EPSW R2, R0	exptract psw

LOC	OBJECT CODE	ADDR1	ADDR2	STMT					
000035AA	5020 8EF0		000010F0	3521+	ST	R2, CCPSW	to save CC		
000035AE	07FB			3522+	BR	R11	return		
000035B0				3523+RE97	DC	0F			
000035B0				3524+	DROP	R5			
000035B0	00000000	00000000		3525	DC	XL16' 00000000000000000000000002000000D'		V1	
000035B8	00000000	2000000D							
000035C0	00000000	00000000		3526	DC	XL16' 000000000000000000000000222000000A'		V2	
000035C8	00000022	2000000A							
				3527					
				3528	*+++++				
				3529	*test				
				3530					
000035D0	00000000			3531	DC	F' 0'	END OF TABLE		
000035D4	00000000			3532	DC	F' 0'			
				3533	*				
				3534	* table of pointers to individual load test				
				3535	*				
000035D8				3536	E6TESTS	DS	OF		
				3537		PTTABLE			
000035D8				3538+	TTABLE	DS	OF		
000035D8	00001170			3539+	DC	A(T1)	address of test		
000035DC	000011D0			3540+	DC	A(T2)	address of test		
000035E0	00001230			3541+	DC	A(T3)	address of test		
000035E4	00001290			3542+	DC	A(T4)	address of test		
000035E8	000012F0			3543+	DC	A(T5)	address of test		
000035EC	00001350			3544+	DC	A(T6)	address of test		
000035F0	000013B0			3545+	DC	A(T7)	address of test		
000035F4	00001410			3546+	DC	A(T8)	address of test		
000035F8	00001470			3547+	DC	A(T9)	address of test		
000035FC	000014D0			3548+	DC	A(T10)	address of test		
00003600	00001530			3549+	DC	A(T11)	address of test		
00003604	00001590			3550+	DC	A(T12)	address of test		
00003608	000015F0			3551+	DC	A(T13)	address of test		
0000360C	00001650			3552+	DC	A(T14)	address of test		
00003610	000016B0			3553+	DC	A(T15)	address of test		
00003614	00001710			3554+	DC	A(T16)	address of test		
00003618	00001770			3555+	DC	A(T17)	address of test		
0000361C	000017D0			3556+	DC	A(T18)	address of test		
00003620	00001830			3557+	DC	A(T19)	address of test		
00003624	00001890			3558+	DC	A(T20)	address of test		
00003628	000018F0			3559+	DC	A(T21)	address of test		
0000362C	00001950			3560+	DC	A(T22)	address of test		
00003630	000019B0			3561+	DC	A(T23)	address of test		
00003634	00001A10			3562+	DC	A(T24)	address of test		
00003638	00001A70			3563+	DC	A(T25)	address of test		
0000363C	00001AD0			3564+	DC	A(T26)	address of test		
00003640	00001B30			3565+	DC	A(T27)	address of test		
00003644	00001B90			3566+	DC	A(T28)	address of test		
00003648	00001BF0			3567+	DC	A(T29)	address of test		
0000364C	00001C50			3568+	DC	A(T30)	address of test		
00003650	00001CB0			3569+	DC	A(T31)	address of test		
00003654	00001D10			3570+	DC	A(T32)	address of test		
00003658	00001D70			3571+	DC	A(T33)	address of test		
0000365C	00001DD0			3572+	DC	A(T34)	address of test		
00003660	00001E30			3573+	DC	A(T35)	address of test		
00003664	00001E90			3574+	DC	A(T36)	address of test		

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
00003668	00001EF0			3575+	DC	A(T37)	address of test
0000366C	00001F50			3576+	DC	A(T38)	address of test
00003670	00001FB0			3577+	DC	A(T39)	address of test
00003674	00002010			3578+	DC	A(T40)	address of test
00003678	00002070			3579+	DC	A(T41)	address of test
0000367C	000020D0			3580+	DC	A(T42)	address of test
00003680	00002130			3581+	DC	A(T43)	address of test
00003684	00002190			3582+	DC	A(T44)	address of test
00003688	000021F0			3583+	DC	A(T45)	address of test
0000368C	00002250			3584+	DC	A(T46)	address of test
00003690	000022B0			3585+	DC	A(T47)	address of test
00003694	00002310			3586+	DC	A(T48)	address of test
00003698	00002370			3587+	DC	A(T49)	address of test
0000369C	000023D0			3588+	DC	A(T50)	address of test
000036A0	00002430			3589+	DC	A(T51)	address of test
000036A4	00002490			3590+	DC	A(T52)	address of test
000036A8	000024F0			3591+	DC	A(T53)	address of test
000036AC	00002550			3592+	DC	A(T54)	address of test
000036B0	000025B0			3593+	DC	A(T55)	address of test
000036B4	00002610			3594+	DC	A(T56)	address of test
000036B8	00002670			3595+	DC	A(T57)	address of test
000036BC	000026D0			3596+	DC	A(T58)	address of test
000036C0	00002730			3597+	DC	A(T59)	address of test
000036C4	00002790			3598+	DC	A(T60)	address of test
000036C8	000027F0			3599+	DC	A(T61)	address of test
000036CC	00002850			3600+	DC	A(T62)	address of test
000036D0	000028B0			3601+	DC	A(T63)	address of test
000036D4	00002910			3602+	DC	A(T64)	address of test
000036D8	00002970			3603+	DC	A(T65)	address of test
000036DC	000029D0			3604+	DC	A(T66)	address of test
000036E0	00002A30			3605+	DC	A(T67)	address of test
000036E4	00002A90			3606+	DC	A(T68)	address of test
000036E8	00002AF0			3607+	DC	A(T69)	address of test
000036EC	00002B50			3608+	DC	A(T70)	address of test
000036F0	00002BB0			3609+	DC	A(T71)	address of test
000036F4	00002C10			3610+	DC	A(T72)	address of test
000036F8	00002C70			3611+	DC	A(T73)	address of test
000036FC	00002CD0			3612+	DC	A(T74)	address of test
00003700	00002D30			3613+	DC	A(T75)	address of test
00003704	00002D90			3614+	DC	A(T76)	address of test
00003708	00002DF0			3615+	DC	A(T77)	address of test
0000370C	00002E50			3616+	DC	A(T78)	address of test
00003710	00002EB0			3617+	DC	A(T79)	address of test
00003714	00002F10			3618+	DC	A(T80)	address of test
00003718	00002F70			3619+	DC	A(T81)	address of test
0000371C	00002FD0			3620+	DC	A(T82)	address of test
00003720	00003030			3621+	DC	A(T83)	address of test
00003724	00003090			3622+	DC	A(T84)	address of test
00003728	000030F0			3623+	DC	A(T85)	address of test
0000372C	00003150			3624+	DC	A(T86)	address of test
00003730	000031B0			3625+	DC	A(T87)	address of test
00003734	00003210			3626+	DC	A(T88)	address of test
00003738	00003270			3627+	DC	A(T89)	address of test
0000373C	000032D0			3628+	DC	A(T90)	address of test
00003740	00003330			3629+	DC	A(T91)	address of test
00003744	00003390			3630+	DC	A(T92)	address of test

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT				
					3643	*****			
					3644	*	Register equates		
					3645	*****			
			00000000	00000001	3647	R0	EQU	0	
			00000001	00000001	3648	R1	EQU	1	
			00000002	00000001	3649	R2	EQU	2	
			00000003	00000001	3650	R3	EQU	3	
			00000004	00000001	3651	R4	EQU	4	
			00000005	00000001	3652	R5	EQU	5	
			00000006	00000001	3653	R6	EQU	6	
			00000007	00000001	3654	R7	EQU	7	
			00000008	00000001	3655	R8	EQU	8	
			00000009	00000001	3656	R9	EQU	9	
			0000000A	00000001	3657	R10	EQU	10	
			0000000B	00000001	3658	R11	EQU	11	
			0000000C	00000001	3659	R12	EQU	12	
			0000000D	00000001	3660	R13	EQU	13	
			0000000E	00000001	3661	R14	EQU	14	
			0000000F	00000001	3662	R15	EQU	15	
					3664	*****			
					3665	*	Register equates		
					3666	*****			
			00000000	00000001	3668	V0	EQU	0	
			00000001	00000001	3669	V1	EQU	1	
			00000002	00000001	3670	V2	EQU	2	
			00000003	00000001	3671	V3	EQU	3	
			00000004	00000001	3672	V4	EQU	4	
			00000005	00000001	3673	V5	EQU	5	
			00000006	00000001	3674	V6	EQU	6	
			00000007	00000001	3675	V7	EQU	7	
			00000008	00000001	3676	V8	EQU	8	
			00000009	00000001	3677	V9	EQU	9	
			0000000A	00000001	3678	V10	EQU	10	
			0000000B	00000001	3679	V11	EQU	11	
			0000000C	00000001	3680	V12	EQU	12	
			0000000D	00000001	3681	V13	EQU	13	
			0000000E	00000001	3682	V14	EQU	14	
			0000000F	00000001	3683	V15	EQU	15	
			00000010	00000001	3684	V16	EQU	16	
			00000011	00000001	3685	V17	EQU	17	
			00000012	00000001	3686	V18	EQU	18	
			00000013	00000001	3687	V19	EQU	19	
			00000014	00000001	3688	V20	EQU	20	
			00000015	00000001	3689	V21	EQU	21	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES												
PRTLNG	U	00000059	1	370	221												
PRTM5	C	0000105A	2	368	219												
PRTNAME	C	0000102F	8	360	198												
PRTNUM	C	00001014	3	358	196												
R0	U	00000000	1	3647	53	103	106	119	180	221	229	230	256	258	274	277	279
					281	283	294	605	634	663	692	721	750	779	808	837	866
					895	930	959	988	1017	1046	1075	1154	1183	1213	1242	1272	1301
					1330	1359	1389	1418	1447	1476	1506	1535	1565	1594	1624	1654	1684
					1713	1743	1772	1801	1830	1864	1893	1922	1951	1981	2010	2040	2069
					2103	2133	2163	2192	2222	2251	2281	2310	2345	2374	2403	2432	2463
					2492	2521	2550	2581	2610	2639	2668	2699	2728	2757	2786	2821	2850
					2879	2908	2937	2966	2995	3024	3055	3084	3115	3144	3175	3204	3234
					3263	3296	3327	3359	3391	3425	3456	3488	3520				
R1	U	00000001	1	3648	126	127	128	131	132	144	145	146	151	152	153	154	181
					222	239	240	288	298								
R10	U	0000000A	1	3657	100	101											
R11	U	0000000B	1	3658	123	124	607	636	665	694	723	752	781	810	839	868	897
					932	961	990	1019	1048	1077	1156	1185	1215	1244	1274	1303	1332
					1361	1391	1420	1449	1478	1508	1537	1567	1596	1626	1656	1686	1715
					1745	1774	1803	1832	1866	1895	1924	1953	1983	2012	2042	2071	2105
					2135	2165	2194	2224	2253	2283	2312	2347	2376	2405	2434	2465	2494
					2523	2552	2583	2612	2641	2670	2701	2730	2759	2788	2823	2852	2881
					2910	2939	2968	2997	3026	3057	3086	3117	3146	3177	3206	3236	3265
					3298	3329	3361	3393	3427	3458	3490	3522					
R12	U	0000000C	1	3659	112	115	135	232									
R13	U	0000000D	1	3660													
R14	U	0000000E	1	3661													
R15	U	0000000F	1	3662	182	223	251	261	262								
R2	U	00000002	1	3649	158	159	166	167	168	173	174	175	192	193	200	201	202
					207	208	209	214	215	216	256	257	258	275	277	283	284
					285	287	294	295	601	602	605	606	630	631	634	635	659
					660	663	664	688	689	692	693	717	718	721	722	746	747
					750	751	775	776	779	780	804	805	808	809	833	834	837
					838	862	863	866	867	891	892	895	896	926	927	930	931
					955	956	959	960	984	985	988	989	1013	1014	1017	1018	1042
					1043	1046	1047	1071	1072	1075	1076	1150	1151	1154	1155	1179	1180
					1183	1184	1209	1210	1213	1214	1238	1239	1242	1243	1268	1269	1272
					1273	1297	1298	1301	1302	1326	1327	1330	1331	1355	1356	1359	1360
					1385	1386	1389	1390	1414	1415	1418	1419	1443	1444	1447	1448	1472
					1473	1476	1477	1502	1503	1506	1507	1531	1532	1535	1536	1561	1562
					1565	1566	1590	1591	1594	1595	1620	1621	1624	1625	1650	1651	1654
					1655	1680	1681	1684	1685	1709	1710	1713	1714	1739	1740	1743	1744
					1768	1769	1772	1773	1797	1798	1801	1802	1826	1827	1830	1831	1860
					1861	1864	1865	1889	1890	1893	1894	1918	1919	1922	1923	1947	1948
					1951	1952	1977	1978	1981	1982	2006	2007	2010	2011	2036	2037	2040
					2041	2065	2066	2069	2070	2099	2100	2103	2104	2129	2130	2133	2134
					2159	2160	2163	2164	2188	2189	2192	2193	2218	2219	2222	2223	2247
					2248	2251	2252	2277	2278	2281	2282	2306	2307	2310	2311	2341	2342
					2345	2346	2370	2371	2374	2375	2399	2400	2403	2404	2428	2429	2432
					2433	2459	2460	2463	2464	2488	2489	2492	2493	2517	2518	2521	2522
					2546	2547	2550	2551	2577	2578	2581	2582	2606	2607	2610	2611	2635
					2636	2639	2640	2664	2665	2668	2669	2695	2696	2699	2700	2724	2725
					2728	2729	2753	2754	2757	2758	2782	2783	2786	2787	2817	2818	2821
					2822	2846	2847	2850	2851	2875	2876	2879	2880	2904	2905	2908	2909
					2933	2934	2937	2938	2962	2963	2966	2967	2991	2992	2995	2996	3020
					3021	3024	3025	3051	3052	3055	3056	3080	3081	3084	3085	3111	3112

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES												
					3115	3116	3140	3141	3144	3145	3171	3172	3175	3176	3200	3201	3204
					3205	3230	3231	3234	3235	3259	3260	3263	3264	3292	3293	3296	3297
					3323	3324	3327	3328	3355	3356	3359	3360	3387	3388	3391	3392	3421
					3422	3425	3426	3452	3453	3456	3457	3484	3485	3488	3489	3516	3517
					3520	3521											
R3	U	00000003	1	3650													
R4	U	00000004	1	3651													
R5	U	00000005	1	3652	115	116	121	252	260	586	609	615	638	644	667	673	696
					702	725	731	754	760	783	789	812	818	841	847	870	876
					899	911	934	940	963	969	992	998	1021	1027	1050	1056	1079
					1135	1158	1164	1187	1194	1217	1223	1246	1253	1276	1282	1305	1311
					1334	1340	1363	1370	1393	1399	1422	1428	1451	1457	1480	1487	1510
					1516	1539	1546	1569	1575	1598	1605	1628	1635	1658	1665	1688	1694
					1717	1724	1747	1753	1776	1782	1805	1811	1834	1845	1868	1874	1897
					1903	1926	1932	1955	1962	1985	1991	2014	2021	2044	2050	2073	2084
					2107	2114	2137	2144	2167	2173	2196	2203	2226	2232	2255	2262	2285
					2291	2314	2326	2349	2355	2378	2384	2407	2413	2436	2444	2467	2473
					2496	2502	2525	2531	2554	2562	2585	2591	2614	2620	2643	2649	2672
					2680	2703	2709	2732	2738	2761	2767	2790	2802	2825	2831	2854	2860
					2883	2889	2912	2918	2941	2947	2970	2976	2999	3005	3028	3036	3059
					3065	3088	3096	3119	3125	3148	3156	3179	3185	3208	3215	3238	3244
					3267	3277	3300	3308	3331	3340	3363	3372	3395	3406	3429	3437	3460
					3469	3492	3501	3524									
R6	U	00000006	1	3653													
R7	U	00000007	1	3654													
R8	U	00000008	1	3655	90	93	94	95	97								
R9	U	00000009	1	3656	91	97	98	100									
RE1	F	000011B0	4	608	595	598											
RE10	F	00001510	4	869	856	859											
RE11	F	00001570	4	898	885	888											
RE12	F	000015D0	4	933	920	923											
RE13	F	00001630	4	962	949	952											
RE14	F	00001690	4	991	978	981											
RE15	F	000016F0	4	1020	1007	1010											
RE16	F	00001750	4	1049	1036	1039											
RE17	F	000017B0	4	1078	1065	1068											
RE18	F	00001810	4	1157	1144	1147											
RE19	F	00001870	4	1186	1173	1176											
RE2	F	00001210	4	637	624	627											
RE20	F	000018D0	4	1216	1203	1206											
RE21	F	00001930	4	1245	1232	1235											
RE22	F	00001990	4	1275	1262	1265											
RE23	F	000019F0	4	1304	1291	1294											
RE24	F	00001A50	4	1333	1320	1323											
RE25	F	00001AB0	4	1362	1349	1352											
RE26	F	00001B10	4	1392	1379	1382											
RE27	F	00001B70	4	1421	1408	1411											
RE28	F	00001BD0	4	1450	1437	1440											
RE29	F	00001C30	4	1479	1466	1469											
RE3	F	00001270	4	666	653	656											
RE30	F	00001C90	4	1509	1496	1499											
RE31	F	00001CF0	4	1538	1525	1528											
RE32	F	00001D50	4	1568	1555	1558											
RE33	F	00001DB0	4	1597	1584	1587											
RE34	F	00001E10	4	1627	1614	1617											
RE35	F	00001E70	4	1657	1644	1647											

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
RE36	F	00001ED0	4	1687	1674 1677
RE37	F	00001F30	4	1716	1703 1706
RE38	F	00001F90	4	1746	1733 1736
RE39	F	00001FF0	4	1775	1762 1765
RE4	F	000012D0	4	695	682 685
RE40	F	00002050	4	1804	1791 1794
RE41	F	000020B0	4	1833	1820 1823
RE42	F	00002110	4	1867	1854 1857
RE43	F	00002170	4	1896	1883 1886
RE44	F	000021D0	4	1925	1912 1915
RE45	F	00002230	4	1954	1941 1944
RE46	F	00002290	4	1984	1971 1974
RE47	F	000022F0	4	2013	2000 2003
RE48	F	00002350	4	2043	2030 2033
RE49	F	000023B0	4	2072	2059 2062
RE5	F	00001330	4	724	711 714
RE50	F	00002410	4	2106	2093 2096
RE51	F	00002470	4	2136	2123 2126
RE52	F	000024D0	4	2166	2153 2156
RE53	F	00002530	4	2195	2182 2185
RE54	F	00002590	4	2225	2212 2215
RE55	F	000025F0	4	2254	2241 2244
RE56	F	00002650	4	2284	2271 2274
RE57	F	000026B0	4	2313	2300 2303
RE58	F	00002710	4	2348	2335 2338
RE59	F	00002770	4	2377	2364 2367
RE6	F	00001390	4	753	740 743
RE60	F	000027D0	4	2406	2393 2396
RE61	F	00002830	4	2435	2422 2425
RE62	F	00002890	4	2466	2453 2456
RE63	F	000028F0	4	2495	2482 2485
RE64	F	00002950	4	2524	2511 2514
RE65	F	000029B0	4	2553	2540 2543
RE66	F	00002A10	4	2584	2571 2574
RE67	F	00002A70	4	2613	2600 2603
RE68	F	00002AD0	4	2642	2629 2632
RE69	F	00002B30	4	2671	2658 2661
RE7	F	000013F0	4	782	769 772
RE70	F	00002B90	4	2702	2689 2692
RE71	F	00002BF0	4	2731	2718 2721
RE72	F	00002C50	4	2760	2747 2750
RE73	F	00002CB0	4	2789	2776 2779
RE74	F	00002D10	4	2824	2811 2814
RE75	F	00002D70	4	2853	2840 2843
RE76	F	00002DD0	4	2882	2869 2872
RE77	F	00002E30	4	2911	2898 2901
RE78	F	00002E90	4	2940	2927 2930
RE79	F	00002EF0	4	2969	2956 2959
RE8	F	00001450	4	811	798 801
RE80	F	00002F50	4	2998	2985 2988
RE81	F	00002FB0	4	3027	3014 3017
RE82	F	00003010	4	3058	3045 3048
RE83	F	00003070	4	3087	3074 3077
RE84	F	000030D0	4	3118	3105 3108
RE85	F	00003130	4	3147	3134 3137
RE86	F	00003190	4	3178	3165 3168

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SYMBOL		TYPE	VALUE	LENGTH	DEFN	REFERENCES			
RE87		F	000031F0	4	3207	3194	3197		
RE88		F	00003250	4	3237	3224	3227		
RE89		F	000032B0	4	3266	3253	3256		
RE9		F	000014B0	4	840	827	830		
RE90		F	00003310	4	3299	3286	3289		
RE91		F	00003370	4	3330	3317	3320		
RE92		F	000033D0	4	3362	3349	3352		
RE93		F	00003430	4	3394	3381	3384		
RE94		F	00003490	4	3428	3415	3418		
RE95		F	000034F0	4	3459	3446	3449		
RE96		F	00003550	4	3491	3478	3481		
RE97		F	000035B0	4	3523	3510	3513		
REA1		A	0000118C	4	598				
REA10		A	000014EC	4	859				
REA11		A	0000154C	4	888				
REA12		A	000015AC	4	923				
REA13		A	0000160C	4	952				
REA14		A	0000166C	4	981				
REA15		A	000016CC	4	1010				
REA16		A	0000172C	4	1039				
REA17		A	0000178C	4	1068				
REA18		A	000017EC	4	1147				
REA19		A	0000184C	4	1176				
REA2		A	000011EC	4	627				
REA20		A	000018AC	4	1206				
REA21		A	0000190C	4	1235				
REA22		A	0000196C	4	1265				
REA23		A	000019CC	4	1294				
REA24		A	00001A2C	4	1323				
REA25		A	00001A8C	4	1352				
REA26		A	00001AEC	4	1382				
REA27		A	00001B4C	4	1411				
REA28		A	00001BAC	4	1440				
REA29		A	00001C0C	4	1469				
REA3		A	0000124C	4	656				
REA30		A	00001C6C	4	1499				
REA31		A	00001CCC	4	1528				
REA32		A	00001D2C	4	1558				
REA33		A	00001D8C	4	1587				
REA34		A	00001DEC	4	1617				
REA35		A	00001E4C	4	1647				
REA36		A	00001EAC	4	1677				
REA37		A	00001F0C	4	1706				
REA38		A	00001F6C	4	1736				
REA39		A	00001FCC	4	1765				
REA4		A	000012AC	4	685				
REA40		A	0000202C	4	1794				
REA41		A	0000208C	4	1823				
REA42		A	000020EC	4	1857				
REA43		A	0000214C	4	1886				
REA44		A	000021AC	4	1915				
REA45		A	0000220C	4	1944				
REA46		A	0000226C	4	1974				
REA47		A	000022CC	4	2003				
REA48		A	0000232C	4	2033				
REA49		A	0000238C	4	2062				

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
REA5	A	0000130C	4	714	
REA50	A	000023EC	4	2096	
REA51	A	0000244C	4	2126	
REA52	A	000024AC	4	2156	
REA53	A	0000250C	4	2185	
REA54	A	0000256C	4	2215	
REA55	A	000025CC	4	2244	
REA56	A	0000262C	4	2274	
REA57	A	0000268C	4	2303	
REA58	A	000026EC	4	2338	
REA59	A	0000274C	4	2367	
REA6	A	0000136C	4	743	
REA60	A	000027AC	4	2396	
REA61	A	0000280C	4	2425	
REA62	A	0000286C	4	2456	
REA63	A	000028CC	4	2485	
REA64	A	0000292C	4	2514	
REA65	A	0000298C	4	2543	
REA66	A	000029EC	4	2574	
REA67	A	00002A4C	4	2603	
REA68	A	00002AAC	4	2632	
REA69	A	00002B0C	4	2661	
REA7	A	000013CC	4	772	
REA70	A	00002B6C	4	2692	
REA71	A	00002BCC	4	2721	
REA72	A	00002C2C	4	2750	
REA73	A	00002C8C	4	2779	
REA74	A	00002CEC	4	2814	
REA75	A	00002D4C	4	2843	
REA76	A	00002DAC	4	2872	
REA77	A	00002E0C	4	2901	
REA78	A	00002E6C	4	2930	
REA79	A	00002ECC	4	2959	
REA8	A	0000142C	4	801	
REA80	A	00002F2C	4	2988	
REA81	A	00002F8C	4	3017	
REA82	A	00002FEC	4	3048	
REA83	A	0000304C	4	3077	
REA84	A	000030AC	4	3108	
REA85	A	0000310C	4	3137	
REA86	A	0000316C	4	3168	
REA87	A	000031CC	4	3197	
REA88	A	0000322C	4	3227	
REA89	A	0000328C	4	3256	
REA9	A	0000148C	4	830	
REA90	A	000032EC	4	3289	
REA91	A	0000334C	4	3320	
REA92	A	000033AC	4	3352	
REA93	A	0000340C	4	3384	
REA94	A	0000346C	4	3418	
REA95	A	000034CC	4	3449	
REA96	A	0000352C	4	3481	
REA97	A	0000358C	4	3513	
READDR	A	0000001C	4	437	131
REG2LOW	U	000000DD	1	339	
REG2PATT	U	AABBCCDD	1	338	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES	
RELEN	A	00000018	4	436		
RPTDWSAV	D	000003C8	8	267	256	258
RPTERROR	I	000003A2	4	251	182	223
RPTSAVE	F	000003C0	4	264	251	261
RPTSVR5	F	000003C4	4	265	252	260
SVOLDPSW	U	00000140	0	55		
T1	A	00001170	4	587	3539	
T10	A	000014D0	4	848	3548	
T11	A	00001530	4	877	3549	
T12	A	00001590	4	912	3550	
T13	A	000015F0	4	941	3551	
T14	A	00001650	4	970	3552	
T15	A	000016B0	4	999	3553	
T16	A	00001710	4	1028	3554	
T17	A	00001770	4	1057	3555	
T18	A	000017D0	4	1136	3556	
T19	A	00001830	4	1165	3557	
T2	A	000011D0	4	616	3540	
T20	A	00001890	4	1195	3558	
T21	A	000018F0	4	1224	3559	
T22	A	00001950	4	1254	3560	
T23	A	000019B0	4	1283	3561	
T24	A	00001A10	4	1312	3562	
T25	A	00001A70	4	1341	3563	
T26	A	00001AD0	4	1371	3564	
T27	A	00001B30	4	1400	3565	
T28	A	00001B90	4	1429	3566	
T29	A	00001BF0	4	1458	3567	
T3	A	00001230	4	645	3541	
T30	A	00001C50	4	1488	3568	
T31	A	00001CB0	4	1517	3569	
T32	A	00001D10	4	1547	3570	
T33	A	00001D70	4	1576	3571	
T34	A	00001DD0	4	1606	3572	
T35	A	00001E30	4	1636	3573	
T36	A	00001E90	4	1666	3574	
T37	A	00001EF0	4	1695	3575	
T38	A	00001F50	4	1725	3576	
T39	A	00001FB0	4	1754	3577	
T4	A	00001290	4	674	3542	
T40	A	00002010	4	1783	3578	
T41	A	00002070	4	1812	3579	
T42	A	000020D0	4	1846	3580	
T43	A	00002130	4	1875	3581	
T44	A	00002190	4	1904	3582	
T45	A	000021F0	4	1933	3583	
T46	A	00002250	4	1963	3584	
T47	A	000022B0	4	1992	3585	
T48	A	00002310	4	2022	3586	
T49	A	00002370	4	2051	3587	
T5	A	000012F0	4	703	3543	
T50	A	000023D0	4	2085	3588	
T51	A	00002430	4	2115	3589	
T52	A	00002490	4	2145	3590	
T53	A	000024F0	4	2174	3591	
T54	A	00002550	4	2204	3592	

ASMA Ver. 0.7.0		zvector- e6- 16- VSRP- VPSOP (Zvector E6 VRI- g)										02 Jun 2024 16:00:38										Page	85
SYMBOL		TYPE	VALUE	LENGTH	DEFN	REFERENCES																	
T55		A	000025B0	4	2233	3593																	
T56		A	00002610	4	2263	3594																	
T57		A	00002670	4	2292	3595																	
T58		A	000026D0	4	2327	3596																	
T59		A	00002730	4	2356	3597																	
T6		A	00001350	4	732	3544																	
T60		A	00002790	4	2385	3598																	
T61		A	000027F0	4	2414	3599																	
T62		A	00002850	4	2445	3600																	
T63		A	000028B0	4	2474	3601																	
T64		A	00002910	4	2503	3602																	
T65		A	00002970	4	2532	3603																	
T66		A	000029D0	4	2563	3604																	
T67		A	00002A30	4	2592	3605																	
T68		A	00002A90	4	2621	3606																	
T69		A	00002AF0	4	2650	3607																	
T7		A	000013B0	4	761	3545																	
T70		A	00002B50	4	2681	3608																	
T71		A	00002BB0	4	2710	3609																	
T72		A	00002C10	4	2739	3610																	
T73		A	00002C70	4	2768	3611																	
T74		A	00002CD0	4	2803	3612																	
T75		A	00002D30	4	2832	3613																	
T76		A	00002D90	4	2861	3614																	
T77		A	00002DF0	4	2890	3615																	
T78		A	00002E50	4	2919	3616																	
T79		A	00002EB0	4	2948	3617																	
T8		A	00001410	4	790	3546																	
T80		A	00002F10	4	2977	3618																	
T81		A	00002F70	4	3006	3619																	
T82		A	00002FD0	4	3037	3620																	
T83		A	00003030	4	3066	3621																	
T84		A	00003090	4	3097	3622																	
T85		A	000030F0	4	3126	3623																	
T86		A	00003150	4	3157	3624																	
T87		A	000031B0	4	3186	3625																	
T88		A	00003210	4	3216	3626																	
T89		A	00003270	4	3245	3627																	
T9		A	00001470	4	819	3547																	
T90		A	000032D0	4	3278	3628																	
T91		A	00003330	4	3309	3629																	
T92		A	00003390	4	3341	3630																	
T93		A	000033F0	4	3373	3631																	
T94		A	00003450	4	3407	3632																	
T95		A	000034B0	4	3438	3633																	
T96		A	00003510	4	3470	3634																	
T97		A	00003570	4	3502	3635																	
TESTCC		I	0000026A	4	138	128																	
TESTREST		U	00000252	1	130	147																	
TNUM		H	00000004	2	424	158	192																
TSUB		A	00000000	4	423	123																	
TTABLE		F	000035D8	4	3538																		
V0		U	00000000	1	3668																		
V1		U	00000001	1	3669	122	603	604	632	633	661	662	690	691	719	720	748	749					
						777	778	806	807	835	836	864	865	893	894	928	929	957					
						958	986	987	1015	1016	1044	1045	1073	1074	1152	1153	1181	1182					

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
					1211 1212 1240 1241 1270 1271 1299 1300 1328 1329 1357 1358 1387
					1388 1416 1417 1445 1446 1474 1475 1504 1505 1533 1534 1563 1564
					1592 1593 1622 1623 1652 1653 1682 1683 1711 1712 1741 1742 1770
					1771 1799 1800 1828 1829 1862 1863 1891 1892 1920 1921 1949 1950
					1979 1980 2008 2009 2038 2039 2067 2068 2101 2102 2131 2132 2161
					2162 2190 2191 2220 2221 2249 2250 2279 2280 2308 2309 2343 2344
					2372 2373 2401 2402 2430 2431 2461 2462 2490 2491 2519 2520 2548
					2549 2579 2580 2608 2609 2637 2638 2666 2667 2697 2698 2726 2727
					2755 2756 2784 2785 2819 2820 2848 2849 2877 2878 2906 2907 2935
					2936 2964 2965 2993 2994 3022 3023 3053 3054 3082 3083 3113 3114
					3142 3143 3173 3174 3202 3203 3232 3233 3261 3262 3294 3295 3325
					3326 3357 3358 3389 3390 3423 3424 3454 3455 3486 3487 3518 3519
V10	U	0000000A	1	3678	
V11	U	0000000B	1	3679	
V12	U	0000000C	1	3680	
V13	U	0000000D	1	3681	
V14	U	0000000E	1	3682	
V15	U	0000000F	1	3683	
V16	U	00000010	1	3684	
V17	U	00000011	1	3685	
V18	U	00000012	1	3686	
V19	U	00000013	1	3687	
V1FUDGE	X	00001130	16	412	122
V1INPUT	C	00001140	16	413	
V1OUTPUT	X	00001110	16	410	132 604 633 662 691 720 749 778 807 836 865 894 929
					958 987 1016 1045 1074 1153 1182 1212 1241 1271 1300 1329 1358
					1388 1417 1446 1475 1505 1534 1564 1593 1623 1653 1683 1712 1742
					1771 1800 1829 1863 1892 1921 1950 1980 2009 2039 2068 2102 2132
					2162 2191 2221 2250 2280 2309 2344 2373 2402 2431 2462 2491 2520
					2549 2580 2609 2638 2667 2698 2727 2756 2785 2820 2849 2878 2907
					2936 2965 2994 3023 3054 3083 3114 3143 3174 3203 3233 3262 3295
					3326 3358 3390 3424 3455 3487 3519
V2	U	00000002	1	3670	602 603 631 632 660 661 689 690 718 719 747 748 776
					777 805 806 834 835 863 864 892 893 927 928 956 957
					985 986 1014 1015 1043 1044 1072 1073 1151 1152 1180 1181 1210
					1211 1239 1240 1269 1270 1298 1299 1327 1328 1356 1357 1386 1387
					1415 1416 1444 1445 1473 1474 1503 1504 1532 1533 1562 1563 1591
					1592 1621 1622 1651 1652 1681 1682 1710 1711 1740 1741 1769 1770
					1798 1799 1827 1828 1861 1862 1890 1891 1919 1920 1948 1949 1978
					1979 2007 2008 2037 2038 2066 2067 2100 2101 2130 2131 2160 2161
					2189 2190 2219 2220 2248 2249 2278 2279 2307 2308 2342 2343 2371
					2372 2400 2401 2429 2430 2460 2461 2489 2490 2518 2519 2547 2548
					2578 2579 2607 2608 2636 2637 2665 2666 2696 2697 2725 2726 2754
					2755 2783 2784 2818 2819 2847 2848 2876 2877 2905 2906 2934 2935
					2963 2964 2992 2993 3021 3022 3052 3053 3081 3082 3112 3113 3141
					3142 3172 3173 3201 3202 3231 3232 3260 3261 3293 3294 3324 3325
					3356 3357 3388 3389 3422 3423 3453 3454 3485 3486 3517 3518
V20	U	00000014	1	3688	
V21	U	00000015	1	3689	
V22	U	00000016	1	3690	
V23	U	00000017	1	3691	
V24	U	00000018	1	3692	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
V29	U	0000001D	1	3697	
V2VALUE	A	0000000C	4	432	
V2_1	A	0000117C	4	595	601
V2_10	A	000014DC	4	856	862
V2_11	A	0000153C	4	885	891
V2_12	A	0000159C	4	920	926
V2_13	A	000015FC	4	949	955
V2_14	A	0000165C	4	978	984
V2_15	A	000016BC	4	1007	1013
V2_16	A	0000171C	4	1036	1042
V2_17	A	0000177C	4	1065	1071
V2_18	A	000017DC	4	1144	1150
V2_19	A	0000183C	4	1173	1179
V2_2	A	000011DC	4	624	630
V2_20	A	0000189C	4	1203	1209
V2_21	A	000018FC	4	1232	1238
V2_22	A	0000195C	4	1262	1268
V2_23	A	000019BC	4	1291	1297
V2_24	A	00001A1C	4	1320	1326
V2_25	A	00001A7C	4	1349	1355
V2_26	A	00001ADC	4	1379	1385
V2_27	A	00001B3C	4	1408	1414
V2_28	A	00001B9C	4	1437	1443
V2_29	A	00001BFC	4	1466	1472
V2_3	A	0000123C	4	653	659
V2_30	A	00001C5C	4	1496	1502
V2_31	A	00001CBC	4	1525	1531
V2_32	A	00001D1C	4	1555	1561
V2_33	A	00001D7C	4	1584	1590
V2_34	A	00001DDC	4	1614	1620
V2_35	A	00001E3C	4	1644	1650
V2_36	A	00001E9C	4	1674	1680
V2_37	A	00001EFC	4	1703	1709
V2_38	A	00001F5C	4	1733	1739
V2_39	A	00001FBC	4	1762	1768
V2_4	A	0000129C	4	682	688
V2_40	A	0000201C	4	1791	1797
V2_41	A	0000207C	4	1820	1826
V2_42	A	000020DC	4	1854	1860
V2_43	A	0000213C	4	1883	1889
V2_44	A	0000219C	4	1912	1918
V2_45	A	000021FC	4	1941	1947
V2_46	A	0000225C	4	1971	1977
V2_47	A	000022BC	4	2000	2006
V2_48	A	0000231C	4	2030	2036
V2_49	A	0000237C	4	2059	2065
V2_5	A	000012FC	4	711	717
V2_50	A	000023DC	4	2093	2099
V2_51	A	0000243C	4	2123	2129
V2_52	A	0000249C	4	2153	2159
V2_53	A	000024FC	4	2182	2188
V2_54	A	0000255C	4	2212	2218
V2_55	A	000025BC	4	2241	2247
V2_56	A	0000261C	4	2271	2277
V2_57	A	0000267C	4	2300	2306
V2_58	A	000026DC	4	2335	2341

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
V2_59	A	0000273C	4	2364	2370
V2_6	A	0000135C	4	740	746
V2_60	A	0000279C	4	2393	2399
V2_61	A	000027FC	4	2422	2428
V2_62	A	0000285C	4	2453	2459
V2_63	A	000028BC	4	2482	2488
V2_64	A	0000291C	4	2511	2517
V2_65	A	0000297C	4	2540	2546
V2_66	A	000029DC	4	2571	2577
V2_67	A	00002A3C	4	2600	2606
V2_68	A	00002A9C	4	2629	2635
V2_69	A	00002AFC	4	2658	2664
V2_7	A	000013BC	4	769	775
V2_70	A	00002B5C	4	2689	2695
V2_71	A	00002BBC	4	2718	2724
V2_72	A	00002C1C	4	2747	2753
V2_73	A	00002C7C	4	2776	2782
V2_74	A	00002CDC	4	2811	2817
V2_75	A	00002D3C	4	2840	2846
V2_76	A	00002D9C	4	2869	2875
V2_77	A	00002DFC	4	2898	2904
V2_78	A	00002E5C	4	2927	2933
V2_79	A	00002EBC	4	2956	2962
V2_8	A	0000141C	4	798	804
V2_80	A	00002F1C	4	2985	2991
V2_81	A	00002F7C	4	3014	3020
V2_82	A	00002FDC	4	3045	3051
V2_83	A	0000303C	4	3074	3080
V2_84	A	0000309C	4	3105	3111
V2_85	A	000030FC	4	3134	3140
V2_86	A	0000315C	4	3165	3171
V2_87	A	000031BC	4	3194	3200
V2_88	A	0000321C	4	3224	3230
V2_89	A	0000327C	4	3253	3259
V2_9	A	0000147C	4	827	833
V2_90	A	000032DC	4	3286	3292
V2_91	A	0000333C	4	3317	3323
V2_92	A	0000339C	4	3349	3355
V2_93	A	000033FC	4	3381	3387
V2_94	A	0000345C	4	3415	3421
V2_95	A	000034BC	4	3446	3452
V2_96	A	0000351C	4	3478	3484
V2_97	A	0000357C	4	3510	3516
V3	U	00000003	1	3671	
V30	U	0000001E	1	3698	
V31	U	0000001F	1	3699	
V4	U	00000004	1	3672	
V5	U	00000005	1	3673	
V6	U	00000006	1	3674	
V7	U	00000007	1	3675	
V8	U	00000008	1	3676	
V9	U	00000009	1	3677	
X1	F	00001190	4	600	587
X10	F	000014F0	4	861	848
X11	F	00001550	4	890	877
X12	F	000015B0	4	925	912

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
X13	F	00001610	4	954	941
X14	F	00001670	4	983	970
X15	F	000016D0	4	1012	999
X16	F	00001730	4	1041	1028
X17	F	00001790	4	1070	1057
X18	F	000017F0	4	1149	1136
X19	F	00001850	4	1178	1165
X2	F	000011F0	4	629	616
X20	F	000018B0	4	1208	1195
X21	F	00001910	4	1237	1224
X22	F	00001970	4	1267	1254
X23	F	000019D0	4	1296	1283
X24	F	00001A30	4	1325	1312
X25	F	00001A90	4	1354	1341
X26	F	00001AF0	4	1384	1371
X27	F	00001B50	4	1413	1400
X28	F	00001BB0	4	1442	1429
X29	F	00001C10	4	1471	1458
X3	F	00001250	4	658	645
X30	F	00001C70	4	1501	1488
X31	F	00001CD0	4	1530	1517
X32	F	00001D30	4	1560	1547
X33	F	00001D90	4	1589	1576
X34	F	00001DF0	4	1619	1606
X35	F	00001E50	4	1649	1636
X36	F	00001EB0	4	1679	1666
X37	F	00001F10	4	1708	1695
X38	F	00001F70	4	1738	1725
X39	F	00001FD0	4	1767	1754
X4	F	000012B0	4	687	674
X40	F	00002030	4	1796	1783
X41	F	00002090	4	1825	1812
X42	F	000020F0	4	1859	1846
X43	F	00002150	4	1888	1875
X44	F	000021B0	4	1917	1904
X45	F	00002210	4	1946	1933
X46	F	00002270	4	1976	1963
X47	F	000022D0	4	2005	1992
X48	F	00002330	4	2035	2022
X49	F	00002390	4	2064	2051
X5	F	00001310	4	716	703
X50	F	000023F0	4	2098	2085
X51	F	00002450	4	2128	2115
X52	F	000024B0	4	2158	2145
X53	F	00002510	4	2187	2174
X54	F	00002570	4	2217	2204
X55	F	000025D0	4	2246	2233
X56	F	00002630	4	2276	2263
X57	F	00002690	4	2305	2292
X58	F	000026F0	4	2340	2327
X59	F	00002750	4	2369	2356
X6	F	00001370	4	745	732
X60	F	000027B0	4	2398	2385
X61	F	00002810	4	2427	2414
X62	F	00002870	4	2458	2445
X63	F	000028D0	4	2487	2474

DESC	SYMBOL	SIZE	POS	ADDR
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Entry: 0

Image	IMAGE	14188	0000- 376B	0000- 376B
Regi on		14188	0000- 376B	0000- 376B
CSECT	ZVE6TST	14188	0000- 376B	0000- 376B

STMT	FILE NAME
1	1
2	2
3	3
4	4
5	5
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84	84
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86	86
87	87
88	88
89	89
90	90
91	91
92	92
93	93
94	94
95	95
96	96
97	97
98	98
99	99
100	100

```
1 /devstor/dev/tests/zvector-e6-16-VSRP-VPS0P.asm
```

**** NO ERRORS FOUND ****