```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                        2 ************************************************************************
                                        3 *
                                        4 *            TRTE instruction tests
                                        5 *
                                        6 *        NOTE: This test is based the CLCL-et-al Test
                                        7 *              modified to only test the TRTE instruction.
                                        8 *
                                        9 *        James Wekel October 2022
                                       10 ************************************************************************


                                       12 ************************************************************************
                                       13 *
                                       14 *            TRTE basic instruction tests
                                       15 *
                                       16 ************************************************************
                                       17 *  This program tests proper functioning of the TRTE
                                       18 *  instructions. Specification exceptions are not tested.
                                       19 *
                                       20 *  PLEASE NOTE that the tests are very SIMPLE TESTS designed to catch
                                       21 *  obvious coding errors.  None of the tests are thorough.  They are
                                       22 *  NOT designed to test all aspects of any of the instructions.
                                       23 *
                                       24 ************************************************************************
                                       25 *
                                       26 *  Example Hercules Testcase:
                                       27 *
                                       28 *         *Testcase TRTE-01-basic (Test TRTE instruction)
                                       29 *
                                       30 *        # ------------------------------------------------------------
                                       31 *        # This tests only the basic function of the TRTE instruction.
                                       32 *        # Specification exceptions are NOT tested.
                                       33 *        # ------------------------------------------------------------
                                       34 *
                                       35 *        # ------------------------------------------------------------
                                       36 *        # need facility bit 26 enabled for:
                                       37 *        #       026_PARSING_ENHANCE      *Parsing-Enhancement Facility
                                       38 *        #       which is not included in archlvl 390
                                       39 *        #       so use backport to 370
                                       40 *        # ------------------------------------------------------------
                                       41 *
                                       42 *        archlvl  S/370
                                       43 *        facility enable  HERC_370_EXTENSION
                                       44 *        mainsize    16
                                       45 *        numcpu      1
                                       46 *
                                       47 *        sysclear
                                       48 *        loadcore    "TRTE-01-basic.core" 0x0
                                       49 *
                                       50 *        runtest     1
                                       51 *
                                       52 *        *Done
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                       53 *
                                       54 ************************************************************************
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                       56          PRINT OFF
                                     3437          PRINT ON

                                     3439 ********************************************************************************
                                     3440 *        SATK prolog stuff...
                                     3441 ********************************************************************************

                                     3443          ARCHLVL  SET=2,ZARCH=NO,MNOTE=NO
                                     3445+$AL      OPSYN AL
                                     3446+$ALR     OPSYN ALR
                                     3447+$B       OPSYN B
                                     3448+$BAS     OPSYN BAS
                                     3449+$BASR    OPSYN BASR
                                     3450+$BC      OPSYN BC
                                     3451+$BCTR    OPSYN BCTR
                                     3452+$BE      OPSYN BE
                                     3453+$BH      OPSYN BH
                                     3454+$BL      OPSYN BL
                                     3455+$BM      OPSYN BM
                                     3456+$BNE     OPSYN BNE
                                     3457+$BNH     OPSYN BNH
                                     3458+$BNL     OPSYN BNL
                                     3459+$BNM     OPSYN BNM
                                     3460+$BNO     OPSYN BNO
                                     3461+$BNP     OPSYN BNP
                                     3462+$BNZ     OPSYN BNZ
                                     3463+$BO      OPSYN BO
                                     3464+$BP      OPSYN BP
                                     3465+$BXLE    OPSYN BXLE
                                     3466+$BZ      OPSYN BZ
                                     3467+$CH      OPSYN CH
                                     3468+$L       OPSYN L
                                     3469+$LH      OPSYN LH
                                     3470+$LM      OPSYN LM
                                     3471+$LPSW    OPSYN LPSW
                                     3472+$LR      OPSYN LR
                                     3473+$LTR     OPSYN LTR
                                     3474+$NR      OPSYN NR
                                     3475+$SL      OPSYN SL
                                     3476+$SLR     OPSYN SLR
                                     3477+$SR      OPSYN SR
                                     3478+$ST      OPSYN ST
                                     3479+$STM     OPSYN STM
                                     3480+$X       OPSYN X
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             3482 ********************************************************************************
                                             3483 *        Initiate the TRTE1TST CSECT in the CODE region
                                             3484 *        with the location counter at 0
                                             3485 ********************************************************************************

                                             3487 TRTE1TST ASALOAD  REGION=CODE
                          000000  0E3F4D     3488+TRTE1TST START 0,CODE
000000  000A0000 00000008                    3490+         PSW   0,0,2,0,X'008'      64-bit Restart ISR Trap New PSW
000008                    000008  000058     3491+         ORG   TRTE1TST+X'058'
000058  000A0000 00000018                    3493+         PSW   0,0,2,0,X'018'      64-bit External ISR Trap New PSW
000060  000A0000 00000020                    3494+         PSW   0,0,2,0,X'020'      64-bit Supervisor Call ISR Trap New PSW
000068  000A0000 00000028                    3495+         PSW   0,0,2,0,X'028'      64-bit Program ISR Trap New PSW
000070  000A0000 00000030                    3496+         PSW   0,0,2,0,X'030'      64-bit Machine Check Trap New PSW
000078  000A0000 00000038                    3497+         PSW   0,0,2,0,X'038'      64-bit Input/Output Trap New PSW
000080                    000080  000200     3498+         ORG   TRTE1TST+512



                                             3500 ********************************************************************************
                                             3501 *        Create IPL (restart) PSW
                                             3502 ********************************************************************************

                                             3504          ASAIPL   IA=BEGIN
                          000000  0E3F4D     3505+TRTE1TST CSECT
000200                    000200  000000     3506+         ORG   TRTE1TST
000000  00080000 00000200                    3507+         PSW   0,0,0,0,BEGIN,24
000008                    000008  000200     3508+         ORG   TRTE1TST+512     Reset CSECT to end of assigned storage area
                          000000  0E3F4D     3509+TRTE1TST CSECT
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                      3511 ************************************************************************
                                      3512 *               The actual "TRTE1TST" program itself...
                                      3513 ************************************************************************
                                      3514 *
                                      3515 *  Architecture Mode: 370
                                      3516 *  Register Usage:
                                      3517 *
                                      3518 *   R0       (work)
                                      3519 *   R1       TRTE - Function-Code Table Address
                                      3520 *   R2       TRTE - First-Operand Address
                                      3521 *   R3       TRTE - First-Operand Length
                                      3522 *   R4       TRTE - Function-Code
                                      3523 *   R5       Testing control table - base current entry
                                      3524 *   R6-R7    (work)
                                      3525 *   R8       First base register
                                      3526 *   R9       Second base register
                                      3527 *   R10-R13  (work)
                                      3528 *   R14      Subroutine call
                                      3529 *   R15      Secondary Subroutine call or work
                                      3530 *
                                      3531 ************************************************************************

000200                      000000    3533          USING  ASA,R0          Low core addressability
000200                      000200    3534          USING  BEGIN,R8        FIRST Base Register
000200                      001200    3535          USING  BEGIN+4096,R9   SECOND Base Register

000200  0580                         3537 BEGIN    BALR  R8,0             Initalize FIRST base register
000202  0680                         3538          BCTR  R8,0             Initalize FIRST base register
000204  0680                         3539          BCTR  R8,0             Initalize FIRST base register

000206  4190 8800           000800   3541          LA    R9,2048(,R8)     Initalize SECOND base register
00020A  4190 9800           000800   3542          LA    R9,2048(,R9)     Initalize SECOND base register
                                      3543 *
                                      3544 **       Run the tests...
                                      3545 *
00020E  45E0 8302           000502   3546          BAL   R14,TEST01       Test TRTE   instruction
                                      3547 *
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3549 ************************************************************************
                                     3550 *         Test for normal or unexpected test completion...
                                     3551 ************************************************************************

000212  95FC 8200                   000400  3553          CLI   TESTNUM,X'FC'    Did we end on expected test?
000216  4770 83D8                   0005D8  3554          BNE   FAILTEST          No?! Then FAIL the test!

00021A  9503 8201                   000401  3556          CLI   SUBTEST,X'03'    Did we end on expected SUB-test?
00021E  4770 83D8                   0005D8  3557          BNE   FAILTEST          No?! Then FAIL the test!

000222  47F0 83C8                   0005C8  3559          B     EOJ               Yes, then normal completion!




                                     3561 ************************************************************************
                                     3562 *        Fixed test storage locations ...
                                     3563 ************************************************************************



000226                       000226  000400  3565          ORG   BEGIN+X'200'
                                     3566
000400                               3567 TESTADDR DS    0D                 Where test/subtest numbers will go
000400  99                           3568 TESTNUM  DC    X'99'      Test number of active test
000401  99                           3569 SUBTEST  DC    X'99'      Active test sub-test number

000402                       000402  000502  3571          ORG   *+X'100'
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3573 ********************************************************************************
                                     3574 *        TEST01                    Test TRTE instruction
                                     3575 ********************************************************************************

000502  9201 8200                   000400  3577 TEST01   MVI   TESTNUM,X'01'
                                     3578
000506  4150 83EC                   0005EC  3579          LA    R5,TRTECTL          Point R5 --> testing control table
00050A                       000000          3580          USING TRTETEST,R5         What each table entry looks like
                                     3581
                             00050A  000001  3582 TST1LOOP EQU   *
00050A  4360 5000                   000000  3583          IC    R6,TNUM             Set test number
00050E  4260 8200                   000400  3584          STC   R6,TESTNUM
                                     3585 *
                                     3586 **       Initialize operand data  (move data to testing address)
                                     3587 *
000512  58A0 5018                   000018  3588          L     R10,OP1WHERE          Where to move operand-1 data to
000516  58B0 5008                   000008  3589          L     R11,OP1LEN            operand-1 length
00051A  50B0 501C                   00001C  3590          ST    R11,OP1WLEN             and save for later
00051E  5860 5004                   000004  3591          L     R6,OP1DATA            Where op1 data is right now
000522  5870 5008                   000008  3592          L     R7,OP1LEN             How much of it there is
000526  0EA6                               3593          MVCL  R10,R6
                                     3594 *
000528  58A0 5014                   000014  3595          L     R10,OP2WHERE          Where to move operand-2 data to
00052C  58B0 5010                   000010  3596          L     R11,OP2LEN            How much of it there is
000530  5860 500C                   00000C  3597          L     R6,OP2DATA            Where op2 data is right now
000534  5870 5010                   000010  3598          L     R7,OP2LEN              How much of it there is
000538  0EA6                               3599          MVCL  R10,R6


                                     3601 **       Execute TRTE instruction and check for expected condition code

00053A  9814 5014           000014  3603          LM    R1,R4,OPSWHERE     get TRTE input
                                     3604
00053E  1B77                         3605          SR    R7,R7              get M3 bits for TRTE
000540  4370 5003           000003  3606          IC    R7,M3              (M3)
000544  4270 8356           000556  3607          STC   R7,TRTEMOD+2       DYNAMICALLY MODIFIED CODE
                                     3608
000548  58B0 5024           000024  3609          L     R11,FAILMASK       (failure CC)
00054C  89B0 0004           000004  3610          SLL   R11,4              (shift to BC instr CC position)
                                     3611
000550  9200 8201           000401  3612          MVI   SUBTEST,X'00'      (primary TRT)
000554  B9BF 0024                   3613 TRTEMOD  TRTE  R2,R4,0             Start with TRTE and m3=0
                                     3614
000558  9014 83A8           0005A8  3615          STM   R1,R4,SAVETRT      (save R1/R4 results)
00055C  44B0 839E           00059E  3616          EX    R11,TRTEBC          fail if...
000560  4710 8354           000554  3617          BC    B'0001',TRTEMOD     cc=3, not finished
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3619 **       Verify R2,R3,R4 contain (or still contain!) expected values
                                     3620
000564  98AC 5028                    000028  3621          LM    R10,R12,ENDREGS
                                     3622
000568  9201 8201                    000401  3623          MVI   SUBTEST,X'01'      (R2 result - op1 found addr)
00056C  152A                         000598  3624          CLR   R2,R10              R2 correct?
00056E  4770 8398                    000598  3625          BNE   TRTEFAIL           No, FAILTEST!
                                     3626
000572  9202 8201                    000401  3627          MVI   SUBTEST,X'02'      (R3 result - op1 remaining len)
000576  153B                         000598  3628          CLR   R3,R11              R3 correct
000578  4770 8398                    000598  3629          BNE   TRTEFAIL           No, FAILTEST!
                                     3630
00057C  9203 8201                    000401  3631          MVI   SUBTEST,X'03'      (R4 result - FC code)
000580  154C                         000598  3632          CLR   R4,R12              R4 correct
000582  4770 8398                    000598  3633          BNE   TRTEFAIL           No, FAILTEST!
                                     3634
000586  4150 5034                    000034  3635          LA    R5,TRTENEXT        Go on to next table entry
00058A  D503 83E8 5000       0005E8  000000  3636          CLC   =F'0',0(R5)        End of table?
000590  4770 830A                    00050A  3637          BNE   TST1LOOP           No, loop...
000594  47F0 839C                    00059C  3638          B     TRTEDONE            Done! (success!)


000598  41E0 83D8                    0005D8  3640 TRTEFAIL LA    R14,FAILTEST       Unexpected results!
00059C  07FE                         000598  3641 TRTEDONE BR    R14                Return to caller or FAILTEST


00059E  4700 8398                    000598  3643 TRTEBC   BC    0,TRTEFAIL         (fail if unexpected condition code)


0005A8  00000000 00000000                    3645 SAVETRT  DC    4D'0'              (saved R1/R4 from TRT results)


0005C8                               000200  3647          DROP  R5
0005C8                               000200  3648          DROP  R15
0005C8                               000200  3649          USING BEGIN,R8
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     3651 ********************************************************************************
                                     3652 *        Normal completion or Abnormal termination PSWs
                                     3653 ********************************************************************************




                                     3655 EOJ      DWAITEND LOAD=YES          Normal completion
0005C8                               3657+EOJ      DS    0H
0005C8  8200 83D0                0005D0  3658+         LPSW  DWAT0008
0005D0  000A0000 00000000                3659+DWAT0008 PSW   0,0,2,0,X'000000'




                                     3661 FAILTEST DWAIT LOAD=YES,CODE=BAD    Abnormal termination
0005D8                               3662+FAILTEST DS    0H
0005D8  8200 83E0                0005E0  3663+         LPSW  DWAT0009
0005E0  000A0000 00010BAD                3664+DWAT0009 PSW   0,0,2,0,X'010BAD'




                                     3666 ********************************************************************************
                                     3667 *        Working Storage
                                     3668 ********************************************************************************


0005E8                               3670          LTORG ,                  Literals pool
0005E8  00000000                     3671                =F'0'

                         000400  000001  3673 K        EQU   1024             One KB
                         001000  000001  3674 PAGE     EQU   (4*K)            Size of one page
                         010000  000001  3675 K64      EQU   (64*K)           64 KB
                         100000  000001  3676 MB       EQU   (K*K)             1 MB
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                            000000  0E3F4D  3678 TRTE1TST CSECT ,

                                            3680 ********************************************************************
                                            3681 *        TRTETEST DSECT
                                            3682 ********************************************************************

                                            3684 TRTETEST DSECT ,
000000  00                                  3685 TNUM     DC    X'00'          TRTE table Number
000001  00                                  3686          DC    X'00'
000002  00                                  3687          DC    X'00'
000003  00                                  3688 M3       DC    X'00'          M3 byte stored into TRTE instruction

000004  00000000                            3690 OP1DATA  DC    A(0)           Pointer to Operand-1 data
000008  00000000                            3691 OP1LEN   DC    F'0'           How much data is there - 1
00000C  00000000                            3692 OP2DATA  DC    A(0)           Pointer to FC table data
000010  00000000                            3693 OP2LEN   DC    F'0'           How much data is there - FC Table

                            000014  000001  3695 OPSWHERE EQU   *
000014  00000000                            3696 OP2WHERE DC    A(0)           Where FC Table  data should be placed
000018  00000000                            3697 OP1WHERE DC    A(0)           Where Operand-1 data should be placed
00001C  00000000                            3698 OP1WLEN  DC    F'0'           How much data is there - 1
000020  00000000                            3699          DC    A(0)           pollute - found FC

000024  00000000                            3701 FAILMASK DC    A(0)           Failure Branch on Condition mask

                                            3703 *                             Ending register values
000028  00000000                            3704 ENDREGS  DC    A(0)              Operand 1 address
00002C  00000000                            3705          DC    A(0)              Operand 1 length
000030  00000000                            3706          DC    A(0)              Function Code

                            000034  000001  3708 TRTENEXT EQU   *              Start of next table entry...

                            BBCCDD  000001  3710 REG2PATT EQU   X'AABBCCDD'    Polluted Register pattern
                            0000DD  000001  3711 REG2LOW  EQU         X'DD'    (last byte above)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                            000000  0E3F4D  3713 TRTE1TST CSECT ,


                                            3715 ********************************************************************
                                            3716 *        TRTE Testing Control tables   (ref: TRTETEST DSECT)
                                            3717 ********************************************************************
                                            3718          PRINT DATA
0005EC                                      3719 TRTECTL  DC    0A(0)    start of table


                                            3721 ********************************************************************
                                            3722 *        tests with   M3: A=0,F=0,L=0, reserved=0    (0)
                                            3723 *                            FC Table = 1 byte
                                            3724 ********************************************************************


0005EC                                      3726 M0T1     DS    0F
0005EC  01                                  3727          DC    X'01'                      Test Num
0005ED  0000                                3728          DC    X'00',X'00'
0005EF  00                                  3729          DC    X'00'                      M3: A=0,F=0,L=0
0005F0  0000142C 00000001                   3730          DC    A(TRTOP10),A(001)          Source - Op 1 & length
0005F8  0000312C 00000100                   3731          DC    A(TRTOP20),A(256)          Source - FC Table & length
                                            3732 *                                         Target -
000600  00110000 00210000                   3733          DC    A(1*MB+(1*K64)),A(2*MB+(1*K64)),A(0)  FC, Op1, Op1L
000608  00000000
00060C  AABBCCDD                            3734          DC    A(REG2PATT)
000610  00000007                            3735          DC    A(7) CC0
000614  00210001 00000000                   3736          DC    A(2*MB+(1*K64)+001),A(000),A(0)
00061C  00000000


000620                                      3738 M0T2     DS    0F
000620  02                                  3739          DC    X'02'                      Test Num
000621  0000                                3740          DC    X'00',X'00'
000623  00                                  3741          DC    X'00'                      M3: A=0,F=0,L=0
000624  0000142C 00000002                   3742          DC    A(TRTOP10),A(002)          Source - Op 1 & length
00062C  0000312C 00000100                   3743          DC    A(TRTOP20),A(256)          Source - FC Table & length
                                            3744 *                                         Target -
000634  00120000 00220000                   3745          DC    A(MB+(2*K64)),A(2*MB+(2*K64)),A(0)  FC, Op1, Op1L
00063C  00000000
000640  AABBCCDD                            3746          DC    A(REG2PATT)
000644  00000007                            3747          DC    A(7) CC0
000648  00220002 00000000                   3748          DC    A(2*MB+(2*K64)+002),A(000),A(0)
000650  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000654                                3750 M0T3     DS    0F
000654  03                            3751          DC    X'03'                        Test Num
000655  0000                          3752          DC    X'00',X'00'
000657  00                            3753          DC    X'00'                        M3: A=0,F=0,L=0
000658  0000142C 00000004             3754          DC    A(TRTOP10),A(004)            Source - Op 1 & length
000660  0000312C 00000100             3755          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                      3756 *                                           Target -
000668  00130000 00230000             3757          DC    A(MB+(3*K64)),A(2*MB+(3*K64)),A(0)  FC, Op1, Op1L
000670  00000000
000674  AABBCCDD                      3758          DC    A(REG2PATT)
000678  00000007                      3759          DC    A(7) CC0
00067C  00230004 00000000             3760          DC    A(2*MB+(3*K64)+004),A(000),A(0)
000684  00000000



000688                                3762 M0T4     DS    0F
000688  04                            3763          DC    X'04'                        Test Num
000689  0000                          3764          DC    X'00',X'00'
00068B  00                            3765          DC    X'00'                        M3: A=0,F=0,L=0
00068C  0000142C 00000008             3766          DC    A(TRTOP10),A(008)            Source - Op 1 & length
000694  0000312C 00000100             3767          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                      3768 *                                           Target -
00069C  00140000 00240000             3769          DC    A(MB+(4*K64)),A(2*MB+(4*K64)),A(0)  FC, Op1, Op1L
0006A4  00000000
0006A8  AABBCCDD                      3770          DC    A(REG2PATT)
0006AC  00000007                      3771          DC    A(7) CC0
0006B0  00240008 00000000             3772          DC    A(2*MB+(4*K64)+008),A(000),A(0)
0006B8  00000000



0006BC                                3774 M0T5     DS    0F
0006BC  05                            3775          DC    X'05'                        Test Num
0006BD  0000                          3776          DC    X'00',X'00'
0006BF  00                            3777          DC    X'00'                        M3: A=0,F=0,L=0
0006C0  0000142C 00000100             3778          DC    A(TRTOP10),A(256)            Source - Op 1 & length
0006C8  0000312C 00000100             3779          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                      3780 *                                           Target -
0006D0  00150000 00250000             3781          DC    A(MB+(5*K64)),A(2*MB+(5*K64)),A(0)  FC, Op1, Op1L
0006D8  00000000
0006DC  AABBCCDD                      3782          DC    A(REG2PATT)
0006E0  00000007                      3783          DC    A(7) CC0
0006E4  00250100 00000000             3784          DC    A(2*MB+(5*K64)+256),A(000),A(0)
0006EC  00000000



0006F0                                3786 M0T6     DS    0F
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0006F0  06                                3787          DC    X'06'                          Test Num
0006F1  0000                              3788          DC    X'00',X'00'
0006F3  00                                3789          DC    X'00'                          M3: A=0,F=0,L=0
0006F4  0000152C 00000100                 3790          DC    A(TRTOP111),A(256)             Source - Op 1 & length
0006FC  0002322C 00000100                 3791          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                          3792 *                                             Target -
000704  0015FFE0 0025FFF4                 3793          DC    A(MB+(6*K64)-32),A(2*MB+(6*K64)-12),A(0)  FC, Op1, Op1L
00070C  00000000
000710  AABBCCDD                          3794          DC    A(REG2PATT)
000714  0000000A                          3795          DC    A(10) CC1 or CC3
000718  00260005 000000EF                 3796          DC    A(2*MB+(6*K64)-12+X'11'),A(256-X'11'),XL4'11'
000720  00000011



000724                                    3798 M0T7     DS    0F
000724  07                                3799          DC    X'07'                          Test Num
000725  0000                              3800          DC    X'00',X'00'
000727  00                                3801          DC    X'00'                          M3: A=0,F=0,L=0
000728  0000162C 00000100                 3802          DC    A(TRTOP1F0),A(256)             Source - Op 1 & length
000730  0002332C 00000100                 3803          DC    A(TRTOP2F0),A(256)             Source - FC Table & length
                                          3804 *                                             Target -
000738  00170000 0026FFF4                 3805          DC    A(MB+(7*K64)),A(2*MB+(7*K64)-12),A(0)   FC, Op1, Op1L
000740  00000000
000744  AABBCCDD                          3806          DC    A(REG2PATT)
000748  0000000A                          3807          DC    A(10) CC1 or CC3
00074C  002700F3 00000001                 3808          DC    A(2*MB+(7*K64)-12+255),A(256-255),XL4'F0'
000754  000000F0
                                          3809



000758                                    3811 M0T8     DS    0F
000758  08                                3812          DC    X'08'                          Test Num
000759  0000                              3813          DC    X'00',X'00'
00075B  00                                3814          DC    X'00'                          M3: A=0,F=0,L=0
00075C  0000152C 00000100                 3815          DC    A(TRTOP111),A(256)             Source - Op 1 & length
000764  0002322C 00000100                 3816          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                          3817 *                                             Target -
00076C  0017FFE0 00280000                 3818          DC    A(MB+(8*K64)-32),A(2*MB+(8*K64)),A(0)  FC, Op1, Op1L
000774  00000000
000778  AABBCCDD                          3819          DC    A(REG2PATT)
00077C  0000000B                          3820          DC    A(11) CC1
000780  00280011 000000EF                 3821          DC    A(2*MB+(8*K64)+X'11'),A(256-X'11'),XL4'11'
000788  00000011



00078C                                    3823 M0T9     DS    0F
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00078C  09                                3824          DC    X'09'                          Test Num
00078D  0000                              3825          DC    X'00',X'00'
00078F  00                                3826          DC    X'00'                          M3: A=0,F=0,L=0
000790  0000192C 00000800                 3827          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
000798  0000312C 00000100                 3828          DC    A(TRTOP20),A(256)              Source - FC Table & length
                                          3829 *                                             Target -
0007A0  00190000 00290000                 3830          DC    A(MB+(9*K64)),A(2*MB+(9*K64)),A(0)  FC, Op1, Op1L
0007A8  00000000
0007AC  AABBCCDD                          3831          DC    A(REG2PATT)
0007B0  00000007                          3832          DC    A(7) CC0
0007B4  00290800 00000000                 3833          DC    A(2*MB+(9*K64)+2048),A(000),A(0)
0007BC  00000000



0007C0                                    3835 M0T10    DS    0F
0007C0  0A                                3836          DC    X'0A'                          Test Num
0007C1  0000                              3837          DC    X'00',X'00'
0007C3  00                                3838          DC    X'00'                          M3: A=0,F=0,L=0
0007C4  0000212C 00000800                 3839          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
0007CC  0002322C 00000100                 3840          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                          3841 *                                             Target -
0007D4  001A0000 0029FF38                 3842          DC    A(MB+(10*K64)),A(2*MB+(10*K64)-200),A(0)  FC, Op1, Op1L
0007DC  00000000
0007E0  AABBCCDD                          3843          DC    A(REG2PATT)
0007E4  0000000A                          3844          DC    A(10) CC1 or CC3
0007E8  002A0339 000003FF                 3845          DC    A(2*MB+(10*K64)-200+(4*256)+1),A(1023),Xl4'11'
0007F0  00000011



0007F4                                    3847 M0T11    DS    0F
0007F4  0B                                3848          DC    X'0B'                          Test Num
0007F5  0000                              3849          DC    X'00',X'00'
0007F7  00                                3850          DC    X'00'                          M3: A=0,F=0,L=0
0007F8  0000292C 00000800                 3851          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
000800  0002332C 00000100                 3852          DC    A(TRTOP2F0),A(256)             Source - FC Table & length
                                          3853 *                                             Target -
000808  001AFFC0 002B0000                 3854          DC    A(MB+(11*K64)-64),A(2*MB+(11*K64)),A(0) FC, Op1, Op1L
000810  00000000
000814  AABBCCDD                          3855          DC    A(REG2PATT)
000818  0000000B                          3856          DC    A(11) CC1
00081C  002B07FF 00000001                 3857          DC    A(2*MB+(11*K64)+2048-1),A(1),Xl4'F0'
000824  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             3859 ************************************************************************
                                             3860 *        tests with   M3: A=0,F=1,L=0, reserved=0    (4)
                                             3861 *                              FC Table = 2 bytes
                                             3862 ************************************************************************


000828                                       3864 M4T1     DS    0F
000828  41                                   3865          DC    X'41'                       Test Num
000829  0000                                 3866          DC    X'00',X'00'
00082B  40                                   3867          DC    X'40'                       M3: A=0,F=1,L=0
00082C  0000142C 00000001                    3868          DC    A(TRTOP10),A(001)           Source - Op 1 & length
000834  0000312C 00000200                    3869          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             3870 *                                          Target -
00083C  00310000 00410000                    3871          DC    A(3*MB+(1*K64)),A(4*MB+(1*K64)),A(0)  FC, Op1, Op1L
000844  00000000
000848  AABBCCDD                             3872          DC    A(REG2PATT)
00084C  00000007                             3873          DC    A(7) CC0
000850  00410001 00000000                    3874          DC    A(4*MB+(1*K64)+001),A(000),A(0)
000858  00000000



00085C                                       3876 M4T2     DS    0F
00085C  42                                   3877          DC    X'42'                       Test Num
00085D  0000                                 3878          DC    X'00',X'00'
00085F  40                                   3879          DC    X'40'                       M3: A=0,F=1,L=0
000860  0000142C 00000002                    3880          DC    A(TRTOP10),A(002)           Source - Op 1 & length
000868  0000312C 00000200                    3881          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             3882 *                                          Target -
000870  00320000 00420000                    3883          DC    A(3*MB+(2*K64)),A(4*MB+(2*K64)),A(0)  FC, Op1, Op1L
000878  00000000
00087C  AABBCCDD                             3884          DC    A(REG2PATT)
000880  00000007                             3885          DC    A(7) CC0
000884  00420002 00000000                    3886          DC    A(4*MB+(2*K64)+002),A(000),A(0)
00088C  00000000



000890                                       3888 M4T3     DS    0F
000890  43                                   3889          DC    X'43'                       Test Num
000891  0000                                 3890          DC    X'00',X'00'
000893  40                                   3891          DC    X'40'                       M3: A=0,F=1,L=0
000894  0000142C 00000004                    3892          DC    A(TRTOP10),A(004)           Source - Op 1 & length
00089C  0000312C 00000200                    3893          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                             3894 *                                          Target -
0008A4  00340000 00440000                    3895          DC    A(3*MB+(4*K64)),A(4*MB+(4*K64)),A(0)  FC, Op1, Op1L
0008AC  00000000
0008B0  AABBCCDD                             3896          DC    A(REG2PATT)
0008B4  00000007                             3897          DC    A(7) CC0
0008B8  00440004 00000000                    3898          DC    A(4*MB+(4*K64)+004),A(000),A(0)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0008C0  00000000

0008C4                                    3900 M4T4     DS    0F
0008C4  44                                3901          DC    X'44'                       Test Num
0008C5  0000                              3902          DC    X'00',X'00'
0008C7  40                                3903          DC    X'40'                       M3: A=0,F=1,L=0
0008C8  0000142C 00000008                 3904          DC    A(TRTOP10),A(008)           Source - Op 1 & length
0008D0  0000312C 00000200                 3905          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                          3906 *                                          Target -
0008D8  00340000 00440000                 3907          DC    A(3*MB+(4*K64)),A(4*MB+(4*K64)),A(0)  FC, Op1, Op1L
0008E0  00000000
0008E4  AABBCCDD                          3908          DC    A(REG2PATT)
0008E8  00000007                          3909          DC    A(7) CC0
0008EC  00440008 00000000                 3910          DC    A(4*MB+(4*K64)+008),A(000),A(0)
0008F4  00000000

0008F8                                    3912 M4T5     DS    0F
0008F8  45                                3913          DC    X'45'                       Test Num
0008F9  0000                              3914          DC    X'00',X'00'
0008FB  40                                3915          DC    X'40'                       M3: A=0,F=1,L=0
0008FC  0000142C 00000100                 3916          DC    A(TRTOP10),A(256)           Source - Op 1 & length
000904  0000312C 00000200                 3917          DC    A(TRTOP20),A(512)           Source - FC Table & length
                                          3918 *                                          Target -
00090C  00350000 00450000                 3919          DC    A(3*MB+(5*K64)),A(4*MB+(5*K64)),A(0)  FC, Op1, Op1L
000914  00000000
000918  AABBCCDD                          3920          DC    A(REG2PATT)
00091C  00000007                          3921          DC    A(7) CC0
000920  00450100 00000000                 3922          DC    A(4*MB+(5*K64)+256),A(000),A(0)
000928  00000000

00092C                                    3924 M4T6     DS    0F
00092C  46                                3925          DC    X'46'                       Test Num
00092D  0000                              3926          DC    X'00',X'00'
00092F  40                                3927          DC    X'40'                       M3: A=0,F=1,L=0
000930  0000152C 00000100                 3928          DC    A(TRTOP111),A(256)          Source - Op 1 & length
000938  0002342C 00000200                 3929          DC    A(TRTOP411),A(512)          Source - FC Table & length
                                          3930 *                                          Target -
000940  0035FFE0 0045FFF4                 3931          DC    A(3*MB+(6*K64)-32),A(4*MB+(6*K64)-12),A(0) FC, Op1, Op1L
000948  00000000
00094C  AABBCCDD                          3932          DC    A(REG2PATT)
000950  0000000A                          3933          DC    A(10) CC1 or CC3
000954  00460005 000000EF                 3934          DC    A(4*MB+(6*K64)-12+X'11'),A(256-X'11'),XL4'11'
00095C  00000011
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000960                                3936 M4T7     DS    0F
000960  47                            3937          DC    X'47'                          Test Num
000961  0000                          3938          DC    X'00',X'00'
000963  40                            3939          DC    X'40'                          M3: A=0,F=1,L=0
000964  0000162C 00000100             3940          DC    A(TRTOP1F0),A(256)             Source - Op 1 & length
00096C  0002362C 00000200             3941          DC    A(TRTOP4F0),A(512)             Source - FC Table & length
                                      3942 *                                             Target -
000974  00370000 0046FFF4             3943          DC    A(3*MB+(7*K64)),A(4*MB+(7*K64)-12),A(0) FC, Op1, Op1L
00097C  00000000
000980  AABBCCDD                      3944          DC    A(REG2PATT)
000984  0000000A                      3945          DC    A(10) CC1 or CC3
000988  004700F3 00000001             3946          DC    A(4*MB+(7*K64)-12+255),A(256-255),XL4'F0'
000990  000000F0



000994                                3948 M4T8     DS    0F
000994  48                            3949          DC    X'48'                          Test Num
000995  0000                          3950          DC    X'00',X'00'
000997  40                            3951          DC    X'40'                          M3: A=0,F=1,L=0
000998  0000152C 00000100             3952          DC    A(TRTOP111),A(256)             Source - Op 1 & length
0009A0  0002342C 00000200             3953          DC    A(TRTOP411),A(512)             Source - FC Table & length
                                      3954 *                                             Target -
0009A8  0037FFE0 00480000             3955          DC    A(3*MB+(8*K64)-32),A(4*MB+(8*K64)),A(0) FC, Op1, Op1L
0009B0  00000000
0009B4  AABBCCDD                      3956          DC    A(REG2PATT)
0009B8  0000000B                      3957          DC    A(11) CC1
0009BC  00480011 000000EF             3958          DC    A(4*MB+(8*K64)+X'11'),A(256-X'11'),XL4'11'
0009C4  00000011



0009C8                                3960 M4T9     DS    0F
0009C8  49                            3961          DC    X'49'                          Test Num
0009C9  0000                          3962          DC    X'00',X'00'
0009CB  40                            3963          DC    X'40'                          M3: A=0,F=1,L=0
0009CC  0000192C 00000800             3964          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
0009D4  0000312C 00000200             3965          DC    A(TRTOP20),A(512)              Source - FC Table & length
                                      3966 *                                             Target -
0009DC  00390000 00490000             3967          DC    A(3*MB+(9*K64)),A(4*MB+(9*K64)),A(0) FC, Op1, Op1L
0009E4  00000000
0009E8  AABBCCDD                      3968          DC    A(REG2PATT)
0009EC  00000007                      3969          DC    A(7) CC0
0009F0  00490800 00000000             3970          DC    A(4*MB+(9*K64)+2048),A(000),A(0)
0009F8  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0009FC                                     3972 M4T10    DS    0F
0009FC  4A                                 3973          DC    X'4A'                          Test Num
0009FD  0000                               3974          DC    X'00',X'00'
0009FF  40                                 3975          DC    X'40'                          M3: A=0,F=1,L=0
000A00  0000212C 00000800                  3976          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
000A08  0002342C 00000200                  3977          DC    A(TRTOP411),A(512)             Source - FC Table & length
                                           3978 *                                             Target -
000A10  003A0000 0049FF38                  3979          DC    A(3*MB+(10*K64)),A(4*MB+(10*K64)-200),A(0) FC, Op1, Op1L
000A18  00000000
000A1C  AABBCCDD                           3980          DC    A(REG2PATT)
000A20  0000000A                           3981          DC    A(10) CC1 or CC3
000A24  004A0339 000003FF                  3982          DC    A(4*MB+(10*K64)-200+(4*256)+1),A(1023),XL4'11'
000A2C  00000011



000A30                                     3984 M4T11    DS    0F
000A30  4B                                 3985          DC    X'4B'                          Test Num
000A31  0000                               3986          DC    X'00',X'00'
000A33  40                                 3987          DC    X'40'                          M3: A=0,F=1,L=0
000A34  0000292C 00000800                  3988          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
000A3C  0002362C 00000200                  3989          DC    A(TRTOP4F0),A(512)             Source - FC Table & length
                                           3990 *                                             Target -
000A44  003AFFC0 004B0000                  3991          DC    A(3*MB+(11*K64)-64),A(4*MB+(11*K64)),A(0) FC, Op1, Op1L
000A4C  00000000
000A50  AABBCCDD                           3992          DC    A(REG2PATT)
000A54  0000000B                           3993          DC    A(11) CC1
000A58  004B07FF 00000001                  3994          DC    A(4*MB+(11*K64)+2048-1),A(1),XL4'F0'
000A60  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             3996 ************************************************************************
                                             3997 *        tests with   M3: A=1,F=0,L=0, reserved=0    (8)
                                             3998 *                     FC Table : SIZE: 65,536 (2 BYTE ARGUMENT)
                                             3999 *
                                             4000 *              Note: Op1 length must be a multiple of 2
                                             4001 ************************************************************************


000A64                                       4003 M8T1     DS    0F
000A64  81                                   4004          DC    X'81'                          Test Num
000A65  0000                                 4005          DC    X'00',X'00'
000A67  80                                   4006          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000A68  0000142C 00000002                    4007          DC    A(TRTOP10),A(002)              Source - Op 1 & length
000A70  0000312C 00010000                    4008          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                             4009 *                                             Target -
000A78  00510000 00610000                    4010          DC    A(5*MB+(1*K64)),A(6*MB+(1*K64)),A(0)  FC, Op1, Op1L
000A80  00000000
000A84  AABBCCDD                             4011          DC    A(REG2PATT)
000A88  00000007                             4012          DC    A(7) CC0
000A8C  00610002 00000000                    4013          DC    A(6*MB+(1*K64)+002),A(000),A(0)
000A94  00000000
                                             4014


000A98                                       4016 M8T2     DS    0F
000A98  82                                   4017          DC    X'82'                          Test Num
000A99  0000                                 4018          DC    X'00',X'00'
000A9B  80                                   4019          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000A9C  0000142C 00000004                    4020          DC    A(TRTOP10),A(004)              Source - Op 1 & length
000AA4  0000312C 00010000                    4021          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                             4022 *                                             Target -
000AAC  00520000 00620000                    4023          DC    A(5*MB+(2*K64)),A(6*MB+(2*K64)),A(0)  FC, Op1, Op1L
000AB4  00000000
000AB8  AABBCCDD                             4024          DC    A(REG2PATT)
000ABC  00000007                             4025          DC    A(7) CC0
000AC0  00620004 00000000                    4026          DC    A(6*MB+(2*K64)+004),A(000),A(0)
000AC8  00000000


000ACC                                       4028 M8T3     DS    0F
000ACC  83                                   4029          DC    X'83'                          Test Num
000ACD  0000                                 4030          DC    X'00',X'00'
000ACF  80                                   4031          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000AD0  0000142C 00000008                    4032          DC    A(TRTOP10),A(008)              Source - Op 1 & length
000AD8  0000312C 00010000                    4033          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                             4034 *                                             Target -
000AE0  00530000 00630000                    4035          DC    A(5*MB+(3*K64)),A(6*MB+(3*K64)),A(0)  FC, Op1, Op1L
000AE8  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000AEC  AABBCCDD                           4036          DC    A(REG2PATT)
000AF0  00000007                           4037          DC    A(7) CC0
000AF4  00630008 00000000                  4038          DC    A(6*MB+(3*K64)+008),A(000),A(0)
000AFC  00000000



000B00                                     4040 M8T4     DS    0F
000B00  84                                 4041          DC    X'84'                        Test Num
000B01  0000                               4042          DC    X'00',X'00'
000B03  80                                 4043          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000B04  0000142C 00000100                  4044          DC    A(TRTOP10),A(256)            Source - Op 1 & length
000B0C  0000312C 00010000                  4045          DC    A(TRTOP20),A(K64)            Source - FC Table & length
                                           4046 *                                           Target -
000B14  00540000 00640000                  4047          DC    A(5*MB+(4*K64)),A(6*MB+(4*K64)),A(0)  FC, Op1, Op1L
000B1C  00000000
000B20  AABBCCDD                           4048          DC    A(REG2PATT)
000B24  00000007                           4049          DC    A(7) CC0
000B28  00640100 00000000                  4050          DC    A(6*MB+(4*K64)+256),A(000),A(0)
000B30  00000000



000B34                                     4052 M8T5     DS    0F
000B34  85                                 4053          DC    X'85'                        Test Num
000B35  0000                               4054          DC    X'00',X'00'
000B37  80                                 4055          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000B38  0000152C 00000100                  4056          DC    A(TRTOP111),A(256)           Source - Op 1 & length
000B40  0002382C 00010000                  4057          DC    A(TRTOP811),A(K64)           Source - FC Table & length
                                           4058 *                                           Target -
000B48  00550000 0064FFF4                  4059          DC    A(5*MB+(5*K64)),A(6*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
000B50  00000000
000B54  AABBCCDD                           4060          DC    A(REG2PATT)
000B58  0000000A                           4061          DC    A(10) CC1 or CC3
000B5C  00650004 000000F0                  4062          DC    A(6*MB+(5*K64)-12+X'10'),A(256-X'10'),XL4'11'
000B64  00000011



000B68                                     4064 M8T6     DS    0F
000B68  86                                 4065          DC    X'86'                        Test Num
000B69  0000                               4066          DC    X'00',X'00'
000B6B  80                                 4067          DC    X'80'                        M3: A=1,F=0,L=0,--=0
000B6C  0000162C 00000100                  4068          DC    A(TRTOP1F0),A(256)           Source - Op 1 & length
000B74  0004392C 00010000                  4069          DC    A(TRTOP8F0),A(K64)           Source - FC Table & length
                                           4070 *                                           Target -
000B7C  00560000 0065FFF4                  4071          DC    A(5*MB+(6*K64)),A(6*MB+(6*K64)-12),A(0)  FC, Op1, Op1L
000B84  00000000
000B88  AABBCCDD                           4072          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000B8C  0000000A                           4073          DC    A(10) CC1 or CC3
000B90  006600F2 00000002                  4074          DC    A(6*MB+(6*K64)-12+(256-2)),A(2),XL4'F0'
000B98  000000F0



000B9C                                     4076 M8T7     DS    0F
000B9C  87                                 4077          DC    X'87'                          Test Num
000B9D  0000                               4078          DC    X'00',X'00'
000B9F  80                                 4079          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000BA0  0000152C 00000100                  4080          DC    A(TRTOP111),A(256)             Source - Op 1 & length
000BA8  0002382C 00010000                  4081          DC    A(TRTOP811),A(K64)             Source - FC Table & length
                                           4082 *                                             Target -
000BB0  0057FFE0 00680000                  4083          DC    A(5*MB+(8*K64)-32),A(6*MB+(8*K64)),A(0)  FC, Op1, Op1L
000BB8  00000000
000BBC  AABBCCDD                           4084          DC    A(REG2PATT)
000BC0  0000000B                           4085          DC    A(11) CC1
000BC4  00680010 000000F0                  4086          DC    A(6*MB+(8*K64)+X'10'),A(256-X'10'),XL4'11'
000BCC  00000011



000BD0                                     4088 M8T8     DS    0F
000BD0  88                                 4089          DC    X'88'                          Test Num
000BD1  0000                               4090          DC    X'00',X'00'
000BD3  80                                 4091          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000BD4  0000172C 00000200                  4092          DC    A(TRTOP1F1),A(512)             Source - Op 1 & length
000BDC  00063A2C 00010000                  4093          DC    A(TRTOP8F1),A(K64)             Source - FC Table & length
                                           4094 *                                             Target -
000BE4  0058FFE0 00690000                  4095          DC    A(5*MB+(9*K64)-32),A(6*MB+(9*K64)),A(0)  FC, Op1, Op1L
000BEC  00000000
000BF0  AABBCCDD                           4096          DC    A(REG2PATT)
000BF4  0000000B                           4097          DC    A(11) CC1
000BF8  006901FE 00000002                  4098          DC    A(6*MB+(9*K64)+510),A(2),XL4'F1'
000C00  000000F1



000C04                                     4100 M8T9     DS    0F
000C04  89                                 4101          DC    X'89'                          Test Num
000C05  0000                               4102          DC    X'00',X'00'
000C07  80                                 4103          DC    X'80'                          M3: A=1,F=0,L=0,--=0
000C08  0000192C 00000800                  4104          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
000C10  0000312C 00010000                  4105          DC    A(TRTOP20),A(K64)              Source - FC Table & length
                                           4106 *                                             Target -
000C18  005A0000 006A0000                  4107          DC    A(5*MB+(10*K64)),A(6*MB+(10*K64)),A(0)  FC, Op1, Op1L
000C20  00000000
000C24  AABBCCDD                           4108          DC    A(REG2PATT)
000C28  00000007                           4109          DC    A(7) CC0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000C2C  006A0800 00000000                  4110          DC    A(6*MB+(10*K64)+2048),A(0),XL4'00'
000C34  00000000



000C38                                     4112 M8T10    DS    0F
000C38  8A                                 4113          DC    X'8A'                         Test Num
000C39  0000                               4114          DC    X'00',X'00'
000C3B  80                                 4115          DC    X'80'                         M3: A=1,F=0,L=0,--=0
000C3C  0000212C 00000800                  4116          DC    A(TRTO1L11),A(2048)           Source - Op 1 & length
000C44  0002382C 00010000                  4117          DC    A(TRTOP811),A(K64)            Source - FC Table & length
                                           4118 *                                            Target -
000C4C  005C0000 006BFF39                  4119          DC    A(5*MB+(12*K64)),A(6*MB+(12*K64)-199),A(0) FC, Op1, Op1L
000C54  00000000
000C58  AABBCCDD                           4120          DC    A(REG2PATT)
000C5C  0000000A                           4121          DC    A(10) CC1 or CC3
000C60  006C0339 00000400                  4122          DC    A(6*MB+(12*K64)-199+(4*256)),A(1024),XL4'11'
000C68  00000011



000C6C                                     4124 M8T11    DS    0F
000C6C  8B                                 4125          DC    X'8B'                         Test Num
000C6D  0000                               4126          DC    X'00',X'00'
000C6F  80                                 4127          DC    X'80'                         M3: A=1,F=0,L=0,--=0
000C70  0000292C 00000800                  4128          DC    A(TRTO1LF0),A(2048)           Source - Op 1 & length
000C78  0004392C 00010000                  4129          DC    A(TRTOP8F0),A(K64)            Source - FC Table & length
                                           4130 *                                            Target -
000C80  005DFFC1 006E0000                  4131          DC    A(5*MB+(14*K64)-63),A(6*MB+(14*K64)),A(0) FC, Op1, Op1L
000C88  00000000
000C8C  AABBCCDD                           4132          DC    A(REG2PATT)
000C90  0000000B                           4133          DC    A(11) CC1
000C94  006E07FE 00000002                  4134          DC    A(6*MB+(14*K64)+2048-2),A(2),XL4'F0'
000C9C  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4136 ********************************************************************************
                                             4137 *        tests with   M3: A=1,F=0,L=1, reserved=0    (10)
                                             4138 *                     FC Table : SIZE: 256 (2 BYTE ARGUMENT)
                                             4139 *                                Function Code is 1 byte
                                             4140 *                                Limit arg to 255
                                             4141 *
                                             4142 *              Note: Op1 length must be a multiple of 2
                                             4143 ********************************************************************************


000CA0                                       4145 M10T1    DS    0F
000CA0  A1                                   4146          DC    X'A1'                        Test Num
000CA1  0000                                 4147          DC    X'00',X'00'
000CA3  A0                                   4148          DC    X'A0'                        M3: A=1,F=0,L=1,--=0
000CA4  0000142C 00000002                    4149          DC    A(TRTOP10),A(002)            Source - Op 1 & length
000CAC  0000312C 00000100                    4150          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                             4151 *                                           Target -
000CB4  00A00000 00B00000                    4152          DC    A(10*MB+(0*K64)),A(11*MB+(0*K64)),A(0)  FC, Op1, Op1L
000CBC  00000000
000CC0  AABBCCDD                             4153          DC    A(REG2PATT)
000CC4  00000007                             4154          DC    A(7) CC0
000CC8  00B00002 00000000                    4155          DC    A(11*MB+(0*K64)+002),A(000),A(0)
000CD0  00000000


000CD4                                       4157 M10T2    DS    0F
000CD4  A2                                   4158          DC    X'A2'                        Test Num
000CD5  0000                                 4159          DC    X'00',X'00'
000CD7  A0                                   4160          DC    X'A0'                        M3: A=1,F=0,L=1,--=0
000CD8  0000142C 00000004                    4161          DC    A(TRTOP10),A(004)            Source - Op 1 & length
000CE0  0000312C 00000100                    4162          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                             4163 *                                           Target -
000CE8  00A10000 00B10000                    4164          DC    A(10*MB+(1*K64)),A(11*MB+(1*K64)),A(0)  FC, Op1, Op1L
000CF0  00000000
000CF4  AABBCCDD                             4165          DC    A(REG2PATT)
000CF8  00000007                             4166          DC    A(7) CC0
000CFC  00B10004 00000000                    4167          DC    A(11*MB+(1*K64)+004),A(000),A(0)
000D04  00000000


000D08                                       4169 M10T3    DS    0F
000D08  A3                                   4170          DC    X'A3'                        Test Num
000D09  0000                                 4171          DC    X'00',X'00'
000D0B  A0                                   4172          DC    X'A0'                        M3: A=1,F=0,L=1,--=0
000D0C  0000142C 00000008                    4173          DC    A(TRTOP10),A(008)            Source - Op 1 & length
000D14  0000312C 00000100                    4174          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                             4175 *                                           Target -
000D1C  00A20000 00B20000                    4176          DC    A(10*MB+(2*K64)),A(11*MB+(2*K64)),A(0)  FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000D24  00000000
000D28  AABBCCDD                           4177          DC    A(REG2PATT)
000D2C  00000007                           4178          DC    A(7) CC0
000D30  00B20008 00000000                  4179          DC    A(11*MB+(2*K64)+008),A(000),A(0)
000D38  00000000




000D3C                                     4181 M10T4    DS    0F
000D3C  A4                                 4182          DC    X'A4'                        Test Num
000D3D  0000                               4183          DC    X'00',X'00'
000D3F  A0                                 4184          DC    X'A0'                        M3: A=1,F=0,L=1,--=0
000D40  0000142C 00000100                  4185          DC    A(TRTOP10),A(256)            Source - Op 1 & length
000D48  0000312C 00000100                  4186          DC    A(TRTOP20),A(256)            Source - FC Table & length
                                           4187 *                                           Target -
000D50  00A30000 00B30000                  4188          DC    A(10*MB+(3*K64)),A(11*MB+(3*K64)),A(0)  FC, Op1, Op1L
000D58  00000000
000D5C  AABBCCDD                           4189          DC    A(REG2PATT)
000D60  00000007                           4190          DC    A(7) CC0
000D64  00B30100 00000000                  4191          DC    A(11*MB+(3*K64)+256),A(000),A(0)
000D6C  00000000

000D70                                     4193 M10T5    DS    0F
000D70  A5                                 4194          DC    X'A5'                        Test Num
000D71  0000                               4195          DC    X'00',X'00'
000D73  A0                                 4196          DC    X'A0'                        M3: A=1,F=0,L=1,--=0
000D74  0000152C 00000100                  4197          DC    A(TRTOP111),A(256)           Source - Op 1 & length
000D7C  0002322C 00000100                  4198          DC    A(TRTOP211),A(256)           Source - FC Table & length
                                           4199 *                                           Target -
000D84  00A40000 00B3FFF4                  4200          DC    A(10*MB+(4*K64)),A(11*MB+(4*K64)-12),A(0)  FC, Op1, Op1L
000D8C  00000000
000D90  AABBCCDD                           4201          DC    A(REG2PATT)
000D94  0000000A                           4202          DC    A(10) CC1 or CC3
000D98  00B40004 000000F0                  4203          DC    A(11*MB+(4*K64)-12+X'10'),A(256-X'10'),XL4'11'
000DA0  00000011




000DA4                                     4205 M10T6    DS    0F
000DA4  A6                                 4206          DC    X'A6'                        Test Num
000DA5  0000                               4207          DC    X'00',X'00'
000DA7  A0                                 4208          DC    X'A0'                        M3: A=1,F=0,L=1,--=0
000DA8  0000162C 00000100                  4209          DC    A(TRTOP1F0),A(256)           Source - Op 1 & length
000DB0  0002332C 00000100                  4210          DC    A(TRTOP2F0),A(256)           Source - FC Table & length
                                           4211 *                                           Target -
000DB8  00A50000 00B4FFF4                  4212          DC    A(10*MB+(5*K64)),A(11*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
000DC0  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000DC4  AABBCCDD                           4213          DC    A(REG2PATT)
000DC8  0000000A                           4214          DC    A(10) CC1 or CC3
000DCC  00B500F2 00000002                  4215          DC    A(11*MB+(5*K64)-12+(256-2)),A(2),XL4'F0'
000DD4  000000F0


000DD8                                     4217 M10T7    DS    0F
000DD8  A7                                 4218          DC    X'A7'                          Test Num
000DD9  0000                               4219          DC    X'00',X'00'
000DDB  A0                                 4220          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000DDC  0000152C 00000100                  4221          DC    A(TRTOP111),A(256)             Source - Op 1 & length
000DE4  0002322C 00000100                  4222          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                           4223 *                                             Target -
000DEC  00A5FFE0 00B60000                  4224          DC    A(10*MB+(6*K64-32)),A(11*MB+(6*K64)),A(0)  FC, Op1, Op1L
000DF4  00000000
000DF8  AABBCCDD                           4225          DC    A(REG2PATT)
000DFC  0000000B                           4226          DC    A(11) CC1
000E00  00B60010 000000F0                  4227          DC    A(11*MB+(6*K64)+X'10'),A(256-X'10'),XL4'11'
000E08  00000011


000E0C                                     4229 M10T8    DS    0F
000E0C  A8                                 4230          DC    X'A8'                          Test Num
000E0D  0000                               4231          DC    X'00',X'00'
000E0F  A0                                 4232          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000E10  0000172C 00000200                  4233          DC    A(TRTOP1F1),A(512)             Source - Op 1 & length
000E18  00063A2C 00000100                  4234          DC    A(TRTOP8F1),A(256)             Source - FC Table & length
                                           4235 *                                             Target -
000E20  00A70000 00B70000                  4236          DC    A(10*MB+(7*K64)),A(11*MB+(7*K64)),A(0)  FC, Op1, Op1L
000E28  00000000
000E2C  AABBCCDD                           4237          DC    A(REG2PATT)
000E30  0000000B                           4238          DC    A(11) CC1
000E34  00B701FE 00000002                  4239          DC    A(11*MB+(7*K64)+510),A(2),XL4'F1'
000E3C  000000F1


000E40                                     4241 M10T9    DS    0F
000E40  A9                                 4242          DC    X'A9'                          Test Num
000E41  0000                               4243          DC    X'00',X'00'
000E43  A0                                 4244          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000E44  0000192C 00000800                  4245          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
000E4C  0000312C 00000100                  4246          DC    A(TRTOP20),A(256)             Source - FC Table & length
                                           4247 *                                             Target -
000E54  00A80000 00B80000                  4248          DC    A(10*MB+(8*K64)),A(11*MB+(8*K64)),A(0)  FC, Op1, Op1L
000E5C  00000000
000E60  AABBCCDD                           4249          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000E64  00000007                          4250          DC    A(7) CC0
000E68  00B80800 00000000                 4251          DC    A(11*MB+(8*K64)+2048),A(0),XL4'00'
000E70  00000000


000E74                                    4253 M10T10   DS    0F
000E74  AA                                4254          DC    X'AA'                          Test Num
000E75  0000                              4255          DC    X'00',X'00'
000E77  A0                                4256          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000E78  0000212C 00000800                 4257          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
000E80  0002322C 00000100                 4258          DC    A(TRTOP211),A(256)             Source - FC Table & length
                                          4259 *                                             Target -
000E88  00A90000 00B8FF39                 4260          DC    A(10*MB+(9*K64)),A(11*MB+(9*K64)-199),A(0) FC, Op1, Op1L
000E90  00000000
000E94  AABBCCDD                          4261          DC    A(REG2PATT)
000E98  0000000A                          4262          DC    A(10) CC1 or CC3
000E9C  00B90339 00000400                 4263          DC    A(11*MB+(9*K64)-199+(4*256)),A(1024),XL4'11'
000EA4  00000011


000EA8                                    4265 M10T11   DS    0F
000EA8  AB                                4266          DC    X'AB'                          Test Num
000EA9  0000                              4267          DC    X'00',X'00'
000EAB  A0                                4268          DC    X'A0'                          M3: A=1,F=0,L=1,--=0
000EAC  0000292C 00000800                 4269          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
000EB4  0002332C 00000100                 4270          DC    A(TRTOP2F0),A(256)             Source - FC Table & length
                                          4271 *                                             Target - FC, Op1, Op1L
000EBC  00A9FE1F 00BA0000                 4272          DC    A(10*MB+(10*K64)-481),A(11*MB+(10*K64)),A(0)
000EC4  00000000
000EC8  AABBCCDD                          4273          DC    A(REG2PATT)
000ECC  0000000B                          4274          DC    A(11) CC1
000ED0  00BA07FE 00000002                 4275          DC    A(11*MB+(10*K64)+2048-2),A(2),XL4'F0'
000ED8  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4277 ************************************************************************
                                             4278 *        tests with   M3: A=1,F=1,L=0, reserved=0    (12)
                                             4279 *                     FC Table : SIZE: 131,072 (2 BYTE ARGUMENT)
                                             4280 *                                Function Code is 2 bytes
                                             4281 *
                                             4282 *              Note: Op1 length must be a multiple of 2
                                             4283 ************************************************************************


000EDC                                       4285 M12T1    DS    0F
000EDC  C1                                   4286          DC    X'C1'                          Test Num
000EDD  0000                                 4287          DC    X'00',X'00'
000EDF  C0                                   4288          DC    X'C0'                          M3: A=1,F=1,L=0,--=0
000EE0  0000142C 00000002                    4289          DC    A(TRTOP10),A(002)              Source - Op 1 & length
000EE8  0000312C 00020000                    4290          DC    A(TRTOP20),A(2*K64)            Source - FC Table & length
                                             4291 *                                             Target -
000EF0  00700000 00900000                    4292          DC    A(7*MB+(0*K64)),A(9*MB+(0*K64)),A(0)  FC, Op1, Op1L
000EF8  00000000
000EFC  AABBCCDD                             4293          DC    A(REG2PATT)
000F00  00000007                             4294          DC    A(7) CC0
000F04  00900002 00000000                    4295          DC    A(9*MB+(0*K64)+002),A(000),A(0)
000F0C  00000000
                                             4296


000F10                                       4298 M12T2    DS    0F
000F10  C2                                   4299          DC    X'C2'                          Test Num
000F11  0000                                 4300          DC    X'00',X'00'
000F13  C0                                   4301          DC    X'C0'                          M3: A=1,F=1,L=0,--=0
000F14  0000142C 00000004                    4302          DC    A(TRTOP10),A(004)              Source - Op 1 & length
000F1C  0000312C 00020000                    4303          DC    A(TRTOP20),A(2*K64)            Source - FC Table & length
                                             4304 *                                             Target -
000F24  00720000 00910000                    4305          DC    A(7*MB+(2*K64)),A(9*MB+(1*K64)),A(0)  FC, Op1, Op1L
000F2C  00000000
000F30  AABBCCDD                             4306          DC    A(REG2PATT)
000F34  00000007                             4307          DC    A(7) CC0
000F38  00910004 00000000                    4308          DC    A(9*MB+(1*K64)+004),A(000),A(0)
000F40  00000000


000F44                                       4310 M12T3    DS    0F
000F44  C3                                   4311          DC    X'C3'                          Test Num
000F45  0000                                 4312          DC    X'00',X'00'
000F47  C0                                   4313          DC    X'C0'                          M3: A=1,F=1,L=0,--=0
000F48  0000142C 00000008                    4314          DC    A(TRTOP10),A(008)              Source - Op 1 & length
000F50  0000312C 00020000                    4315          DC    A(TRTOP20),A(2*K64)            Source - FC Table & length
                                             4316 *                                             Target -
000F58  00740000 00920000                    4317          DC    A(7*MB+(4*K64)),A(9*MB+(2*K64)),A(0)  FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

000F60  00000000
000F64  AABBCCDD                           4318          DC    A(REG2PATT)
000F68  00000007                           4319          DC    A(7) CC0
000F6C  00920008 00000000                  4320          DC    A(9*MB+(2*K64)+008),A(000),A(0)
000F74  00000000



000F78                                     4322 M12T4    DS    0F
000F78  C4                                 4323          DC    X'C4'                        Test Num
000F79  0000                               4324          DC    X'00',X'00'
000F7B  C0                                 4325          DC    X'C0'                        M3: A=1,F=1,L=0,--=0
000F7C  0000142C 00000100                  4326          DC    A(TRTOP10),A(256)            Source - Op 1 & length
000F84  0000312C 00020000                  4327          DC    A(TRTOP20),A(2*K64)          Source - FC Table & length
                                           4328 *                                           Target -
000F8C  00760000 00930000                  4329          DC    A(7*MB+(6*K64)),A(9*MB+(3*K64)),A(0)  FC, Op1, Op1L
000F94  00000000
000F98  AABBCCDD                           4330          DC    A(REG2PATT)
000F9C  00000007                           4331          DC    A(7) CC0
000FA0  00930100 00000000                  4332          DC    A(9*MB+(3*K64)+256),A(000),A(0)
000FA8  00000000

000FAC                                     4334 M12T5    DS    0F
000FAC  C5                                 4335          DC    X'C5'                        Test Num
000FAD  0000                               4336          DC    X'00',X'00'
000FAF  C0                                 4337          DC    X'C0'                        M3: A=1,F=1,L=0,--=0
000FB0  0000152C 00000100                  4338          DC    A(TRTOP111),A(256)           Source - Op 1 & length
000FB8  00083B2C 00020000                  4339          DC    A(TRTOPC11),A(2*K64)         Source - FC Table & length
                                           4340 *                                           Target -
000FC0  00780000 0093FFF4                  4341          DC    A(7*MB+(8*K64)),A(9*MB+(4*K64)-12),A(0)  FC, Op1, Op1L
000FC8  00000000
000FCC  AABBCCDD                           4342          DC    A(REG2PATT)
000FD0  0000000A                           4343          DC    A(10) CC1 or CC3
000FD4  00940004 000000F0                  4344          DC    A(9*MB+(4*K64)-12+X'10'),A(256-X'10'),XL4'11'
000FDC  00000011



000FE0                                     4346 M12T6    DS    0F
000FE0  C6                                 4347          DC    X'C6'                        Test Num
000FE1  0000                               4348          DC    X'00',X'00'
000FE3  C0                                 4349          DC    X'C0'                        M3: A=1,F=1,L=0,--=0
000FE4  0000162C 00000100                  4350          DC    A(TRTOP1F0),A(256)           Source - Op 1 & length
000FEC  000A3B50 00020000                  4351          DC    A(TRTOPCF0),A(2*K64)         Source - FC Table & length
                                           4352 *                                           Target -
000FF4  007A0000 0094FFF4                  4353          DC    A(7*MB+(10*K64)),A(9*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
000FFC  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001000  AABBCCDD                          4354          DC    A(REG2PATT)
001004  0000000A                          4355          DC    A(10) CC1 or CC3
001008  009500F2 00000002                 4356          DC    A(9*MB+(5*K64)-12+(256-2)),A(2),XL4'F0'
001010  000000F0


001014                                    4358 M12T7    DS    0F
001014  C7                                4359          DC    X'C7'                         Test Num
001015  0000                              4360          DC    X'00',X'00'
001017  C0                                4361          DC    X'C0'                         M3: A=1,F=1,L=0,--=0
001018  0000152C 00000100                 4362          DC    A(TRTOP111),A(256)            Source - Op 1 & length
001020  00083B2C 00020000                 4363          DC    A(TRTOPC11),A(2*K64)          Source - FC Table & length
                                          4364 *                                            Target -
001028  007CFFE0 00960000                 4365          DC    A(7*MB+(13*K64)-32),A(9*MB+(6*K64)),A(0)  FC, Op1, Op1L
001030  00000000
001034  AABBCCDD                          4366          DC    A(REG2PATT)
001038  0000000B                          4367          DC    A(11) CC1
00103C  00960010 000000F0                 4368          DC    A(9*MB+(6*K64)+X'10'),A(256-X'10'),XL4'11'
001044  00000011


001048                                    4370 M12T8    DS    0F
001048  C8                                4371          DC    X'C8'                         Test Num
001049  0000                              4372          DC    X'00',X'00'
00104B  C0                                4373          DC    X'C0'                         M3: A=1,F=1,L=0,--=0
00104C  0000172C 00000200                 4374          DC    A(TRTOP1F1),A(512)            Source - Op 1 & length
001054  000C3D4E 00020000                 4375          DC    A(TRTOPCF1),A(2*K64)          Source - FC Table & length
                                          4376 *                                            Target -
00105C  007F0000 00970000                 4377          DC    A(7*MB+(15*K64)),A(9*MB+(7*K64)),A(0)  FC, Op1, Op1L
001064  00000000
001068  AABBCCDD                          4378          DC    A(REG2PATT)
00106C  0000000B                          4379          DC    A(11) CC1
001070  009701FE 00000002                 4380          DC    A(9*MB+(7*K64)+510),A(2),XL4'F1'
001078  000000F1


00107C                                    4382 M12T9    DS    0F
00107C  C9                                4383          DC    X'C9'                         Test Num
00107D  0000                              4384          DC    X'00',X'00'
00107F  C0                                4385          DC    X'C0'                         M3: A=1,F=1,L=0,--=0
001080  0000192C 00000800                 4386          DC    A(TRTO1L0),A(2048)            Source - Op 1 & length
001088  0000312C 00020000                 4387          DC    A(TRTOP20),A(2*K64)           Source - FC Table & length
                                          4388 *                                            Target -
001090  00810000 00980000                 4389          DC    A(7*MB+(17*K64)),A(9*MB+(8*K64)),A(0)  FC, Op1, Op1L
001098  00000000
00109C  AABBCCDD                          4390          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0010A0  00000007                          4391          DC    A(7) CC0
0010A4  00980800 00000000                 4392          DC    A(9*MB+(8*K64)+2048),A(0),XL4'00'
0010AC  00000000



0010B0                                    4394 M12T10   DS    0F
0010B0  CA                                4395          DC    X'CA'                          Test Num
0010B1  0000                              4396          DC    X'00',X'00'
0010B3  C0                                4397          DC    X'C0'                          M3: A=1,F=1,L=0,--=0
0010B4  0000212C 00000800                 4398          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
0010BC  00083B2C 00020000                 4399          DC    A(TRTOPC11),A(2*K64)           Source - FC Table & length
                                          4400 *                                             Target -
0010C4  00830000 0098FF39                 4401          DC    A(7*MB+(19*K64)),A(9*MB+(9*K64)-199),A(0)  FC, Op1, Op1L
0010CC  00000000
0010D0  AABBCCDD                          4402          DC    A(REG2PATT)
0010D4  0000000A                          4403          DC    A(10) CC1 or CC3
0010D8  00990339 00000400                 4404          DC    A(9*MB+(9*K64)-199+(4*256)),A(1024),XL4'11'
0010E0  00000011



0010E4                                    4406 M12T11   DS    0F
0010E4  CB                                4407          DC    X'CB'                          Test Num
0010E5  0000                              4408          DC    X'00',X'00'
0010E7  C0                                4409          DC    X'C0'                          M3: A=1,F=1,L=0,--=0
0010E8  0000292C 00000800                 4410          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
0010F0  000A3B50 00020000                 4411          DC    A(TRTOPCF0),A(2*K64)           Source - FC Table & length
                                          4412 *                                             Target -
0010F8  0085FE1F 009A0000                 4413          DC    A(7*MB+(22*K64)-481),A(9*MB+(10*K64)),A(0) FC, Op1, Op1L
001100  00000000
001104  AABBCCDD                          4414          DC    A(REG2PATT)
001108  0000000B                          4415          DC    A(11) CC1
00110C  009A07FE 00000002                 4416          DC    A(9*MB+(10*K64)+2048-2),A(2),XL4'F0'
001114  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4418 ************************************************************************
                                             4419 *        tests with   M3: A=1,F=1,L=1, reserved=0    (14)
                                             4420 *                     FC Table : SIZE: 512 (2 BYTE ARGUMENT)
                                             4421 *                                Function Code is 2 byte
                                             4422 *                                Limit arg to 255
                                             4423 *
                                             4424 *              Note: Op1 length must be a multiple of 2
                                             4425 ************************************************************************


001118                                       4427 M14T1    DS    0F
001118  E1                                   4428          DC    X'E1'                        Test Num
001119  0000                                 4429          DC    X'00',X'00'
00111B  E0                                   4430          DC    X'E0'                        M3: A=1,F=1,L=1,---=0
00111C  0000142C 00000002                    4431          DC    A(TRTOP10),A(002)            Source - Op 1 & length
001124  0000312C 00000200                    4432          DC    A(TRTOP20),A(512)            Source - FC Table & length
                                             4433 *                                           Target -
00112C  00B00000 00C00000                    4434          DC    A(11*MB+(0*K64)),A(12*MB+(0*K64)),A(0)  FC, Op1, Op1L
001134  00000000
001138  AABBCCDD                             4435          DC    A(REG2PATT)
00113C  00000007                             4436          DC    A(7) CC0
001140  00C00002 00000000                    4437          DC    A(12*MB+(0*K64)+002),A(000),A(0)
001148  00000000


00114C                                       4439 M14T2    DS    0F
00114C  E2                                   4440          DC    X'E2'                        Test Num
00114D  0000                                 4441          DC    X'00',X'00'
00114F  E0                                   4442          DC    X'E0'                        M3: A=1,F=1,L=1,---=0
001150  0000142C 00000004                    4443          DC    A(TRTOP10),A(004)            Source - Op 1 & length
001158  0000312C 00000200                    4444          DC    A(TRTOP20),A(512)            Source - FC Table & length
                                             4445 *                                           Target -
001160  00B10000 00C10000                    4446          DC    A(11*MB+(1*K64)),A(12*MB+(1*K64)),A(0)  FC, Op1, Op1L
001168  00000000
00116C  AABBCCDD                             4447          DC    A(REG2PATT)
001170  00000007                             4448          DC    A(7) CC0
001174  00C10004 00000000                    4449          DC    A(12*MB+(1*K64)+004),A(000),A(0)
00117C  00000000


001180                                       4451 M14T3    DS    0F
001180  E3                                   4452          DC    X'E3'                        Test Num
001181  0000                                 4453          DC    X'00',X'00'
001183  E0                                   4454          DC    X'E0'                        M3: A=1,F=1,L=1,---=0
001184  0000142C 00000008                    4455          DC    A(TRTOP10),A(008)            Source - Op 1 & length
00118C  0000312C 00000200                    4456          DC    A(TRTOP20),A(512)            Source - FC Table & length
                                             4457 *                                           Target -
001194  00B20000 00C20000                    4458          DC    A(11*MB+(2*K64)),A(12*MB+(2*K64)),A(0)  FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00119C  00000000
0011A0  AABBCCDD                           4459          DC    A(REG2PATT)
0011A4  00000007                           4460          DC    A(7) CC0
0011A8  00C20008 00000000                  4461          DC    A(12*MB+(2*K64)+008),A(000),A(0)
0011B0  00000000



0011B4                                     4463 M14T4    DS    0F
0011B4  E4                                 4464          DC    X'E4'                        Test Num
0011B5  0000                               4465          DC    X'00',X'00'
0011B7  E0                                 4466          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
0011B8  0000142C 00000100                  4467          DC    A(TRTOP10),A(256)            Source - Op 1 & length
0011C0  0000312C 00000200                  4468          DC    A(TRTOP20),A(512)            Source - FC Table & length
                                           4469 *                                           Target -
0011C8  00B30000 00C30000                  4470          DC    A(11*MB+(3*K64)),A(12*MB+(3*K64)),A(0)  FC, Op1, Op1L
0011D0  00000000
0011D4  AABBCCDD                           4471          DC    A(REG2PATT)
0011D8  00000007                           4472          DC    A(7) CC0
0011DC  00C30100 00000000                  4473          DC    A(12*MB+(3*K64)+256),A(000),A(0)
0011E4  00000000

0011E8                                     4475 M14T5    DS    0F
0011E8  E5                                 4476          DC    X'E5'                        Test Num
0011E9  0000                               4477          DC    X'00',X'00'
0011EB  E0                                 4478          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
0011EC  0000152C 00000100                  4479          DC    A(TRTOP111),A(256)           Source - Op 1 & length
0011F4  0002342C 00000200                  4480          DC    A(TRTOP411),A(512)           Source - FC Table & length
                                           4481 *                                           Target -
0011FC  00B40000 00C3FFF4                  4482          DC    A(11*MB+(4*K64)),A(12*MB+(4*K64)-12),A(0)  FC, Op1, Op1L
001204  00000000
001208  AABBCCDD                           4483          DC    A(REG2PATT)
00120C  0000000A                           4484          DC    A(10) CC1 or CC3
001210  00C40004 000000F0                  4485          DC    A(12*MB+(4*K64)-12+X'10'),A(256-X'10'),XL4'11'
001218  00000011



00121C                                     4487 M14T6    DS    0F
00121C  E6                                 4488          DC    X'E6'                        Test Num
00121D  0000                               4489          DC    X'00',X'00'
00121F  E0                                 4490          DC    X'E0'                        M3: A=1,F=1,L=1,--=0
001220  0000162C 00000100                  4491          DC    A(TRTOP1F0),A(256)           Source - Op 1 & length
001228  0002362C 00000200                  4492          DC    A(TRTOP4F0),A(512)           Source - FC Table & length
                                           4493 *                                           Target -
001230  00B50000 00C4FFF4                  4494          DC    A(11*MB+(5*K64)),A(12*MB+(5*K64)-12),A(0)  FC, Op1, Op1L
001238  00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00123C  AABBCCDD                           4495          DC    A(REG2PATT)
001240  0000000A                           4496          DC    A(10) CC1 or CC3
001244  00C500F2 00000002                  4497          DC    A(12*MB+(5*K64)-12+254),A(2),XL4'F0'
00124C  000000F0


001250                                     4499 M14T7    DS    0F
001250  E7                                 4500          DC    X'E7'                          Test Num
001251  0000                               4501          DC    X'00',X'00'
001253  E0                                 4502          DC    X'E0'                          M3: A=1,F=1,L=1,--=0
001254  0000152C 00000100                  4503          DC    A(TRTOP111),A(256)             Source - Op 1 & length
00125C  0002342C 00000200                  4504          DC    A(TRTOP411),A(512)             Source - FC Table & length
                                           4505 *                                             Target -
001264  00B5FFE0 00C60000                  4506          DC    A(11*MB+(6*K64)-32),A(12*MB+(6*K64)),A(0)  FC, Op1, Op1L
00126C  00000000
001270  AABBCCDD                           4507          DC    A(REG2PATT)
001274  0000000B                           4508          DC    A(11) CC1
001278  00C60010 000000F0                  4509          DC    A(12*MB+(6*K64)+X'10'),A(256-X'10'),XL4'11'
001280  00000011


001284                                     4511 M14T8    DS    0F
001284  E8                                 4512          DC    X'E8'                          Test Num
001285  0000                               4513          DC    X'00',X'00'
001287  E0                                 4514          DC    X'E0'                          M3: A=1,F=1,L=1,--=0
001288  0000172C 00000200                  4515          DC    A(TRTOP1F1),A(512)             Source - Op 1 & length
001290  000C3D4E 00000200                  4516          DC    A(TRTOPCF1),A(512)             Source - FC Table & length
                                           4517 *                                             Target -
001298  00B70000 00C70000                  4518          DC    A(11*MB+(7*K64)),A(12*MB+(7*K64)),A(0)  FC, Op1, Op1L
0012A0  00000000
0012A4  AABBCCDD                           4519          DC    A(REG2PATT)
0012A8  0000000B                           4520          DC    A(11) CC1
0012AC  00C701FE 00000002                  4521          DC    A(12*MB+(7*K64)+510),A(2),XL4'F1'
0012B4  000000F1


0012B8                                     4523 M14T9    DS    0F
0012B8  E9                                 4524          DC    X'E9'                          Test Num
0012B9  0000                               4525          DC    X'00',X'00'
0012BB  E0                                 4526          DC    X'E0'                          M3: A=1,F=1,L=1,--=0
0012BC  0000192C 00000800                  4527          DC    A(TRTO1L0),A(2048)             Source - Op 1 & length
0012C4  0000312C 00000200                  4528          DC    A(TRTOP20),A(512)              Source - FC Table & length
                                           4529 *                                             Target -
0012CC  00B80000 00C80000                  4530          DC    A(11*MB+(8*K64)),A(12*MB+(8*K64)),A(0)  FC, Op1, Op1L
0012D4  00000000
0012D8  AABBCCDD                           4531          DC    A(REG2PATT)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0012DC  00000007                          4532          DC    A(7) CC0
0012E0  00C80800 00000000                 4533          DC    A(12*MB+(8*K64)+2048),A(000),A(0)
0012E8  00000000



0012EC                                    4535 M14T10   DS    0F
0012EC  EA                                4536          DC    X'EA'                          Test Num
0012ED  0000                              4537          DC    X'00',X'00'
0012EF  E0                                4538          DC    X'E0'                          M3: A=1,F=1,L=1,--=0
0012F0  0000212C 00000800                 4539          DC    A(TRTO1L11),A(2048)            Source - Op 1 & length
0012F8  0002342C 00000200                 4540          DC    A(TRTOP411),A(512)             Source - FC Table & length
                                          4541 *                                             Target -
001300  00B90000 00C8FF38                 4542          DC    A(11*MB+(9*K64)),A(12*MB+(9*K64)-200),A(0) FC, Op1, Op1L
001308  00000000
00130C  AABBCCDD                          4543          DC    A(REG2PATT)
001310  0000000A                          4544          DC    A(10) CC1 or CC3
001314  00C90338 00000400                 4545          DC    A(12*MB+(9*K64)-200+(4*256)),A(1024),XL4'11'
00131C  00000011



001320                                    4547 M14T11   DS    0F
001320  EB                                4548          DC    X'EB'                          Test Num
001321  0000                              4549          DC    X'00',X'00'
001323  E0                                4550          DC    X'E0'                          M3: A=1,F=1,L=1,--=0
001324  0000292C 00000800                 4551          DC    A(TRTO1LF0),A(2048)            Source - Op 1 & length
00132C  0002362C 00000200                 4552          DC    A(TRTOP4F0),A(512)             Source - FC Table & length
                                          4553 *                                             Target -  FC, Op1, Op1L
001334  00B9FFC0 00CA0000                 4554          DC    A(11*MB+(10*K64)-64),A(12*MB+(10*K64)),A(0)
00133C  00000000
001340  AABBCCDD                          4555          DC    A(REG2PATT)
001344  0000000B                          4556          DC    A(11) CC1
001348  00CA07FE 00000002                 4557          DC    A(12*MB+(10*K64)+2048-2),A(2),XL4'F0'
001350  000000F0
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                             4559 ********************************************************************
                                             4560 *        Check performance tests are valid.
                                             4561 *        tests with   M3: A=1,F=1,L=0, reserved=0    (12)
                                             4562 *                     FC Table : SIZE: 131,072 (2 BYTE ARGUMENT)
                                             4563 *                                Function Code is 2 bytes
                                             4564 *
                                             4565 *              Note: Op1 length must be a multiple of 2
                                             4566 ********************************************************************


001354                                       4568 F12T8    DS    0F
001354  F8                                   4569          DC    X'F8'                        Test Num
001355  0000                                 4570          DC    X'00',X'00'
001357  C0                                   4571          DC    X'C0'                        M3: A=1,F=1,L=0,---=0
001358  0000172C 00000200                    4572          DC    A(TRTOP1F1),A(512)           Source - Op 1 & length
001360  000C3D4E 00020000                    4573          DC    A(TRTOPCF1),A(2*K64)         Source - FC Table & length
                                             4574 *                                         Target -
001368  00710000 00910000                    4575          DC    A(7*MB+(1*K64)),A(9*MB+(1*K64)),A(0)  FC, Op1, Op1L
001370  00000000
001374  AABBCCDD                             4576          DC    A(REG2PATT)
001378  0000000B                             4577          DC    A(11) CC1
00137C  009101FE 00000002                    4578          DC    A(9*MB+(1*K64)+510),A(2),XL4'F1'
001384  000000F1


001388                                       4580 F12T8A   DS    0F
001388  F9                                   4581          DC    X'F9'                        Test Num
001389  0000                                 4582          DC    X'00',X'00'
00138B  C0                                   4583          DC    X'C0'                        M3: A=1,F=1,L=0,---=0
00138C  0000172C 00000200                    4584          DC    A(TRTOP1F1),A(512)           Source - Op 1 & length
001394  000C3D4E 00020000                    4585          DC    A(TRTOPCF1),A(2*K64)         Source - FC Table & length
                                             4586 *                                         Target - FC, Op1, Op1L
00139C  0072FF81 0092FF81                    4587          DC    A(7*MB+(3*K64)-127),A(9*MB+(3*K64)-127),A(0)
0013A4  00000000
0013A8  AABBCCDD                             4588          DC    A(REG2PATT)
0013AC  0000000A                             4589          DC    A(10) CC1 or CC3
0013B0  0093017F 00000002                    4590          DC    A(9*MB+(3*K64)-127+510),A(2),XL4'F1'
0013B8  000000F1


0013BC                                       4592 F12T11   DS    0F
0013BC  FB                                   4593          DC    X'FB'                        Test Num
0013BD  0000                                 4594          DC    X'00',X'00'
0013BF  C0                                   4595          DC    X'C0'                        M3: A=1,F=1,L=0,---=0
0013C0  0000292C 00000800                    4596          DC    A(TRTO1LF0),A(2048)          Source - Op 1 & length
0013C8  000A3B50 00020000                    4597          DC    A(TRTOPCF0),A(2*K64)         Source - FC Table & length
                                             4598 *                                         Target -
0013D0  00760000 00960000                    4599          DC    A(7*MB+(6*K64)),A(9*MB+(6*K64)),A(0) FC, Op1, Op1L
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0013D8  00000000
0013DC  AABBCCDD                           4600          DC    A(REG2PATT)
0013E0  0000000B                           4601          DC    A(11) CC1
0013E4  009607FE 00000002                  4602          DC    A(9*MB+(6*K64)+2048-2),A(2),XL4'F0'
0013EC  000000F0



0013F0                                     4604 F12T11A  DS    0F
0013F0  FC                                 4605          DC    X'FC'                       Test Num
0013F1  0000                               4606          DC    X'00',X'00'
0013F3  C0                                 4607          DC    X'C0'                       M3: A=1,F=1,L=0,--=0
0013F4  0000292C 00000800                  4608          DC    A(TRTO1LF0),A(2048)         Source - Op 1 & length
0013FC  000A3B50 00020000                  4609          DC    A(TRTOPCF0),A(2*K64)        Source - FC Table & length
                                           4610 *                                    Target - FC, Op1, Op1L
001404  0078FE1F 0098FE1F                  4611          DC    A(7*MB+(9*K64)-481),A(9*MB+(9*K64)-481),A(0)
00140C  00000000
001410  AABBCCDD                           4612          DC    A(REG2PATT)
001414  0000000A                           4613          DC    A(10) CC1 or CC3
001418  0099061D 00000002                  4614          DC    A(9*MB+(9*K64)-481+2048-2),A(2),XL4'F0'
001420  000000F0
                                           4615
                                           4616
                                           4617
001424  00000000                           4618          DC    A(0)     end of table
001428  00000000                           4619          DC    A(0)     end of table
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                     4621 ********************************************************************************
                                     4622 *        TRTE op1 scan data...
                                     4623 ********************************************************************************

00142C  78125634 78125634                   4625 TRTOP10  DC    64XL4'78125634'    (CC0)
001434  78125634 78125634
00143C  78125634 78125634
001444  78125634 78125634
00144C  78125634 78125634
001454  78125634 78125634
00145C  78125634 78125634
001464  78125634 78125634
00146C  78125634 78125634
001474  78125634 78125634
00147C  78125634 78125634
001484  78125634 78125634
00148C  78125634 78125634
001494  78125634 78125634
00149C  78125634 78125634
0014A4  78125634 78125634
0014AC  78125634 78125634
0014B4  78125634 78125634
0014BC  78125634 78125634
0014C4  78125634 78125634
0014CC  78125634 78125634
0014D4  78125634 78125634
0014DC  78125634 78125634
0014E4  78125634 78125634
0014EC  78125634 78125634
0014F4  78125634 78125634
0014FC  78125634 78125634
001504  78125634 78125634
00150C  78125634 78125634
001514  78125634 78125634
00151C  78125634 78125634
001524  78125634 78125634

00152C  78125634 78125634                   4627 TRTOP111 DC    04XL4'78125634',X'00110000',59XL4'78125634'    (CC1)
001534  78125634 78125634
00153C  00110000 78125634
001544  78125634 78125634
00154C  78125634 78125634
001554  78125634 78125634
00155C  78125634 78125634
001564  78125634 78125634
00156C  78125634 78125634
001574  78125634 78125634
00157C  78125634 78125634
001584  78125634 78125634
00158C  78125634 78125634
001594  78125634 78125634
00159C  78125634 78125634
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0015A4  78125634 78125634
0015AC  78125634 78125634
0015B4  78125634 78125634
0015BC  78125634 78125634
0015C4  78125634 78125634
0015CC  78125634 78125634
0015D4  78125634 78125634
0015DC  78125634 78125634
0015E4  78125634 78125634
0015EC  78125634 78125634
0015F4  78125634 78125634
0015FC  78125634 78125634
001604  78125634 78125634
00160C  78125634 78125634
001614  78125634 78125634
00161C  78125634 78125634
001624  78125634 78125634

00162C  78125634 78125634                  4629 TRTOP1F0 DC    63XL4'78125634',X'000000F0'    (CC1)
001634  78125634 78125634
00163C  78125634 78125634
001644  78125634 78125634
00164C  78125634 78125634
001654  78125634 78125634
00165C  78125634 78125634
001664  78125634 78125634
00166C  78125634 78125634
001674  78125634 78125634
00167C  78125634 78125634
001684  78125634 78125634
00168C  78125634 78125634
001694  78125634 78125634
00169C  78125634 78125634
0016A4  78125634 78125634
0016AC  78125634 78125634
0016B4  78125634 78125634
0016BC  78125634 78125634
0016C4  78125634 78125634
0016CC  78125634 78125634
0016D4  78125634 78125634
0016DC  78125634 78125634
0016E4  78125634 78125634
0016EC  78125634 78125634
0016F4  78125634 78125634
0016FC  78125634 78125634
001704  78125634 78125634
00170C  78125634 78125634
001714  78125634 78125634
00171C  78125634 78125634
001724  78125634 000000F0

00172C  78125634 78125634                  4631 TRTOP1F1 DC    127XL4'78125634',X'000000F1'    (CC1)
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001734  78125634 78125634
00173C  78125634 78125634
001744  78125634 78125634
00174C  78125634 78125634
001754  78125634 78125634
00175C  78125634 78125634
001764  78125634 78125634
00176C  78125634 78125634
001774  78125634 78125634
00177C  78125634 78125634
001784  78125634 78125634
00178C  78125634 78125634
001794  78125634 78125634
00179C  78125634 78125634
0017A4  78125634 78125634
0017AC  78125634 78125634
0017B4  78125634 78125634
0017BC  78125634 78125634
0017C4  78125634 78125634
0017CC  78125634 78125634
0017D4  78125634 78125634
0017DC  78125634 78125634
0017E4  78125634 78125634
0017EC  78125634 78125634
0017F4  78125634 78125634
0017FC  78125634 78125634
001804  78125634 78125634
00180C  78125634 78125634
001814  78125634 78125634
00181C  78125634 78125634
001824  78125634 78125634
00182C  78125634 78125634
001834  78125634 78125634
00183C  78125634 78125634
001844  78125634 78125634
00184C  78125634 78125634
001854  78125634 78125634
00185C  78125634 78125634
001864  78125634 78125634
00186C  78125634 78125634
001874  78125634 78125634
00187C  78125634 78125634
001884  78125634 78125634
00188C  78125634 78125634
001894  78125634 78125634
00189C  78125634 78125634
0018A4  78125634 78125634
0018AC  78125634 78125634
0018B4  78125634 78125634
0018BC  78125634 78125634
0018C4  78125634 78125634
0018CC  78125634 78125634
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0018D4  78125634 78125634
0018DC  78125634 78125634
0018E4  78125634 78125634
0018EC  78125634 78125634
0018F4  78125634 78125634
0018FC  78125634 78125634
001904  78125634 78125634
00190C  78125634 78125634
001914  78125634 78125634
00191C  78125634 78125634
001924  78125634 000000F1

00192C  98765432 98765432                4633 TRTO1L0  DC    512XL4'98765432'    (CC0)
001934  98765432 98765432
00193C  98765432 98765432
001944  98765432 98765432
00194C  98765432 98765432
001954  98765432 98765432
00195C  98765432 98765432
001964  98765432 98765432
00196C  98765432 98765432
001974  98765432 98765432
00197C  98765432 98765432
001984  98765432 98765432
00198C  98765432 98765432
001994  98765432 98765432
00199C  98765432 98765432
0019A4  98765432 98765432
0019AC  98765432 98765432
0019B4  98765432 98765432
0019BC  98765432 98765432
0019C4  98765432 98765432
0019CC  98765432 98765432
0019D4  98765432 98765432
0019DC  98765432 98765432
0019E4  98765432 98765432
0019EC  98765432 98765432
0019F4  98765432 98765432
0019FC  98765432 98765432
001A04  98765432 98765432
001A0C  98765432 98765432
001A14  98765432 98765432
001A1C  98765432 98765432
001A24  98765432 98765432
001A2C  98765432 98765432
001A34  98765432 98765432
001A3C  98765432 98765432
001A44  98765432 98765432
001A4C  98765432 98765432
001A54  98765432 98765432
001A5C  98765432 98765432
001A64  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001A6C  98765432 98765432
001A74  98765432 98765432
001A7C  98765432 98765432
001A84  98765432 98765432
001A8C  98765432 98765432
001A94  98765432 98765432
001A9C  98765432 98765432
001AA4  98765432 98765432
001AAC  98765432 98765432
001AB4  98765432 98765432
001ABC  98765432 98765432
001AC4  98765432 98765432
001ACC  98765432 98765432
001AD4  98765432 98765432
001ADC  98765432 98765432
001AE4  98765432 98765432
001AEC  98765432 98765432
001AF4  98765432 98765432
001AFC  98765432 98765432
001B04  98765432 98765432
001B0C  98765432 98765432
001B14  98765432 98765432
001B1C  98765432 98765432
001B24  98765432 98765432
001B2C  98765432 98765432
001B34  98765432 98765432
001B3C  98765432 98765432
001B44  98765432 98765432
001B4C  98765432 98765432
001B54  98765432 98765432
001B5C  98765432 98765432
001B64  98765432 98765432
001B6C  98765432 98765432
001B74  98765432 98765432
001B7C  98765432 98765432
001B84  98765432 98765432
001B8C  98765432 98765432
001B94  98765432 98765432
001B9C  98765432 98765432
001BA4  98765432 98765432
001BAC  98765432 98765432
001BB4  98765432 98765432
001BBC  98765432 98765432
001BC4  98765432 98765432
001BCC  98765432 98765432
001BD4  98765432 98765432
001BDC  98765432 98765432
001BE4  98765432 98765432
001BEC  98765432 98765432
001BF4  98765432 98765432
001BFC  98765432 98765432
001C04  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001C0C  98765432 98765432
001C14  98765432 98765432
001C1C  98765432 98765432
001C24  98765432 98765432
001C2C  98765432 98765432
001C34  98765432 98765432
001C3C  98765432 98765432
001C44  98765432 98765432
001C4C  98765432 98765432
001C54  98765432 98765432
001C5C  98765432 98765432
001C64  98765432 98765432
001C6C  98765432 98765432
001C74  98765432 98765432
001C7C  98765432 98765432
001C84  98765432 98765432
001C8C  98765432 98765432
001C94  98765432 98765432
001C9C  98765432 98765432
001CA4  98765432 98765432
001CAC  98765432 98765432
001CB4  98765432 98765432
001CBC  98765432 98765432
001CC4  98765432 98765432
001CCC  98765432 98765432
001CD4  98765432 98765432
001CDC  98765432 98765432
001CE4  98765432 98765432
001CEC  98765432 98765432
001CF4  98765432 98765432
001CFC  98765432 98765432
001D04  98765432 98765432
001D0C  98765432 98765432
001D14  98765432 98765432
001D1C  98765432 98765432
001D24  98765432 98765432
001D2C  98765432 98765432
001D34  98765432 98765432
001D3C  98765432 98765432
001D44  98765432 98765432
001D4C  98765432 98765432
001D54  98765432 98765432
001D5C  98765432 98765432
001D64  98765432 98765432
001D6C  98765432 98765432
001D74  98765432 98765432
001D7C  98765432 98765432
001D84  98765432 98765432
001D8C  98765432 98765432
001D94  98765432 98765432
001D9C  98765432 98765432
001DA4  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001DAC  98765432 98765432
001DB4  98765432 98765432
001DBC  98765432 98765432
001DC4  98765432 98765432
001DCC  98765432 98765432
001DD4  98765432 98765432
001DDC  98765432 98765432
001DE4  98765432 98765432
001DEC  98765432 98765432
001DF4  98765432 98765432
001DFC  98765432 98765432
001E04  98765432 98765432
001E0C  98765432 98765432
001E14  98765432 98765432
001E1C  98765432 98765432
001E24  98765432 98765432
001E2C  98765432 98765432
001E34  98765432 98765432
001E3C  98765432 98765432
001E44  98765432 98765432
001E4C  98765432 98765432
001E54  98765432 98765432
001E5C  98765432 98765432
001E64  98765432 98765432
001E6C  98765432 98765432
001E74  98765432 98765432
001E7C  98765432 98765432
001E84  98765432 98765432
001E8C  98765432 98765432
001E94  98765432 98765432
001E9C  98765432 98765432
001EA4  98765432 98765432
001EAC  98765432 98765432
001EB4  98765432 98765432
001EBC  98765432 98765432
001EC4  98765432 98765432
001ECC  98765432 98765432
001ED4  98765432 98765432
001EDC  98765432 98765432
001EE4  98765432 98765432
001EEC  98765432 98765432
001EF4  98765432 98765432
001EFC  98765432 98765432
001F04  98765432 98765432
001F0C  98765432 98765432
001F14  98765432 98765432
001F1C  98765432 98765432
001F24  98765432 98765432
001F2C  98765432 98765432
001F34  98765432 98765432
001F3C  98765432 98765432
001F44  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

001F4C  98765432 98765432
001F54  98765432 98765432
001F5C  98765432 98765432
001F64  98765432 98765432
001F6C  98765432 98765432
001F74  98765432 98765432
001F7C  98765432 98765432
001F84  98765432 98765432
001F8C  98765432 98765432
001F94  98765432 98765432
001F9C  98765432 98765432
001FA4  98765432 98765432
001FAC  98765432 98765432
001FB4  98765432 98765432
001FBC  98765432 98765432
001FC4  98765432 98765432
001FCC  98765432 98765432
001FD4  98765432 98765432
001FDC  98765432 98765432
001FE4  98765432 98765432
001FEC  98765432 98765432
001FF4  98765432 98765432
001FFC  98765432 98765432
002004  98765432 98765432
00200C  98765432 98765432
002014  98765432 98765432
00201C  98765432 98765432
002024  98765432 98765432
00202C  98765432 98765432
002034  98765432 98765432
00203C  98765432 98765432
002044  98765432 98765432
00204C  98765432 98765432
002054  98765432 98765432
00205C  98765432 98765432
002064  98765432 98765432
00206C  98765432 98765432
002074  98765432 98765432
00207C  98765432 98765432
002084  98765432 98765432
00208C  98765432 98765432
002094  98765432 98765432
00209C  98765432 98765432
0020A4  98765432 98765432
0020AC  98765432 98765432
0020B4  98765432 98765432
0020BC  98765432 98765432
0020C4  98765432 98765432
0020CC  98765432 98765432
0020D4  98765432 98765432
0020DC  98765432 98765432
0020E4  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0020EC  98765432 98765432
0020F4  98765432 98765432
0020FC  98765432 98765432
002104  98765432 98765432
00210C  98765432 98765432
002114  98765432 98765432
00211C  98765432 98765432
002124  98765432 98765432

00212C  98765432 98765432                4635 TRTO1L11 DC    256XL4'98765432',X'00110000',255XL4'98765432'    (CC1)
002134  98765432 98765432
00213C  98765432 98765432
002144  98765432 98765432
00214C  98765432 98765432
002154  98765432 98765432
00215C  98765432 98765432
002164  98765432 98765432
00216C  98765432 98765432
002174  98765432 98765432
00217C  98765432 98765432
002184  98765432 98765432
00218C  98765432 98765432
002194  98765432 98765432
00219C  98765432 98765432
0021A4  98765432 98765432
0021AC  98765432 98765432
0021B4  98765432 98765432
0021BC  98765432 98765432
0021C4  98765432 98765432
0021CC  98765432 98765432
0021D4  98765432 98765432
0021DC  98765432 98765432
0021E4  98765432 98765432
0021EC  98765432 98765432
0021F4  98765432 98765432
0021FC  98765432 98765432
002204  98765432 98765432
00220C  98765432 98765432
002214  98765432 98765432
00221C  98765432 98765432
002224  98765432 98765432
00222C  98765432 98765432
002234  98765432 98765432
00223C  98765432 98765432
002244  98765432 98765432
00224C  98765432 98765432
002254  98765432 98765432
00225C  98765432 98765432
002264  98765432 98765432
00226C  98765432 98765432
002274  98765432 98765432
00227C  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002284  98765432 98765432
00228C  98765432 98765432
002294  98765432 98765432
00229C  98765432 98765432
0022A4  98765432 98765432
0022AC  98765432 98765432
0022B4  98765432 98765432
0022BC  98765432 98765432
0022C4  98765432 98765432
0022CC  98765432 98765432
0022D4  98765432 98765432
0022DC  98765432 98765432
0022E4  98765432 98765432
0022EC  98765432 98765432
0022F4  98765432 98765432
0022FC  98765432 98765432
002304  98765432 98765432
00230C  98765432 98765432
002314  98765432 98765432
00231C  98765432 98765432
002324  98765432 98765432
00232C  98765432 98765432
002334  98765432 98765432
00233C  98765432 98765432
002344  98765432 98765432
00234C  98765432 98765432
002354  98765432 98765432
00235C  98765432 98765432
002364  98765432 98765432
00236C  98765432 98765432
002374  98765432 98765432
00237C  98765432 98765432
002384  98765432 98765432
00238C  98765432 98765432
002394  98765432 98765432
00239C  98765432 98765432
0023A4  98765432 98765432
0023AC  98765432 98765432
0023B4  98765432 98765432
0023BC  98765432 98765432
0023C4  98765432 98765432
0023CC  98765432 98765432
0023D4  98765432 98765432
0023DC  98765432 98765432
0023E4  98765432 98765432
0023EC  98765432 98765432
0023F4  98765432 98765432
0023FC  98765432 98765432
002404  98765432 98765432
00240C  98765432 98765432
002414  98765432 98765432
00241C  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002424  98765432 98765432
00242C  98765432 98765432
002434  98765432 98765432
00243C  98765432 98765432
002444  98765432 98765432
00244C  98765432 98765432
002454  98765432 98765432
00245C  98765432 98765432
002464  98765432 98765432
00246C  98765432 98765432
002474  98765432 98765432
00247C  98765432 98765432
002484  98765432 98765432
00248C  98765432 98765432
002494  98765432 98765432
00249C  98765432 98765432
0024A4  98765432 98765432
0024AC  98765432 98765432
0024B4  98765432 98765432
0024BC  98765432 98765432
0024C4  98765432 98765432
0024CC  98765432 98765432
0024D4  98765432 98765432
0024DC  98765432 98765432
0024E4  98765432 98765432
0024EC  98765432 98765432
0024F4  98765432 98765432
0024FC  98765432 98765432
002504  98765432 98765432
00250C  98765432 98765432
002514  98765432 98765432
00251C  98765432 98765432
002524  98765432 98765432
00252C  00110000 98765432
002534  98765432 98765432
00253C  98765432 98765432
002544  98765432 98765432
00254C  98765432 98765432
002554  98765432 98765432
00255C  98765432 98765432
002564  98765432 98765432
00256C  98765432 98765432
002574  98765432 98765432
00257C  98765432 98765432
002584  98765432 98765432
00258C  98765432 98765432
002594  98765432 98765432
00259C  98765432 98765432
0025A4  98765432 98765432
0025AC  98765432 98765432
0025B4  98765432 98765432
0025BC  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0025C4  98765432 98765432
0025CC  98765432 98765432
0025D4  98765432 98765432
0025DC  98765432 98765432
0025E4  98765432 98765432
0025EC  98765432 98765432
0025F4  98765432 98765432
0025FC  98765432 98765432
002604  98765432 98765432
00260C  98765432 98765432
002614  98765432 98765432
00261C  98765432 98765432
002624  98765432 98765432
00262C  98765432 98765432
002634  98765432 98765432
00263C  98765432 98765432
002644  98765432 98765432
00264C  98765432 98765432
002654  98765432 98765432
00265C  98765432 98765432
002664  98765432 98765432
00266C  98765432 98765432
002674  98765432 98765432
00267C  98765432 98765432
002684  98765432 98765432
00268C  98765432 98765432
002694  98765432 98765432
00269C  98765432 98765432
0026A4  98765432 98765432
0026AC  98765432 98765432
0026B4  98765432 98765432
0026BC  98765432 98765432
0026C4  98765432 98765432
0026CC  98765432 98765432
0026D4  98765432 98765432
0026DC  98765432 98765432
0026E4  98765432 98765432
0026EC  98765432 98765432
0026F4  98765432 98765432
0026FC  98765432 98765432
002704  98765432 98765432
00270C  98765432 98765432
002714  98765432 98765432
00271C  98765432 98765432
002724  98765432 98765432
00272C  98765432 98765432
002734  98765432 98765432
00273C  98765432 98765432
002744  98765432 98765432
00274C  98765432 98765432
002754  98765432 98765432
00275C  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002764  98765432 98765432
00276C  98765432 98765432
002774  98765432 98765432
00277C  98765432 98765432
002784  98765432 98765432
00278C  98765432 98765432
002794  98765432 98765432
00279C  98765432 98765432
0027A4  98765432 98765432
0027AC  98765432 98765432
0027B4  98765432 98765432
0027BC  98765432 98765432
0027C4  98765432 98765432
0027CC  98765432 98765432
0027D4  98765432 98765432
0027DC  98765432 98765432
0027E4  98765432 98765432
0027EC  98765432 98765432
0027F4  98765432 98765432
0027FC  98765432 98765432
002804  98765432 98765432
00280C  98765432 98765432
002814  98765432 98765432
00281C  98765432 98765432
002824  98765432 98765432
00282C  98765432 98765432
002834  98765432 98765432
00283C  98765432 98765432
002844  98765432 98765432
00284C  98765432 98765432
002854  98765432 98765432
00285C  98765432 98765432
002864  98765432 98765432
00286C  98765432 98765432
002874  98765432 98765432
00287C  98765432 98765432
002884  98765432 98765432
00288C  98765432 98765432
002894  98765432 98765432
00289C  98765432 98765432
0028A4  98765432 98765432
0028AC  98765432 98765432
0028B4  98765432 98765432
0028BC  98765432 98765432
0028C4  98765432 98765432
0028CC  98765432 98765432
0028D4  98765432 98765432
0028DC  98765432 98765432
0028E4  98765432 98765432
0028EC  98765432 98765432
0028F4  98765432 98765432
0028FC  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002904  98765432 98765432
00290C  98765432 98765432
002914  98765432 98765432
00291C  98765432 98765432
002924  98765432 98765432

00292C  98765432 98765432                  4637 TRTO1LF0 DC    511XL4'98765432',X'000000F0'    (CC1)
002934  98765432 98765432
00293C  98765432 98765432
002944  98765432 98765432
00294C  98765432 98765432
002954  98765432 98765432
00295C  98765432 98765432
002964  98765432 98765432
00296C  98765432 98765432
002974  98765432 98765432
00297C  98765432 98765432
002984  98765432 98765432
00298C  98765432 98765432
002994  98765432 98765432
00299C  98765432 98765432
0029A4  98765432 98765432
0029AC  98765432 98765432
0029B4  98765432 98765432
0029BC  98765432 98765432
0029C4  98765432 98765432
0029CC  98765432 98765432
0029D4  98765432 98765432
0029DC  98765432 98765432
0029E4  98765432 98765432
0029EC  98765432 98765432
0029F4  98765432 98765432
0029FC  98765432 98765432
002A04  98765432 98765432
002A0C  98765432 98765432
002A14  98765432 98765432
002A1C  98765432 98765432
002A24  98765432 98765432
002A2C  98765432 98765432
002A34  98765432 98765432
002A3C  98765432 98765432
002A44  98765432 98765432
002A4C  98765432 98765432
002A54  98765432 98765432
002A5C  98765432 98765432
002A64  98765432 98765432
002A6C  98765432 98765432
002A74  98765432 98765432
002A7C  98765432 98765432
002A84  98765432 98765432
002A8C  98765432 98765432
002A94  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002A9C  98765432 98765432
002AA4  98765432 98765432
002AAC  98765432 98765432
002AB4  98765432 98765432
002ABC  98765432 98765432
002AC4  98765432 98765432
002ACC  98765432 98765432
002AD4  98765432 98765432
002ADC  98765432 98765432
002AE4  98765432 98765432
002AEC  98765432 98765432
002AF4  98765432 98765432
002AFC  98765432 98765432
002B04  98765432 98765432
002B0C  98765432 98765432
002B14  98765432 98765432
002B1C  98765432 98765432
002B24  98765432 98765432
002B2C  98765432 98765432
002B34  98765432 98765432
002B3C  98765432 98765432
002B44  98765432 98765432
002B4C  98765432 98765432
002B54  98765432 98765432
002B5C  98765432 98765432
002B64  98765432 98765432
002B6C  98765432 98765432
002B74  98765432 98765432
002B7C  98765432 98765432
002B84  98765432 98765432
002B8C  98765432 98765432
002B94  98765432 98765432
002B9C  98765432 98765432
002BA4  98765432 98765432
002BAC  98765432 98765432
002BB4  98765432 98765432
002BBC  98765432 98765432
002BC4  98765432 98765432
002BCC  98765432 98765432
002BD4  98765432 98765432
002BDC  98765432 98765432
002BE4  98765432 98765432
002BEC  98765432 98765432
002BF4  98765432 98765432
002BFC  98765432 98765432
002C04  98765432 98765432
002C0C  98765432 98765432
002C14  98765432 98765432
002C1C  98765432 98765432
002C24  98765432 98765432
002C2C  98765432 98765432
002C34  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002C3C  98765432 98765432
002C44  98765432 98765432
002C4C  98765432 98765432
002C54  98765432 98765432
002C5C  98765432 98765432
002C64  98765432 98765432
002C6C  98765432 98765432
002C74  98765432 98765432
002C7C  98765432 98765432
002C84  98765432 98765432
002C8C  98765432 98765432
002C94  98765432 98765432
002C9C  98765432 98765432
002CA4  98765432 98765432
002CAC  98765432 98765432
002CB4  98765432 98765432
002CBC  98765432 98765432
002CC4  98765432 98765432
002CCC  98765432 98765432
002CD4  98765432 98765432
002CDC  98765432 98765432
002CE4  98765432 98765432
002CEC  98765432 98765432
002CF4  98765432 98765432
002CFC  98765432 98765432
002D04  98765432 98765432
002D0C  98765432 98765432
002D14  98765432 98765432
002D1C  98765432 98765432
002D24  98765432 98765432
002D2C  98765432 98765432
002D34  98765432 98765432
002D3C  98765432 98765432
002D44  98765432 98765432
002D4C  98765432 98765432
002D54  98765432 98765432
002D5C  98765432 98765432
002D64  98765432 98765432
002D6C  98765432 98765432
002D74  98765432 98765432
002D7C  98765432 98765432
002D84  98765432 98765432
002D8C  98765432 98765432
002D94  98765432 98765432
002D9C  98765432 98765432
002DA4  98765432 98765432
002DAC  98765432 98765432
002DB4  98765432 98765432
002DBC  98765432 98765432
002DC4  98765432 98765432
002DCC  98765432 98765432
002DD4  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002DDC  98765432 98765432
002DE4  98765432 98765432
002DEC  98765432 98765432
002DF4  98765432 98765432
002DFC  98765432 98765432
002E04  98765432 98765432
002E0C  98765432 98765432
002E14  98765432 98765432
002E1C  98765432 98765432
002E24  98765432 98765432
002E2C  98765432 98765432
002E34  98765432 98765432
002E3C  98765432 98765432
002E44  98765432 98765432
002E4C  98765432 98765432
002E54  98765432 98765432
002E5C  98765432 98765432
002E64  98765432 98765432
002E6C  98765432 98765432
002E74  98765432 98765432
002E7C  98765432 98765432
002E84  98765432 98765432
002E8C  98765432 98765432
002E94  98765432 98765432
002E9C  98765432 98765432
002EA4  98765432 98765432
002EAC  98765432 98765432
002EB4  98765432 98765432
002EBC  98765432 98765432
002EC4  98765432 98765432
002ECC  98765432 98765432
002ED4  98765432 98765432
002EDC  98765432 98765432
002EE4  98765432 98765432
002EEC  98765432 98765432
002EF4  98765432 98765432
002EFC  98765432 98765432
002F04  98765432 98765432
002F0C  98765432 98765432
002F14  98765432 98765432
002F1C  98765432 98765432
002F24  98765432 98765432
002F2C  98765432 98765432
002F34  98765432 98765432
002F3C  98765432 98765432
002F44  98765432 98765432
002F4C  98765432 98765432
002F54  98765432 98765432
002F5C  98765432 98765432
002F64  98765432 98765432
002F6C  98765432 98765432
002F74  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

002F7C  98765432 98765432
002F84  98765432 98765432
002F8C  98765432 98765432
002F94  98765432 98765432
002F9C  98765432 98765432
002FA4  98765432 98765432
002FAC  98765432 98765432
002FB4  98765432 98765432
002FBC  98765432 98765432
002FC4  98765432 98765432
002FCC  98765432 98765432
002FD4  98765432 98765432
002FDC  98765432 98765432
002FE4  98765432 98765432
002FEC  98765432 98765432
002FF4  98765432 98765432
002FFC  98765432 98765432
003004  98765432 98765432
00300C  98765432 98765432
003014  98765432 98765432
00301C  98765432 98765432
003024  98765432 98765432
00302C  98765432 98765432
003034  98765432 98765432
00303C  98765432 98765432
003044  98765432 98765432
00304C  98765432 98765432
003054  98765432 98765432
00305C  98765432 98765432
003064  98765432 98765432
00306C  98765432 98765432
003074  98765432 98765432
00307C  98765432 98765432
003084  98765432 98765432
00308C  98765432 98765432
003094  98765432 98765432
00309C  98765432 98765432
0030A4  98765432 98765432
0030AC  98765432 98765432
0030B4  98765432 98765432
0030BC  98765432 98765432
0030C4  98765432 98765432
0030CC  98765432 98765432
0030D4  98765432 98765432
0030DC  98765432 98765432
0030E4  98765432 98765432
0030EC  98765432 98765432
0030F4  98765432 98765432
0030FC  98765432 98765432
003104  98765432 98765432
00310C  98765432 98765432
003114  98765432 98765432
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

00311C  98765432 98765432
003124  98765432 000000F0
                                            4638
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                      4640 ********************************************************************************
                                      4641 *        Function Code (FC) Tables (GR1)
                                      4642 ********************************************************************************

00312C  00000000 00000000             4644 TRTOP20  DC    256X'00'            no stop
003134  00000000 00000000
00313C  00000000 00000000
003144  00000000 00000000
00314C  00000000 00000000
003154  00000000 00000000
00315C  00000000 00000000
003164  00000000 00000000
00316C  00000000 00000000
003174  00000000 00000000
00317C  00000000 00000000
003184  00000000 00000000
00318C  00000000 00000000
003194  00000000 00000000
00319C  00000000 00000000
0031A4  00000000 00000000
0031AC  00000000 00000000
0031B4  00000000 00000000
0031BC  00000000 00000000
0031C4  00000000 00000000
0031CC  00000000 00000000
0031D4  00000000 00000000
0031DC  00000000 00000000
0031E4  00000000 00000000
0031EC  00000000 00000000
0031F4  00000000 00000000
0031FC  00000000 00000000
003204  00000000 00000000
00320C  00000000 00000000
003214  00000000 00000000
00321C  00000000 00000000
003224  00000000 00000000
00322C                 00322C  02322C  4645          ORG   *+2*K64

02322C  00000000 00000000             4647 TRTOP211 DC    17X'00',X'11',238X'00'     stop on X'11'
023234  00000000 00000000
02323C  00110000 00000000
023244  00000000 00000000
02324C  00000000 00000000
023254  00000000 00000000
02325C  00000000 00000000
023264  00000000 00000000
02326C  00000000 00000000
023274  00000000 00000000
02327C  00000000 00000000
023284  00000000 00000000
02328C  00000000 00000000
023294  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

02329C  00000000 00000000
0232A4  00000000 00000000
0232AC  00000000 00000000
0232B4  00000000 00000000
0232BC  00000000 00000000
0232C4  00000000 00000000
0232CC  00000000 00000000
0232D4  00000000 00000000
0232DC  00000000 00000000
0232E4  00000000 00000000
0232EC  00000000 00000000
0232F4  00000000 00000000
0232FC  00000000 00000000
023304  00000000 00000000
02330C  00000000 00000000
023314  00000000 00000000
02331C  00000000 00000000
023324  00000000 00000000

02332C  00000000 00000000                  4649 TRTOP2F0 DC    240X'00',X'F0',15X'00'     stop on X'F0'
023334  00000000 00000000
02333C  00000000 00000000
023344  00000000 00000000
02334C  00000000 00000000
023354  00000000 00000000
02335C  00000000 00000000
023364  00000000 00000000
02336C  00000000 00000000
023374  00000000 00000000
02337C  00000000 00000000
023384  00000000 00000000
02338C  00000000 00000000
023394  00000000 00000000
02339C  00000000 00000000
0233A4  00000000 00000000
0233AC  00000000 00000000
0233B4  00000000 00000000
0233BC  00000000 00000000
0233C4  00000000 00000000
0233CC  00000000 00000000
0233D4  00000000 00000000
0233DC  00000000 00000000
0233E4  00000000 00000000
0233EC  00000000 00000000
0233F4  00000000 00000000
0233FC  00000000 00000000
023404  00000000 00000000
02340C  00000000 00000000
023414  00000000 00000000
02341C  F0000000 00000000
023424  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

02342C  00000000 00000000                  4651 TRTOP411 DC    34X'00',X'0011',476X'00'   stop on X'11'
023434  00000000 00000000
02343C  00000000 00000000
023444  00000000 00000000
02344C  00000011 00000000
023454  00000000 00000000
02345C  00000000 00000000
023464  00000000 00000000
02346C  00000000 00000000
023474  00000000 00000000
02347C  00000000 00000000
023484  00000000 00000000
02348C  00000000 00000000
023494  00000000 00000000
02349C  00000000 00000000
0234A4  00000000 00000000
0234AC  00000000 00000000
0234B4  00000000 00000000
0234BC  00000000 00000000
0234C4  00000000 00000000
0234CC  00000000 00000000
0234D4  00000000 00000000
0234DC  00000000 00000000
0234E4  00000000 00000000
0234EC  00000000 00000000
0234F4  00000000 00000000
0234FC  00000000 00000000
023504  00000000 00000000
02350C  00000000 00000000
023514  00000000 00000000
02351C  00000000 00000000
023524  00000000 00000000
02352C  00000000 00000000
023534  00000000 00000000
02353C  00000000 00000000
023544  00000000 00000000
02354C  00000000 00000000
023554  00000000 00000000
02355C  00000000 00000000
023564  00000000 00000000
02356C  00000000 00000000
023574  00000000 00000000
02357C  00000000 00000000
023584  00000000 00000000
02358C  00000000 00000000
023594  00000000 00000000
02359C  00000000 00000000
0235A4  00000000 00000000
0235AC  00000000 00000000
0235B4  00000000 00000000
0235BC  00000000 00000000
0235C4  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0235CC  00000000 00000000
0235D4  00000000 00000000
0235DC  00000000 00000000
0235E4  00000000 00000000
0235EC  00000000 00000000
0235F4  00000000 00000000
0235FC  00000000 00000000
023604  00000000 00000000
02360C  00000000 00000000
023614  00000000 00000000
02361C  00000000 00000000
023624  00000000 00000000

02362C  00000000 00000000                  4653 TRTOP4F0 DC    480X'00',X'00F0',30X'00'     stop on X'F0'
023634  00000000 00000000
02363C  00000000 00000000
023644  00000000 00000000
02364C  00000000 00000000
023654  00000000 00000000
02365C  00000000 00000000
023664  00000000 00000000
02366C  00000000 00000000
023674  00000000 00000000
02367C  00000000 00000000
023684  00000000 00000000
02368C  00000000 00000000
023694  00000000 00000000
02369C  00000000 00000000
0236A4  00000000 00000000
0236AC  00000000 00000000
0236B4  00000000 00000000
0236BC  00000000 00000000
0236C4  00000000 00000000
0236CC  00000000 00000000
0236D4  00000000 00000000
0236DC  00000000 00000000
0236E4  00000000 00000000
0236EC  00000000 00000000
0236F4  00000000 00000000
0236FC  00000000 00000000
023704  00000000 00000000
02370C  00000000 00000000
023714  00000000 00000000
02371C  00000000 00000000
023724  00000000 00000000
02372C  00000000 00000000
023734  00000000 00000000
02373C  00000000 00000000
023744  00000000 00000000
02374C  00000000 00000000
023754  00000000 00000000
02375C  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

023764  00000000 00000000
02376C  00000000 00000000
023774  00000000 00000000
02377C  00000000 00000000
023784  00000000 00000000
02378C  00000000 00000000
023794  00000000 00000000
02379C  00000000 00000000
0237A4  00000000 00000000
0237AC  00000000 00000000
0237B4  00000000 00000000
0237BC  00000000 00000000
0237C4  00000000 00000000
0237CC  00000000 00000000
0237D4  00000000 00000000
0237DC  00000000 00000000
0237E4  00000000 00000000
0237EC  00000000 00000000
0237F4  00000000 00000000
0237FC  00000000 00000000
023804  00000000 00000000
02380C  00F00000 00000000
023814  00000000 00000000
02381C  00000000 00000000
023824  00000000 00000000
                                      4654
02382C  00000000 00000000            4655 TRTOP811 DC    17X'00',X'11',238X'00'       stop on X'11'
023834  00000000 00000000
02383C  00110000 00000000
023844  00000000 00000000
02384C  00000000 00000000
023854  00000000 00000000
02385C  00000000 00000000
023864  00000000 00000000
02386C  00000000 00000000
023874  00000000 00000000
02387C  00000000 00000000
023884  00000000 00000000
02388C  00000000 00000000
023894  00000000 00000000
02389C  00000000 00000000
0238A4  00000000 00000000
0238AC  00000000 00000000
0238B4  00000000 00000000
0238BC  00000000 00000000
0238C4  00000000 00000000
0238CC  00000000 00000000
0238D4  00000000 00000000
0238DC  00000000 00000000
0238E4  00000000 00000000
0238EC  00000000 00000000
0238F4  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0238FC  00000000 00000000
023904  00000000 00000000
02390C  00000000 00000000
023914  00000000 00000000
02391C  00000000 00000000
023924  00000000 00000000
02392C                   02392C  04392C  4656          ORG   *+2*K64
                                         4657
04392C  00000000 00000000                4658 TRTOP8F0 DC    240X'00',X'F0',15X'00'     stop on X'F0'
043934  00000000 00000000
04393C  00000000 00000000
043944  00000000 00000000
04394C  00000000 00000000
043954  00000000 00000000
04395C  00000000 00000000
043964  00000000 00000000
04396C  00000000 00000000
043974  00000000 00000000
04397C  00000000 00000000
043984  00000000 00000000
04398C  00000000 00000000
043994  00000000 00000000
04399C  00000000 00000000
0439A4  00000000 00000000
0439AC  00000000 00000000
0439B4  00000000 00000000
0439BC  00000000 00000000
0439C4  00000000 00000000
0439CC  00000000 00000000
0439D4  00000000 00000000
0439DC  00000000 00000000
0439E4  00000000 00000000
0439EC  00000000 00000000
0439F4  00000000 00000000
0439FC  00000000 00000000
043A04  00000000 00000000
043A0C  00000000 00000000
043A14  00000000 00000000
043A1C  F0000000 00000000
043A24  00000000 00000000
043A2C                   043A2C  063A2C  4659          ORG   *+2*K64
                                         4660
063A2C  00000000 00000000                4661 TRTOP8F1 DC    240X'00',X'00',X'F1',14X'00'     stop on X'F1'
063A34  00000000 00000000
063A3C  00000000 00000000
063A44  00000000 00000000
063A4C  00000000 00000000
063A54  00000000 00000000
063A5C  00000000 00000000
063A64  00000000 00000000
063A6C  00000000 00000000
063A74  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

063A7C  00000000 00000000
063A84  00000000 00000000
063A8C  00000000 00000000
063A94  00000000 00000000
063A9C  00000000 00000000
063AA4  00000000 00000000
063AAC  00000000 00000000
063AB4  00000000 00000000
063ABC  00000000 00000000
063AC4  00000000 00000000
063ACC  00000000 00000000
063AD4  00000000 00000000
063ADC  00000000 00000000
063AE4  00000000 00000000
063AEC  00000000 00000000
063AF4  00000000 00000000
063AFC  00000000 00000000
063B04  00000000 00000000
063B0C  00000000 00000000
063B14  00000000 00000000
063B1C  00F10000 00000000
063B24  00000000 00000000
063B2C                   063B2C  083B2C  4662          ORG   *+2*K64
                                         4663
083B2C  00000000 00000000                4664 TRTOPC11 DC    34X'00',X'0011'        stop on X'11'
083B34  00000000 00000000
083B3C  00000000 00000000
083B44  00000000 00000000
083B4C  00000011
083B50                   083B50  0A3B50  4665          ORG   *+2*K64
                                         4666
                                         4667
0A3B50  00000000 00000000                4668 TRTOPCF0 DC    480X'00',X'00F0',28X'00'     stop on X'F0'
0A3B58  00000000 00000000
0A3B60  00000000 00000000
0A3B68  00000000 00000000
0A3B70  00000000 00000000
0A3B78  00000000 00000000
0A3B80  00000000 00000000
0A3B88  00000000 00000000
0A3B90  00000000 00000000
0A3B98  00000000 00000000
0A3BA0  00000000 00000000
0A3BA8  00000000 00000000
0A3BB0  00000000 00000000
0A3BB8  00000000 00000000
0A3BC0  00000000 00000000
0A3BC8  00000000 00000000
0A3BD0  00000000 00000000
0A3BD8  00000000 00000000
0A3BE0  00000000 00000000
0A3BE8  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0A3BF0  00000000 00000000
0A3BF8  00000000 00000000
0A3C00  00000000 00000000
0A3C08  00000000 00000000
0A3C10  00000000 00000000
0A3C18  00000000 00000000
0A3C20  00000000 00000000
0A3C28  00000000 00000000
0A3C30  00000000 00000000
0A3C38  00000000 00000000
0A3C40  00000000 00000000
0A3C48  00000000 00000000
0A3C50  00000000 00000000
0A3C58  00000000 00000000
0A3C60  00000000 00000000
0A3C68  00000000 00000000
0A3C70  00000000 00000000
0A3C78  00000000 00000000
0A3C80  00000000 00000000
0A3C88  00000000 00000000
0A3C90  00000000 00000000
0A3C98  00000000 00000000
0A3CA0  00000000 00000000
0A3CA8  00000000 00000000
0A3CB0  00000000 00000000
0A3CB8  00000000 00000000
0A3CC0  00000000 00000000
0A3CC8  00000000 00000000
0A3CD0  00000000 00000000
0A3CD8  00000000 00000000
0A3CE0  00000000 00000000
0A3CE8  00000000 00000000
0A3CF0  00000000 00000000
0A3CF8  00000000 00000000
0A3D00  00000000 00000000
0A3D08  00000000 00000000
0A3D10  00000000 00000000
0A3D18  00000000 00000000
0A3D20  00000000 00000000
0A3D28  00000000 00000000
0A3D30  00F00000 00000000
0A3D38  00000000 00000000
0A3D40  00000000 00000000
0A3D48  00000000 0000
0A3D4E                   0A3D4E  0C3D4E  4669          ORG    *+2*K64
                                         4670
0C3D4E  00000000 00000000                4671 TRTOPCF1 DC     480X'00',X'0000',X'00F1',28X'00'     stop on X'F1'
0C3D56  00000000 00000000
0C3D5E  00000000 00000000
0C3D66  00000000 00000000
0C3D6E  00000000 00000000
0C3D76  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0C3D7E  00000000 00000000
0C3D86  00000000 00000000
0C3D8E  00000000 00000000
0C3D96  00000000 00000000
0C3D9E  00000000 00000000
0C3DA6  00000000 00000000
0C3DAE  00000000 00000000
0C3DB6  00000000 00000000
0C3DBE  00000000 00000000
0C3DC6  00000000 00000000
0C3DCE  00000000 00000000
0C3DD6  00000000 00000000
0C3DDE  00000000 00000000
0C3DE6  00000000 00000000
0C3DEE  00000000 00000000
0C3DF6  00000000 00000000
0C3DFE  00000000 00000000
0C3E06  00000000 00000000
0C3E0E  00000000 00000000
0C3E16  00000000 00000000
0C3E1E  00000000 00000000
0C3E26  00000000 00000000
0C3E2E  00000000 00000000
0C3E36  00000000 00000000
0C3E3E  00000000 00000000
0C3E46  00000000 00000000
0C3E4E  00000000 00000000
0C3E56  00000000 00000000
0C3E5E  00000000 00000000
0C3E66  00000000 00000000
0C3E6E  00000000 00000000
0C3E76  00000000 00000000
0C3E7E  00000000 00000000
0C3E86  00000000 00000000
0C3E8E  00000000 00000000
0C3E96  00000000 00000000
0C3E9E  00000000 00000000
0C3EA6  00000000 00000000
0C3EAE  00000000 00000000
0C3EB6  00000000 00000000
0C3EBE  00000000 00000000
0C3EC6  00000000 00000000
0C3ECE  00000000 00000000
0C3ED6  00000000 00000000
0C3EDE  00000000 00000000
0C3EE6  00000000 00000000
0C3EEE  00000000 00000000
0C3EF6  00000000 00000000
0C3EFE  00000000 00000000
0C3F06  00000000 00000000
0C3F0E  00000000 00000000
0C3F16  00000000 00000000
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

0C3F1E  00000000 00000000
0C3F26  00000000 00000000
0C3F2E  000000F1 00000000
0C3F36  00000000 00000000
0C3F3E  00000000 00000000
0C3F46  00000000 00000000
0C3F4E                   0C3F4E  0E3F4E  4672          ORG   *+2*K64
```

```
 LOC       OBJECT CODE      ADDR1   ADDR2   STMT

                                      4674 ********************************************************************************
                                      4675 *        (other DSECTS needed by SATK)
                                      4676 ********************************************************************************

                                      4678          DSECTS PRINT=OFF,NAME=(ASA)
                                      4841          PRINT ON

                                      4843 ********************************************************************************
                                      4844 *        Register equates
                                      4845 ********************************************************************************


                           000000  000001  4847 R0        EQU   0
                           000001  000001  4848 R1        EQU   1
                           000002  000001  4849 R2        EQU   2
                           000003  000001  4850 R3        EQU   3
                           000004  000001  4851 R4        EQU   4
                           000005  000001  4852 R5        EQU   5
                           000006  000001  4853 R6        EQU   6
                           000007  000001  4854 R7        EQU   7
                           000008  000001  4855 R8        EQU   8
                           000009  000001  4856 R9        EQU   9
                           00000A  000001  4857 R10       EQU   10
                           00000B  000001  4858 R11       EQU   11
                           00000C  000001  4859 R12       EQU   12
                           00000D  000001  4860 R13       EQU   13
                           00000E  000001  4861 R14       EQU   14
                           00000F  000001  4862 R15       EQU   15
```

```
                                      4864          END
```

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
ASA	4	00000000	512	4682	3533
ASBEGIN	U	00000000	1	4683	4688 4730 4766 4775 4793 4800 4806 4810 4814 4820 4837
ASEND	U	00000200	1	4836	4837
ASLENGTH	U	00000200	1	4837	
BCEXTCOD	H	0000001A	2	4700	
BCIOCOD	H	0000003A	2	4708	
BCMCKCOD	H	00000032	2	4706	
BCPGMCOD	H	0000002A	2	4704	
BCSVCCOD	H	00000022	2	4702	
BEGIN	I	00000200	2	3537	3565 3507 3534 3535 3649
CAW	F	00000048	4	4712	
CAWADDR	R	00000049	3	4715	
CAWKEY	X	00000048	1	4713	
CAWSUSP	U	00000008	1	4714	
CHANID	F	000000A8	4	4767	
CODE	2	00000000	933710	3488	
CPUID	U	0000031B	1	4839	
CSW	F	00000040	8	4711	
DWAT0008	3	000005D0	8	3659	3658
DWAT0009	3	000005E0	8	3664	3663
ENDREGS	A	00000028	4	3704	3621
EOJ	H	000005C8	2	3657	3559
EXTCPUAD	H	00000084	2	4732	
EXTICODE	H	00000086	2	4733	
EXTIPARM	F	00000080	4	4731	
EXTNPSW	F	00000058	8	4721	
EXTOPSW	F	00000018	8	4693	4699
F12T11	F	000013BC	4	4592	
F12T11A	F	000013F0	4	4604	
F12T8	F	00001354	4	4568	
F12T8A	F	00001388	4	4580	
FAILMASK	A	00000024	4	3701	3609
FAILTEST	H	000005D8	2	3662	3554 3557 3640
IMAGE	1	00000000	933710	0	
IOELADDR	F	000000AC	4	4768	
IOICODE	H	000000BA	2	4773	
IOIID	F	000000C0	4	4778	
IOIPARM	F	000000BC	4	4777	
IONPSW	F	00000078	8	4725	
IOOPSW	F	00000038	8	4697	4707
IOSSID	F	000000B8	4	4776	
IPLCCW1	F	00000008	8	4685	
IPLCCW2	F	00000010	8	4686	
IPLPSW	F	00000000	8	4684	
K	U	00000400	1	3673	3674 3675 3676
K64	U	00010000	1	3675	4645 4656 4659 4662 4665 4669 4672 3733 3736 3745 3748 3757 3760 3769 3772 3781 3784 3793 3796 3805 3808 3818 3821 3830 3833 3842 3845 3854 3857 3871 3874 3883 3886 3895 3898 3907 3910 3919 3922 3931 3934 3943 3946 3955 3958 3967 3970 3979 3982 3991 3994 4008 4010 4013 4021 4023 4026 4033 4035 4038 4045 4047 4050 4057 4059 4062 4069 4071 4074 4081 4083 4086 4093 4095 4098 4105 4107 4110 4117 4119 4122 4129 4131 4134 4152 4155 4164 4167 4176 4179 4188

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
					4191 4200 4203 4212 4215 4224 4227 4236 4239 4248 4251 4260 4263
					4272 4275 4290 4292 4295 4303 4305 4308 4315 4317 4320 4327 4329
					4332 4339 4341 4344 4351 4353 4356 4363 4365 4368 4375 4377 4380
					4387 4389 4392 4399 4401 4404 4411 4413 4416 4434 4437 4446 4449
					4458 4461 4470 4473 4482 4485 4494 4497 4506 4509 4518 4521 4530
					4533 4542 4545 4554 4557 4573 4575 4578 4585 4587 4590 4597 4599
					4602 4609 4611 4614
LCHANLOG	F	000000B0	4	4769	
M0T1	F	000005EC	4	3726	
M0T10	F	000007C0	4	3835	
M0T11	F	000007F4	4	3847	
M0T2	F	00000620	4	3738	
M0T3	F	00000654	4	3750	
M0T4	F	00000688	4	3762	
M0T5	F	000006BC	4	3774	
M0T6	F	000006F0	4	3786	
M0T7	F	00000724	4	3798	
M0T8	F	00000758	4	3811	
M0T9	F	0000078C	4	3823	
M10T1	F	00000CA0	4	4145	
M10T10	F	00000E74	4	4253	
M10T11	F	00000EA8	4	4265	
M10T2	F	00000CD4	4	4157	
M10T3	F	00000D08	4	4169	
M10T4	F	00000D3C	4	4181	
M10T5	F	00000D70	4	4193	
M10T6	F	00000DA4	4	4205	
M10T7	F	00000DD8	4	4217	
M10T8	F	00000E0C	4	4229	
M10T9	F	00000E40	4	4241	
M12T1	F	00000EDC	4	4285	
M12T10	F	000010B0	4	4394	
M12T11	F	000010E4	4	4406	
M12T2	F	00000F10	4	4298	
M12T3	F	00000F44	4	4310	
M12T4	F	00000F78	4	4322	
M12T5	F	00000FAC	4	4334	
M12T6	F	00000FE0	4	4346	
M12T7	F	00001014	4	4358	
M12T8	F	00001048	4	4370	
M12T9	F	0000107C	4	4382	
M14T1	F	00001118	4	4427	
M14T10	F	000012EC	4	4535	
M14T11	F	00001320	4	4547	
M14T2	F	0000114C	4	4439	
M14T3	F	00001180	4	4451	
M14T4	F	000011B4	4	4463	
M14T5	F	000011E8	4	4475	
M14T6	F	0000121C	4	4487	
M14T7	F	00001250	4	4499	
M14T8	F	00001284	4	4511	
M14T9	F	000012B8	4	4523	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
M3	X	00000003	1	3688	3606
M4T1	F	00000828	4	3864	
M4T10	F	000009FC	4	3972	
M4T11	F	00000A30	4	3984	
M4T2	F	0000085C	4	3876	
M4T3	F	00000890	4	3888	
M4T4	F	000008C4	4	3900	
M4T5	F	000008F8	4	3912	
M4T6	F	0000092C	4	3924	
M4T7	F	00000960	4	3936	
M4T8	F	00000994	4	3948	
M4T9	F	000009C8	4	3960	
M8T1	F	00000A64	4	4003	
M8T10	F	00000C38	4	4112	
M8T11	F	00000C6C	4	4124	
M8T2	F	00000A98	4	4016	
M8T3	F	00000ACC	4	4028	
M8T4	F	00000B00	4	4040	
M8T5	F	00000B34	4	4052	
M8T6	F	00000B68	4	4064	
M8T7	F	00000B9C	4	4076	
M8T8	F	00000BD0	4	4088	
M8T9	F	00000C04	4	4100	
MB	U	00100000	1	3676	3733 3736 3745 3748 3757 3760 3769 3772 3781 3784 3793 3796 3805 3808 3818 3821 3830 3833 3842 3845 3854 3857 3871 3874 3883 3886 3895 3898 3907 3910 3919 3922 3931 3934 3943 3946 3955 3958 3967 3970 3979 3982 3991 3994 4010 4013 4023 4026 4035 4038 4047 4050 4059 4062 4071 4074 4083 4086 4095 4098 4107 4110 4119 4122 4131 4134 4152 4155 4164 4167 4176 4179 4188 4191 4200 4203 4212 4215 4224 4227 4236 4239 4248 4251 4260 4263 4272 4275 4292 4295 4305 4308 4317 4320 4329 4332 4341 4344 4353 4356 4365 4368 4377 4380 4389 4392 4401 4404 4413 4416 4434 4437 4446 4449 4458 4461 4470 4473 4482 4485 4494 4497 4506 4509 4518 4521 4530 4533 4542 4545 4554 4557 4575 4578 4587 4590 4599 4602 4611 4614
MCKLOG	F	00000100	4	4801	
MCKNPSW	F	00000070	8	4724	
MCKOPSW	F	00000030	8	4696	4705
MEASUREB	X	000000B9	1	4772	
MKARCHMD	X	000000A3	1	4760	
MKARS	F	00000120	4	4799	
MKCLKCMP	F	000000E0	8	4785	
MKCPUTIM	F	000000D8	8	4784	
MKCRS	F	000001C0	4	4804	
MKDMGCOD	F	000000F4	4	4788	
MKFAILA	F	000000F8	4	4790	
MKFPRS	D	00000160	8	4802	
MKICODE	F	000000E8	4	4786	
MKLOGOUT	F	00000100	4	4792	
MKMODEL	F	000000FC	4	4791	
MKXSAA	F	000000D4	4	4783	
MONCLS	H	00000094	2	4748	
MONCODE	F	0000009C	4	4755	

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
MONNUMBR	X	00000095	1	4750	
MPGACCID	X	000000A2	1	4758	
NKGRS	F	00000180	4	4803	
OP1DATA	A	00000004	4	3690	3591
OP1LEN	F	00000008	4	3691	3589 3592
OP1WHERE	A	00000018	4	3697	3588
OP1WLEN	F	0000001C	4	3698	3590
OP2DATA	A	0000000C	4	3692	3597
OP2LEN	F	00000010	4	3693	3596 3598
OP2WHERE	A	00000014	4	3696	3595
OPSWHERE	U	00000014	1	3695	3603
PAGE	U	00001000	1	3674	
PCFETO	A	000000C4	4	4779	
PERACCID	X	000000A1	1	4757	
PERADDR	F	00000098	4	4754	
PERCODE	X	00000096	1	4751	
PERCODMK	U	000000F0	1	4752	
PGMACCID	X	000000A0	1	4756	
PGMDXC	F	00000090	4	4746	
PGMICODE	H	0000008E	2	4745	
PGMIID	F	0000008C	4	4741	
PGMIILC	X	0000008D	1	4743	
PGMIILCM	U	0000000C	1	4744	
PGMNPSW	F	00000068	8	4723	
PGMOPSW	F	00000028	8	4695	4703
PGMTRX	F	00000090	4	4747	
R0	U	00000000	1	4847	3533
R1	U	00000001	1	4848	3603 3615
R10	U	0000000A	1	4857	3588 3593 3595 3599 3621 3624
R11	U	0000000B	1	4858	3589 3590 3596 3609 3610 3616 3628
R12	U	0000000C	1	4859	3621 3632
R13	U	0000000D	1	4860	
R14	U	0000000E	1	4861	3546 3640 3641
R15	U	0000000F	1	4862	3648
R2	U	00000002	1	4849	3613 3624
R3	U	00000003	1	4850	3628
R4	U	00000004	1	4851	3603 3613 3615 3632
R5	U	00000005	1	4852	3579 3580 3635 3636 3647
R6	U	00000006	1	4853	3583 3584 3591 3593 3597 3599
R7	U	00000007	1	4854	3592 3598 3605 3606 3607
R8	U	00000008	1	4855	3534 3537 3538 3539 3541 3649
R9	U	00000009	1	4856	3535 3541 3542
REG2LOW	U	000000DD	1	3711	
REG2PATT	U	AABBCCDD	1	3710	3734 3746 3758 3770 3782 3794 3806 3819 3831 3843 3855 3872 3884 3896 3908 3920 3932 3944 3956 3968 3980 3992 4011 4024 4036 4048 4060 4072 4084 4096 4108 4120 4132 4153 4165 4177 4189 4201 4213 4225 4237 4249 4261 4273 4293 4306 4318 4330 4342 4354 4366 4378 4390 4402 4414 4435 4447 4459 4471 4483 4495 4507 4519 4531 4543 4555 4576 4588 4600 4612
RSTNPSW	F	00000000	8	4689	
RSTOPSW	F	00000008	8	4690	
SAVETRT	D	000005A8	8	3645	3615

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
SCANOUT	X	00000080	1	4727	4728
SCANOUTL	U	00000000	1	4728	
SSARCHMD	X	000000A3	1	4759	
SSARS	F	00000120	4	4815	
SSCLKCMP	F	000000E0	8	4809	
SSCPUTIM	F	000000D8	8	4808	
SSCRS	F	000001C0	4	4818	
SSFPRS	D	00000160	8	4816	
SSGRS	F	00000180	4	4817	
SSMODEL	F	0000010C	4	4813	
SSPREFIX	F	00000108	4	4812	
SSPSW	F	00000100	8	4811	
SSXSAA	A	000000D4	4	4807	
STFLDATA	F	000000C8	4	4780	
SUBTEST	X	00000401	1	3569	3556 3612 3623 3627 3631
SVCICODE	H	0000008A	2	4739	
SVCIID	F	00000088	4	4735	
SVCIILC	X	00000089	1	4737	
SVCIILCM	U	0000000C	1	4738	
SVCNPSW	F	00000060	8	4722	
SVCOPSW	F	00000020	8	4694	4701
TEST01	I	00000502	4	3577	3546
TESTADDR	D	00000400	8	3567	
TESTNUM	X	00000400	1	3568	3553 3577 3584
TIMER	F	00000050	4	4718	
TNUM	X	00000000	1	3685	3583
TRTE1TST	J	00000000	933710	3488	3491 3498 3506 3508
TRTEBC	I	0000059E	4	3643	3616
TRTECTL	A	000005EC	4	3719	3579
TRTEDONE	I	0000059C	2	3641	3638
TRTEFAIL	I	00000598	4	3640	3625 3629 3633 3643
TRTEMOD	I	00000554	4	3613	3607 3617
TRTENEXT	U	00000034	1	3708	3635
TRTETEST	4	00000000	52	3684	3580
TRTO1L0	X	0000192C	4	4633	3827 3964 4104 4245 4386 4527
TRTO1L11	X	0000212C	4	4635	3839 3976 4116 4257 4398 4539
TRTO1LF0	X	0000292C	4	4637	3851 3988 4128 4269 4410 4551 4596 4608
TRTOP10	X	0000142C	4	4625	3730 3742 3754 3766 3778 3868 3880 3892 3904 3916 4007 4020 4032
					4044 4149 4161 4173 4185 4289 4302 4314 4326 4431 4443 4455 4467
TRTOP111	X	0000152C	4	4627	3790 3815 3928 3952 4056 4080 4197 4221 4338 4362 4479 4503
TRTOP1F0	X	0000162C	4	4629	3802 3940 4068 4209 4350 4491
TRTOP1F1	X	0000172C	4	4631	4092 4233 4374 4515 4572 4584
TRTOP20	X	0000312C	1	4644	3731 3743 3755 3767 3779 3828 3869 3881 3893 3905 3917 3965 4008
					4021 4033 4045 4105 4150 4162 4174 4186 4246 4290 4303 4315 4327
					4387 4432 4444 4456 4468 4528
TRTOP211	X	0002322C	1	4647	3791 3816 3840 4198 4222 4258
TRTOP2F0	X	0002332C	1	4649	3803 3852 4210 4270
TRTOP411	X	0002342C	1	4651	3929 3953 3977 4480 4504 4540
TRTOP4F0	X	0002362C	1	4653	3941 3989 4492 4552
TRTOP811	X	0002382C	1	4655	4057 4081 4117
TRTOP8F0	X	0004392C	1	4658	4069 4129
TRTOP8F1	X	00063A2C	1	4661	4093 4234

SYMBOL	TYPE	VALUE	LENGTH	DEFN	REFERENCES
TRTOPC11	X	00083B2C	1	4664	4339 4363 4399
TRTOPCF0	X	000A3B50	1	4668	4351 4411 4597 4609
TRTOPCF1	X	000C3D4E	1	4671	4375 4516 4573 4585
TST1LOOP	U	0000050A	1	3582	3637
TTDES	F	00000054	4	4719	
UA0	F	00000010	8	4691	
UA1	F	0000004C	4	4716	
UA2	F	000000A4	4	4761	
UA3	F	000000B4	4	4770	
UA4	X	000000B8	1	4771	
UA5	X	000000CC	8	4781	
UA6	X	000000EC	8	4787	
UA7	F	00000118	8	4798	
UA8	X	00000180	32	4827	
ZBRKADDR	A	00000110	8	4797	
ZEMONCNT	F	0000010C	4	4796	
ZEMONCTR	A	00000100	8	4794	
ZEMONSIZ	F	00000108	4	4795	
ZEXTNPSW	X	000001B0	16	4830	
ZEXTOPSW	X	00000130	16	4822	
ZIONPSW	X	000001F0	16	4834	
ZIOOPSW	X	00000170	16	4826	
ZMCKNPSW	X	000001E0	16	4833	
ZMCKOPSW	X	00000160	16	4825	
ZMKFAILA	F	000000F8	8	4789	
ZMONCODE	F	000000B0	8	4764	
ZPGMNPSW	X	000001D0	16	4832	
ZPGMOPSW	X	00000150	16	4824	
ZPGMTRX	F	000000A8	8	4763	
ZRSTNPSW	X	000001A0	16	4829	
ZRSTOPSW	X	00000120	16	4821	
ZSASDISP	U	000011C0	1	4835	
ZSVCNPSW	X	000001C0	16	4831	
ZSVCOPSW	X	00000140	16	4823	
=F'0'	F	000005E8	4	3671	3636

```
 MACRO     DEFN  REFERENCES

ANTR        122
APROB       254
ARCHIND     414   3444
ARCHLVL     555   3443
ASAIPL      681   3504
ASALOAD     761   3487
ASAREA      816   4681
ASAZAREA   1001
CPUWAIT    1084
DSECTS     1410   4678
DWAIT      1613   3656   3661
DWAITEND   1670   3655
ENADEV     1678
ESA390     1778
IOCB       1789
IOCBDS     1965
IOFMT      1999
IOINIT     2337
IOTRFR     2378
ORB        2426
POINTER    2615
PSWFMT     2643
RAWAIT     2777
RAWIO      2873
SIGCPU     3031
SMMGR      3089
SMMGRB     3189
TRAP128    3238
TRAP64     3215   3489   3492
TRAPS      3251
ZARCH      3325
ZEROH      3337
ZEROL      3365
ZEROLH     3393
ZEROLL     3416
```

```
   DESC      SYMBOL     SIZE        POS           ADDR

Entry: 0

Image       IMAGE      933710  00000-E3F4D  00000-E3F4D
  Region    CODE       933710  00000-E3F4D  00000-E3F4D
    CSECT   TRTE1TST   933710  00000-E3F4D  00000-E3F4D
```

```
   STMT                     FILE NAME

1     /devstor/dev/satk/samples/tests/TRTE-01-basic.asm
2     /home/tn529/dev/satk/srcasm/satk.mac


** NO ERRORS FOUND **
```