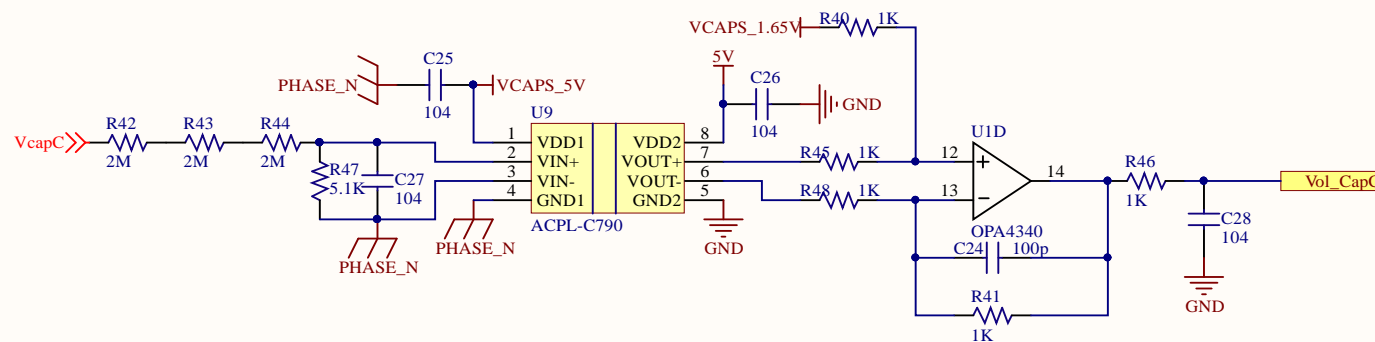
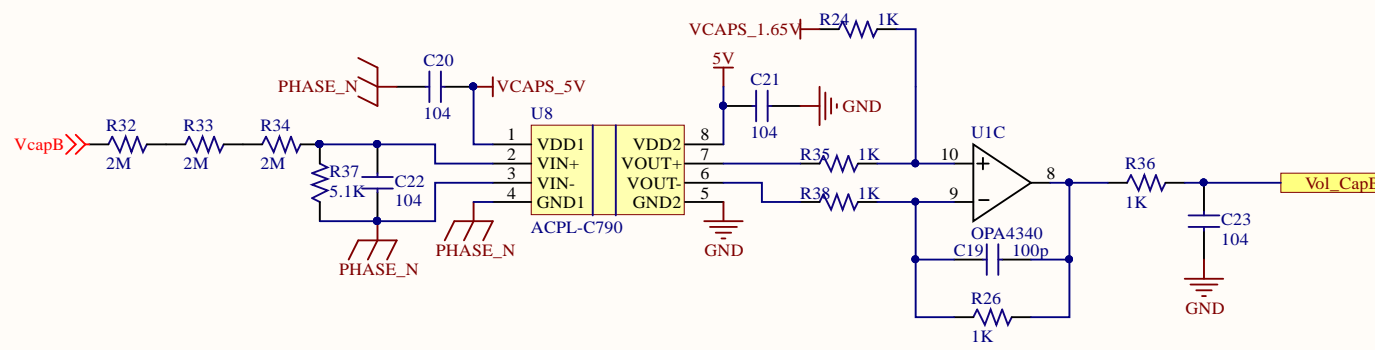
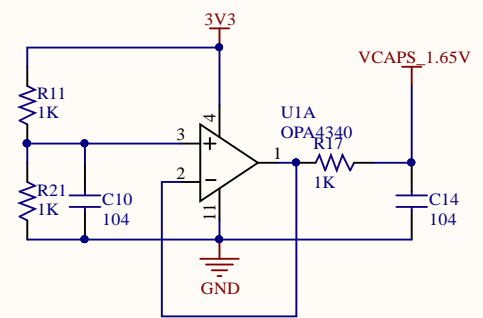
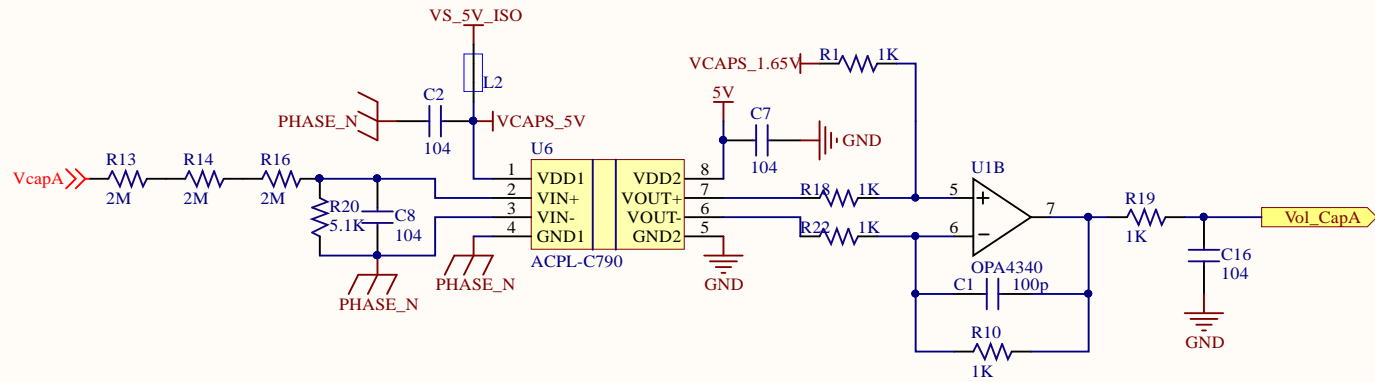
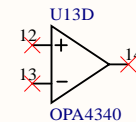
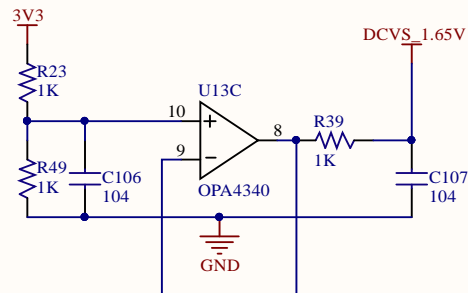
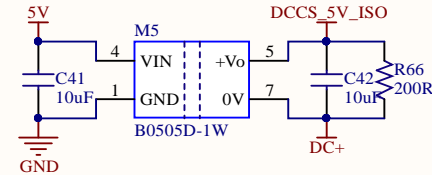
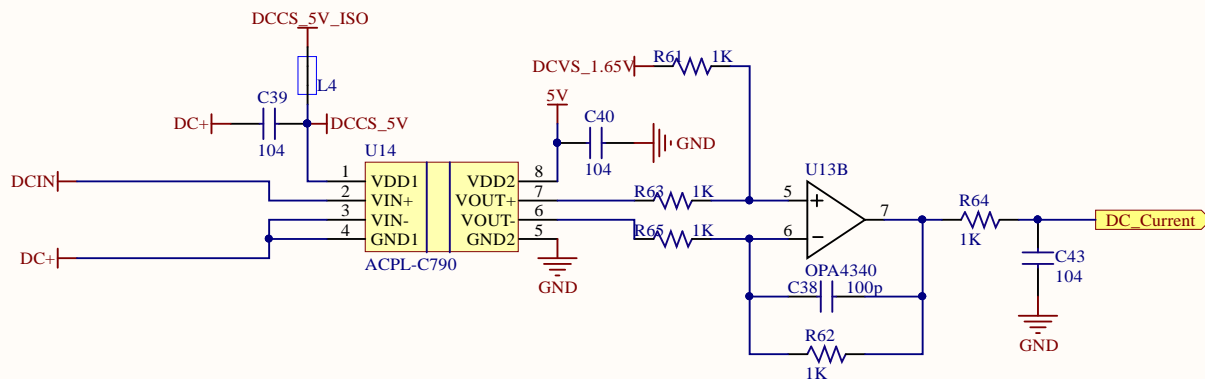
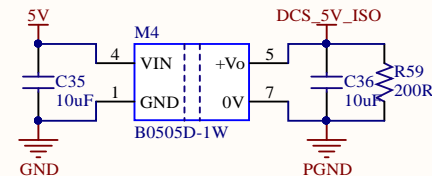
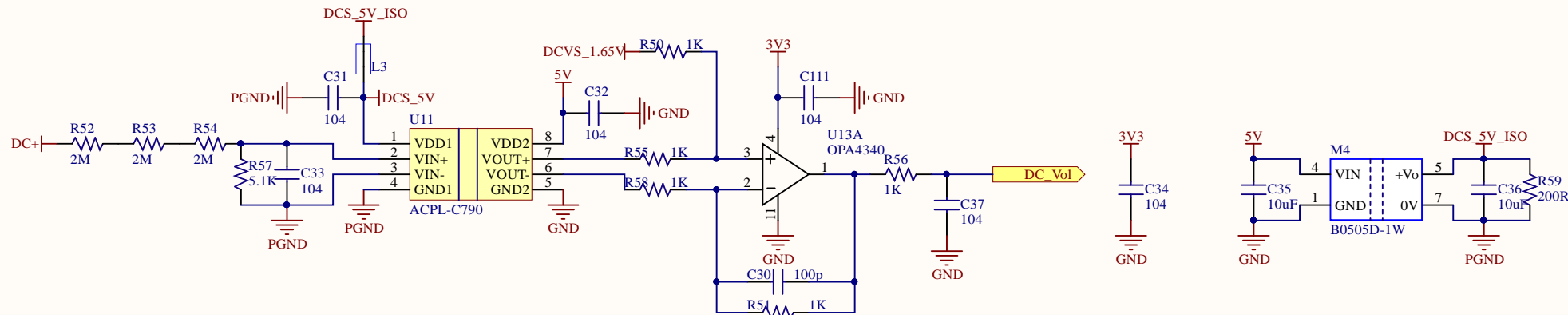


Title <i>Power and A</i>		
Size A4	Number	Revision <i>1.0</i>
Date:	2018/3/22	Sheet <i>3</i> of <i>11</i>
File:	C:\Users\...\TPVSI_Power&DA.SchDoc Drawn By: <i>Chuck Wang</i>	



Title		
Size A4	Number	Revision
Date: 2018/3/22	Sheet 1 of 1	
File:	C:\Users\... \TPVSI_Capacitor_Voltage_Sensing_SchDoc	



Title		
Size	Number	Revision
A4		
Date:	2018/3/22	Sheet of
File:	C:\Users\...TPVSI_DC_Voltage_Sensing	Drawn By:

1	2	3	4	
<div><div></div><div></div><div></div><div></div><div></div><div></div></div>				
A				A
B				B
C				C
D				D
1	2	3	4	

Title

Size

A4

Number

Revision

Date:

2018/3/22

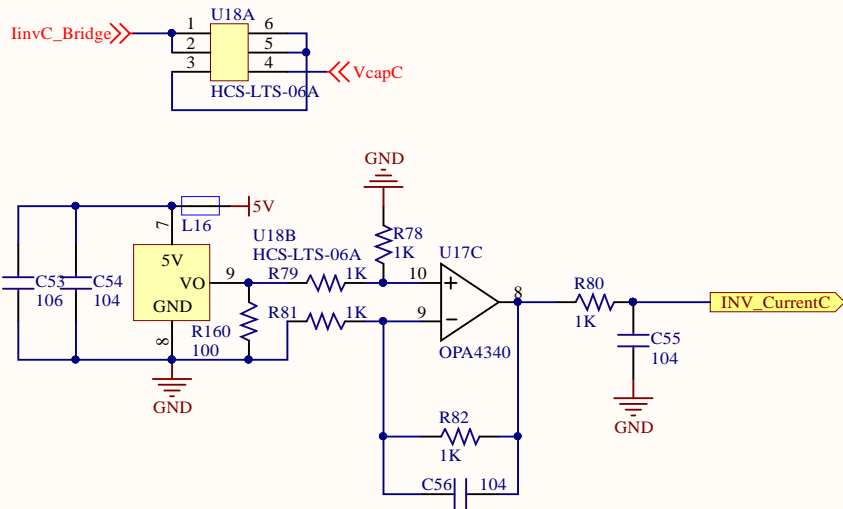
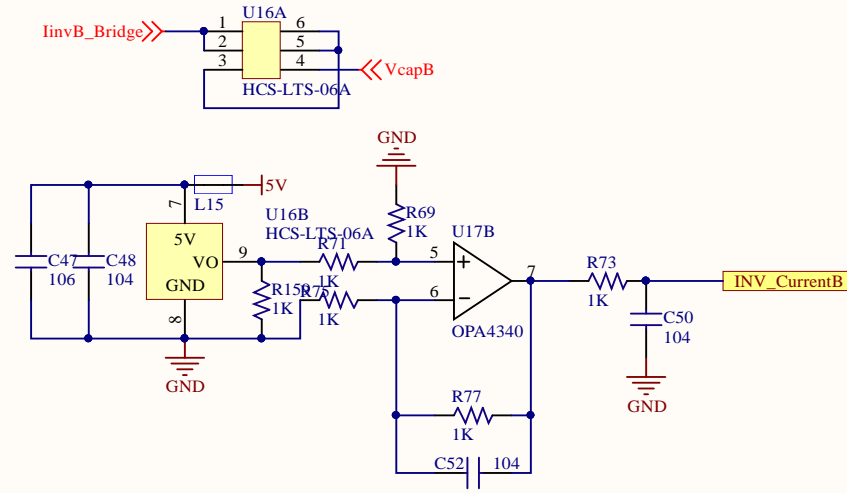
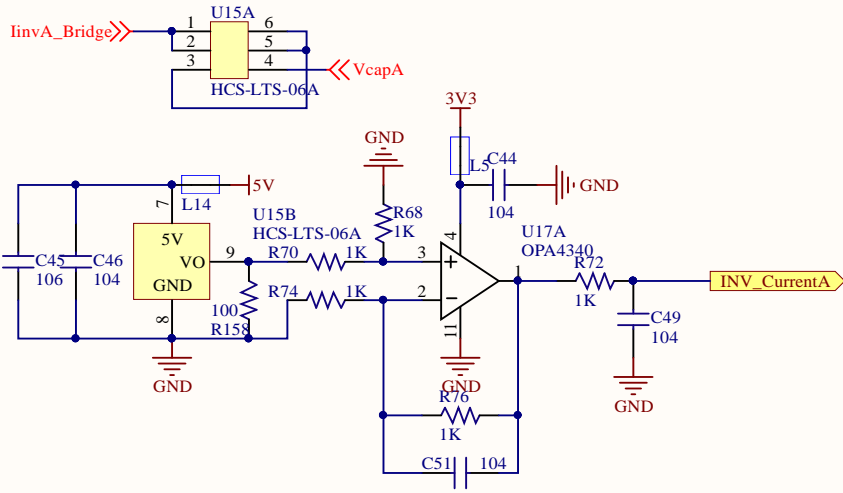
Sheet

of

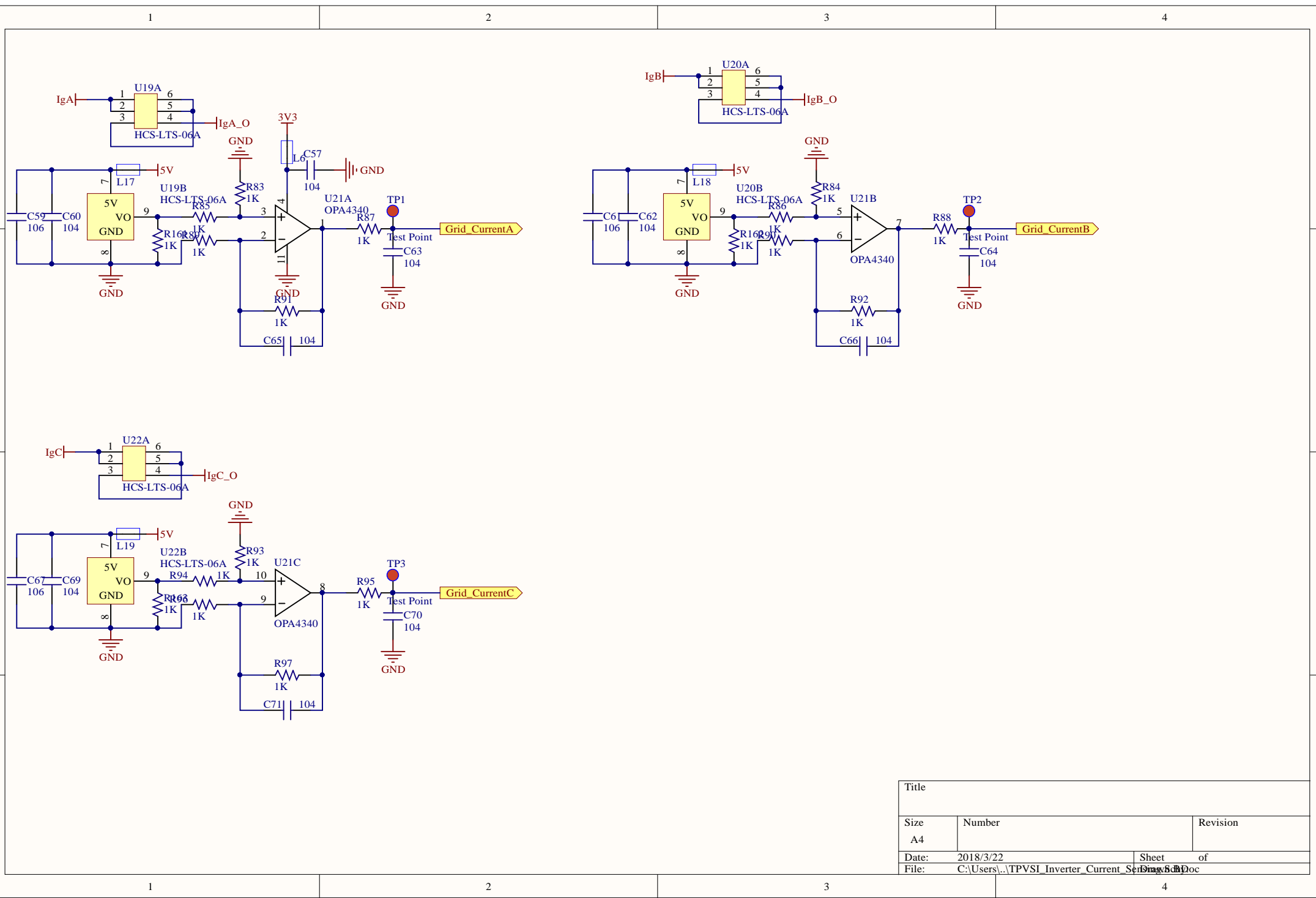
File:

C:\Users\...\TPVSI_Hardware.SchDoc

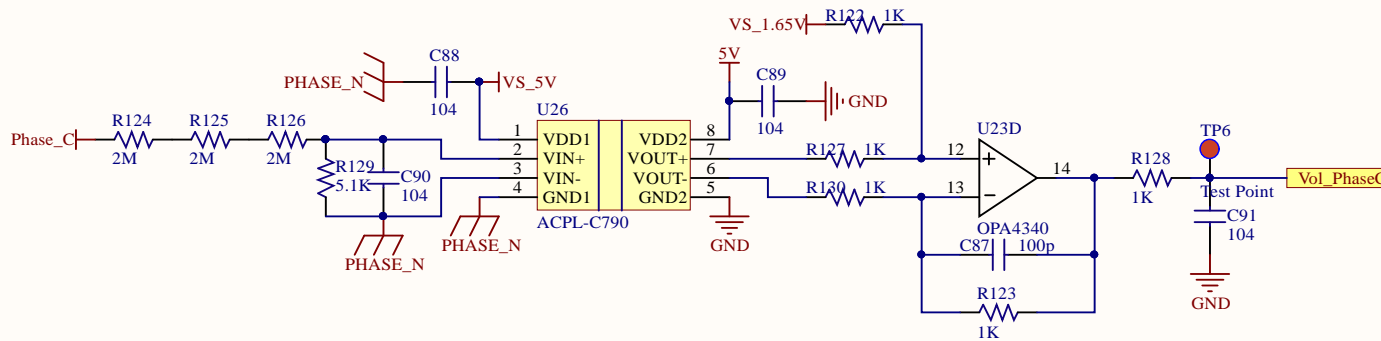
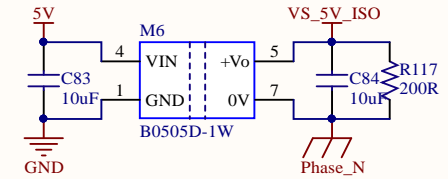
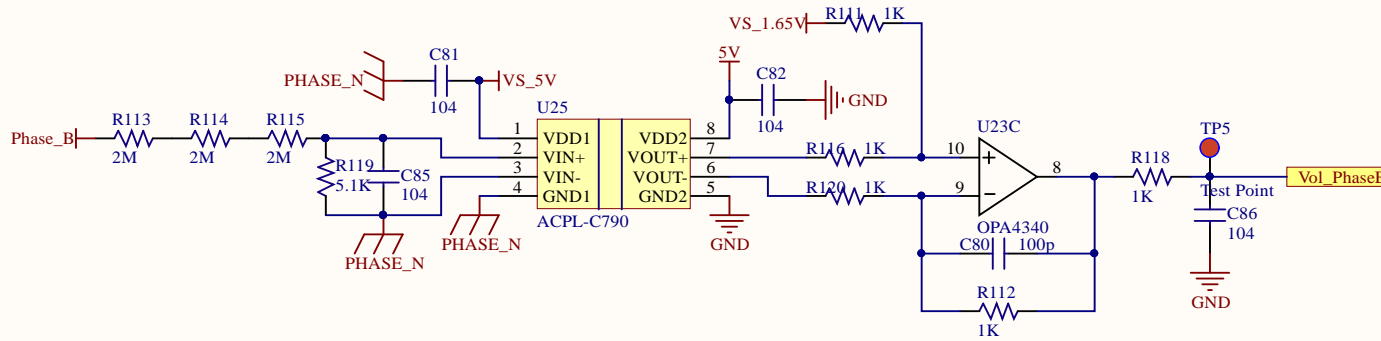
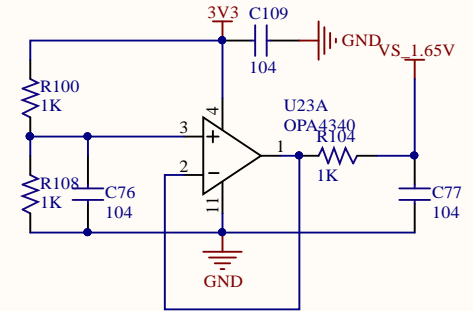
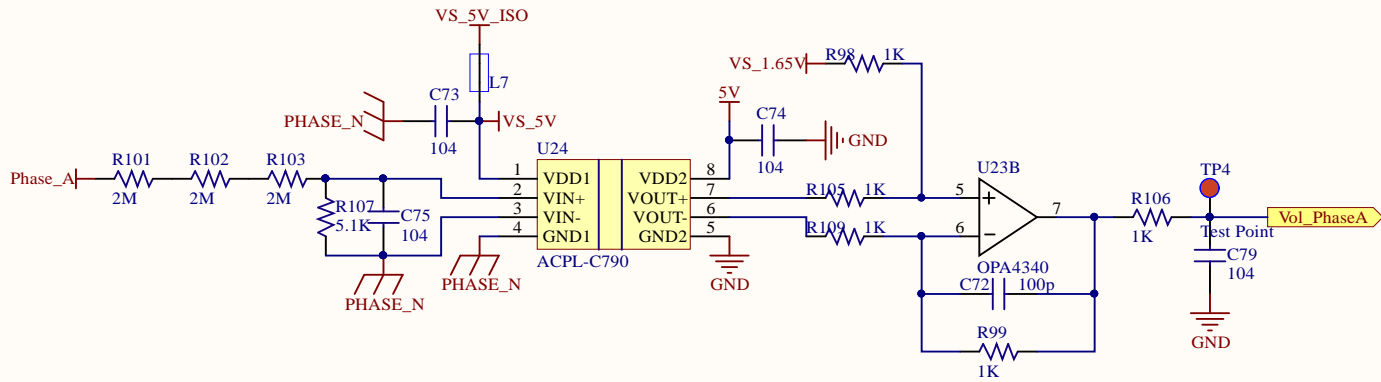
Drawn By:



Title		
Size A4	Number	Revision
Date:	2018/3/22	Sheet of
File:	C:\Users\...\TPVSI_Inductor_Current_Sensing_SchDoc	



Title		
Size	Number	Revision
A4		
Date:	2018/3/22	Sheet 1 of 1
File:	C:\Users\...\TPVSI Inverter_Current_Sensing_SchDoc	



Title		
Size A4	Number	Revision
Date:	2018/3/22	Sheet 1 of 1
File:	C:\Users\...\TPVSI Inverter_Voltage_Sensing_SchDoc	

1

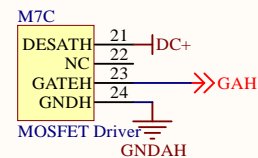
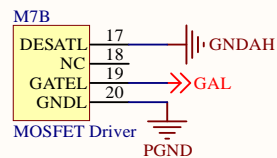
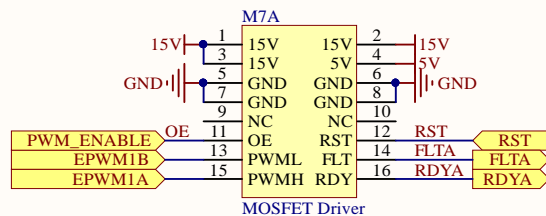
2

3

4

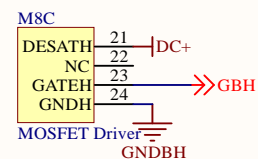
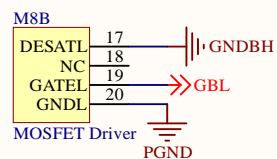
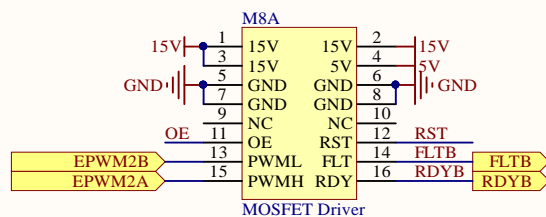
A

A



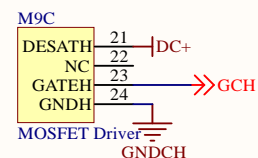
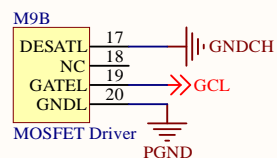
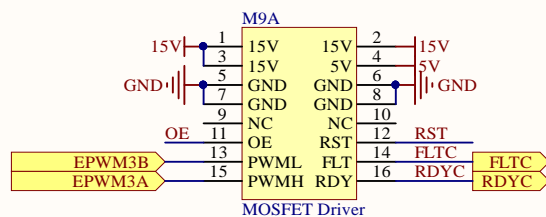
B

B



C

C



D

D

Title		
Size	Number	Revision
A4		
Date:	2018/3/22	Sheet of
File:	C:\Users\...\TPVSI_MOSFET_Driver.SchDrawn By:	

1

2

3

4

