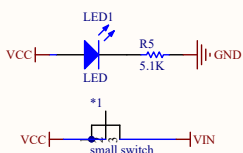
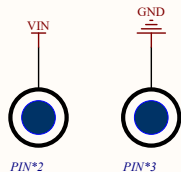
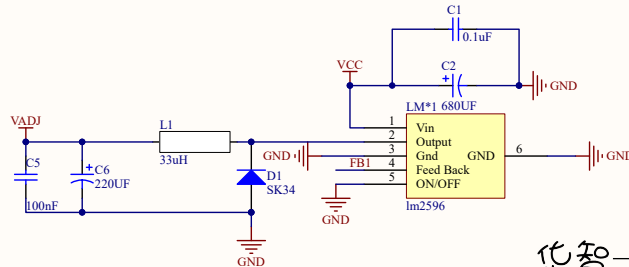


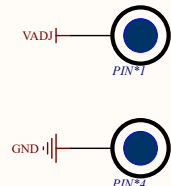
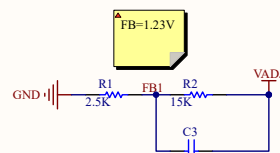
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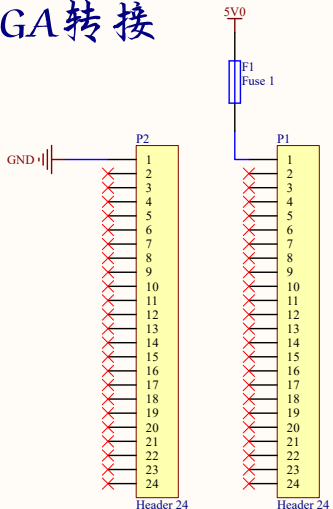
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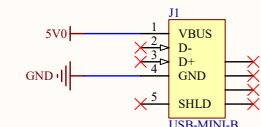
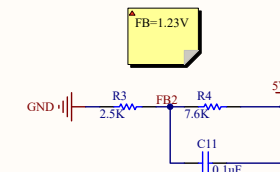
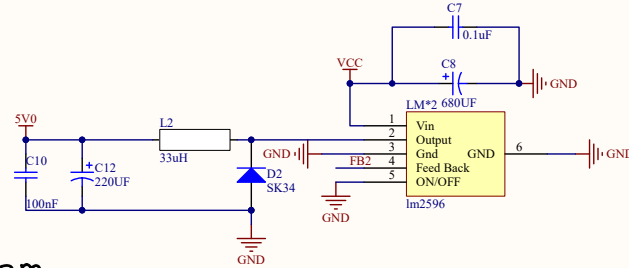


FPGA转接



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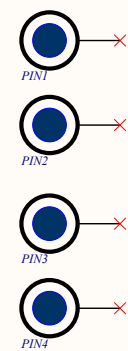
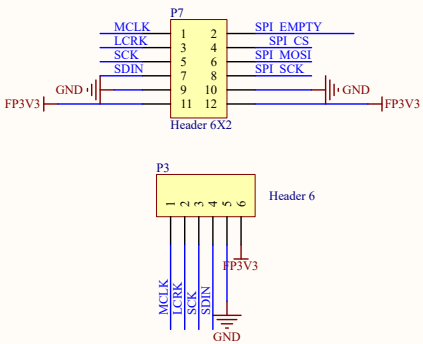
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安装孔

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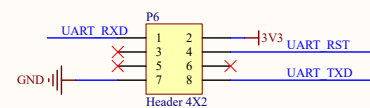
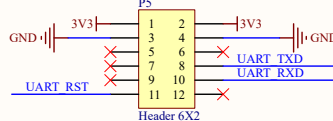
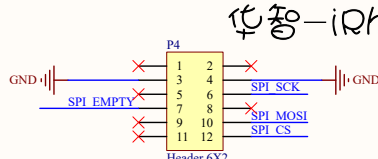
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